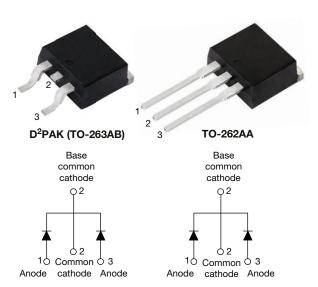
WWW.vishay.com

Vishay Semiconductors

High Performance Schottky Rectifier, 2 x 15 A



VS-32CTQ...S-M3

VS-32CTQ...-1-M3

PRIMARY CHARACTERISTICS								
I _{F(AV)}	2 x 15 A							
V _R	25 V, 30 V							
V _F at I _F	0.40 V							
I _{RM} typ.	97 mA at 125°C							
T _J max.	150 °C							
E _{AS}	13 mJ							
Package	D ² PAK (TO-263AB), TO-262AA							
Circuit configuration	Common cathode							

FEATURES

- 150 °C T_J operation
- Low forward voltage drop
- High frequency operation
- High purity, high temperature epoxy encapsulation for enhanced mechanical strength and moisture resistance
- Guard ring for enhanced ruggedness and long term reliability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Designed and qualified according to JEDEC®-JESD 47
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION

The VS-32CTQ... Schottky rectifier series has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 150 °C junction temperature. Typical applications are in switching power supplies, converters, freewheeling diodes, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS									
SYMBOL	L CHARACTERISTICS VALUES								
I _{F(AV)}	Rectangular waveform	30	A						
V _{RRM}		25, 30	V						
I _{FSM}	t _p = 5 μs sine	900	A						
V _F	15 A _{pk} , T _J = 125 °C	0.40	V						
TJ	Range	-55 to +150	°C						

VOLTAGE RATINGS								
PARAMETER	SYMBOL	VS-32CTQ025S-M3 VS-32CTQ025-1-M3	VS-32CTQ030S-M3 VS-32CTQ030-1-M3	UNITS				
Maximum DC reverse voltage	V _R	25	30	V				
Maximum working peak reverse voltage	V _{RWM}	25	30	v				

 Revision: 07-May-2020
 1
 Document Number: 94936

 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com
 THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



COMPLIANT HALOGEN



www.vishay.com

Vishay Semiconductors

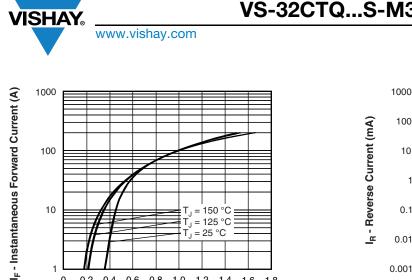
ABSOLUTE MAXIMUM RATINGS									
PARAMETER	SYMBOL	TEST COND	ITIONS	VALUES	UNITS				
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at T_{C} = 115 °C	30						
Maximum peak one cycle non-repetitive		5 µs sine or 3 µs rect. pulse	Following any rated load	900	А				
surge current See fig. 7	I _{FSM}	10 ms sine or 6 ms rect. pulse	condition and with rated V _{RRM} applied	250					
Non-repetitive avalanche energy	E _{AS}	$T_J = 25 \ ^{\circ}C, \ I_{AS} = 1.20 \ A, \ L = 11$	T _J = 25 °C, I _{AS} = 1.20 A, L = 11.10 mH						
Repetitive avalanche current	I _{AR}	Current decaying linearly to zer Frequency limited by T _J maxim	3	А					

ELECTRICAL SPECIFICATIONS									
PARAMETER	SYMBOL	TEST C	ONDITIONS	VALUES	UNITS				
		15 A	T.I = 25 °C	0.49					
Maximum forward voltage drop	V _{FM} ⁽¹⁾	30 A	1j=25 C	0.58	V				
See fig. 1	VFM (*)	15 A		0.40	V				
		30 A	1j=125 0	0.53					
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	$V_{\rm c} = {\rm Retad} V_{\rm c}$	1.75	m۸				
Maximum reverse leakage current	'RM \''	T _J = 125 °C	V _R = Rated V _R	145	mA				
Typical reverse leakage current	I _{RM} ⁽¹⁾	T _J = 125 °C	V _R = Rated V _R	97	mA				
Threshold voltage	V _{F(TO)}		·	0.233	V				
Forward slope resistance	r _t	ij = ij maximum	$T_J = T_J$ maximum						
Maximum junction capacitance per leg	CT	V _R = 5 V _{DC} (test signal rar	1300	pF					
Typical series inductance per leg	L _S	Measured lead to lead 5 r	8.0	nH					
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs				

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 %

THERMAL - MECHANICAL SPECIFICATIONS									
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS				
Maximum junction and storage temperature range		T _J , T _{Stg}		-55 to +150	°C				
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation See fig. 4	3.25	°C/W				
Typical thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.50	0/10				
Approximate weight				2	g				
				0.07	oz.				
Mounting torgue	minimum			6 (5)	kgf ⋅ cm				
Mounting torque	maximum			12 (10)	(lbf · in)				
					Case style D ² PAK (TO-263AB)	32CTC	Q025S		
Maultine, destine			Case sivie D-PAK (10-263AB)	32CTC	2030S				
Marking device			Case at the TO 2624 A	32CTC	025-1				
			Case style TO-262AA	32CTC	030-1				



1.8

1.0 1.2 1.4 1.6 V_{FM} - Forward Voltage Drop (V)

0 0.2 0.4 0.6 0.8

Fig. 1 - Maximum Forward Voltage Drop Characteristics



Vishay Semiconductors

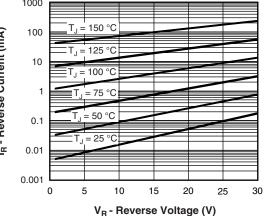


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

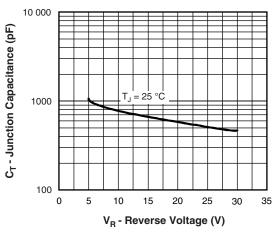


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

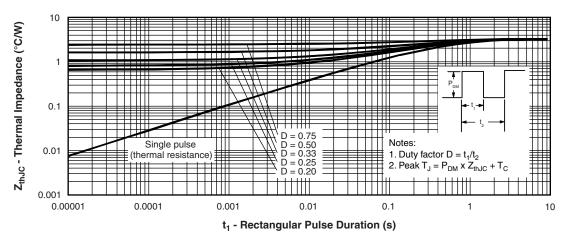
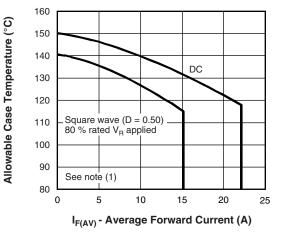


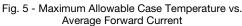
Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

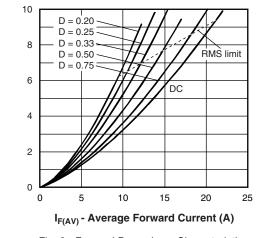
Revision: 07-May-2020 Document Number: 94936 3 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT www.vishay.com/doc?91000



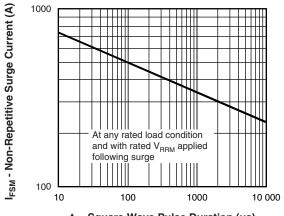
Vishay Semiconductors











Average Power Loss (W)

t_p - Square Wave Pulse Duration (μs)

Fig. 7 - Maximum Non-Repetitive Surge Current

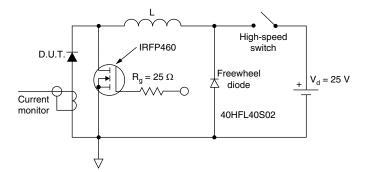


Fig. 8 - Unclamped Inductive Test Circuit

Note

- ⁽¹⁾ Formula used: $T_C = T_J (Pd + Pd_{REV}) \times R_{thJC}$;
- Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 6); Pd_{BEV} = inverse power loss = $V_{B1} \times I_B$ (1 - D); I_B at V_{B1} = 80 % rated V_B

Revision: 07-May-2020

4

For technical questions within your region: <u>DiodesAmericas@vishay.com</u>, <u>DiodesAsia@vishay.com</u>, <u>DiodesEurope@vishay.com</u> THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFIC DISCLAIMERS, SET FORTH AT <u>www.vishay.com/doc?91000</u>



Vishay Semiconductors

ORDERING INFORMATION TABLE

www.vishay.com

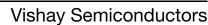
VISHAY

Device code	VS-	32	С	т	Q	030	S	TRL	-M3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
		\bigcirc	\bigcirc	\bigcirc	\bigcirc	\bigcirc	0	\bigcirc	\bigcirc
	<u> </u>		nay Sen		•	oduct			
	2 -	Cur	rent rati	ng (30 A	4)				
	3 -	Circ	cuit conf	iguratior	n: C = c	ommon	cathod	е	
	4 -	- T =	TO-220)					
	5 -	Sch	ottky "C	" series		05 - 05			
	6 -	Vol	tage rati	ngs —		25 = 25 30 = 30			
	7 -	• S	= D ² PA	К (ТО-2					
		• -1	= TO-2	62AA					
	8 -	• N	one = tu	ıbe					
		• TI	RL = tap	e and r	eel (left	oriented	d - for D	² PAK (1	ГО-263
		• TI	RR = ta	be and r	eel (righ	nt orient	ed - for	D ² PAK	(TO-26
	9 -	-M3	= halog	jen-free	, RoHS-	complia	ant, and	l termina	ation lea

ORDERING INFORMATION									
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION						
VS-32CTQ025S-M3	50	1000	Antistatic plastic tubes						
VS-32CTQ025STRR-M3	800	800	13" diameter reel						
VS-32CTQ025STRL-M3	800	800	13" diameter reel						
VS-32CTQ025-1-M3	50	1000	Antistatic plastic tubes						
VS-32CTQ030S-M3	50	1000	Antistatic plastic tubes						
VS-32CTQ030STRR-M3	800	800	13" diameter reel						
VS-32CTQ030STRL-M3	800	800	13" diameter reel						
VS-32CTQ030-1-M3	50	1000	Antistatic plastic tubes						

LINKS TO RELATED DOCUMENTS								
Dimensions	D ² PAK (TO-263AB)	www.vishay.com/doc?96164						
	TO-262AA	www.vishay.com/doc?96165						
Part marking information	D ² PAK (TO-263AB)	www.vishay.com/doc?95444						
	TO-262AA	www.vishay.com/doc?95443						
Packaging information		www.vishay.com/doc?96424						

Outline Dimensions

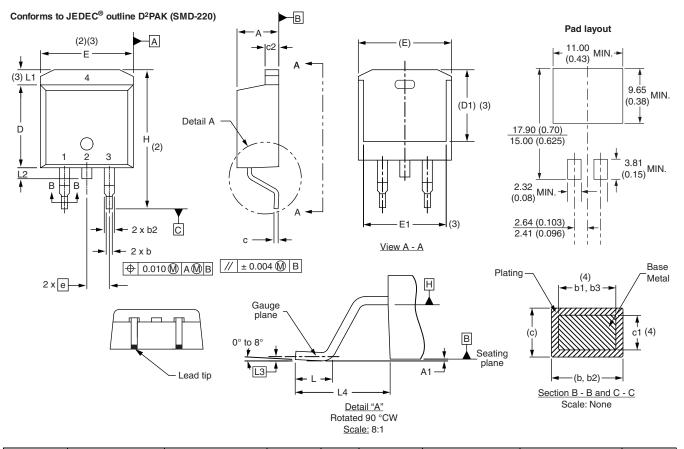


D²PAK

DIMENSIONS in millimeters and inches

www.vishay.com

SHA



SYMBOL	MILLIMETERS		INC	HES	NOTES	NOTES		MILLIM	IETERS	INC	HES	NOTES
STMBOL	MIN.	MAX.	MIN.	MAX.	NOTES		SYMBOL	MIN.	MAX.	MIN.	MAX.	NOTES
A	4.06	4.83	0.160	0.190			D1	6.86	8.00	0.270	0.315	3
A1	0.00	0.254	0.000	0.010			E	9.65	10.67	0.380	0.420	2, 3
b	0.51	0.99	0.020	0.039			E1	7.90	8.80	0.311	0.346	3
b1	0.51	0.89	0.020	0.035	4		е	2.54	BSC	0.100	BSC	
b2	1.14	1.78	0.045	0.070			Н	14.61	15.88	0.575	0.625	
b3	1.14	1.73	0.045	0.068	4		L	1.78	2.79	0.070	0.110	
С	0.38	0.74	0.015	0.029			L1	-	1.65	-	0.066	3
c1	0.38	0.58	0.015	0.023	4		L2	1.27	1.78	0.050	0.070	
c2	1.14	1.65	0.045	0.065			L3	0.25	BSC	0.010	BSC	
D	8.51	9.65	0.335	0.380	2		L4	4.78	5.28	0.188	0.208	

Notes

⁽¹⁾ Dimensioning and tolerancing per ASME Y14.5 M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

⁽⁵⁾ Datum A and B to be determined at datum plane H

⁽⁶⁾ Controlling dimension: inch

⁽⁷⁾ Outline conforms to JEDEC[®] outline TO-263AB

Revision: 08-Jul-15

1

Document Number: 95046

For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFI Downloaded From Oneyac.com

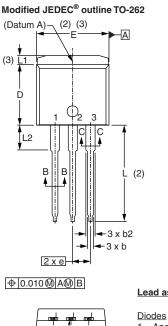
Outline Dimensions



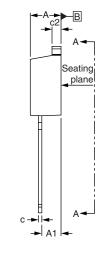
Vishay Semiconductors

TO-262

DIMENSIONS in millimeters and inches

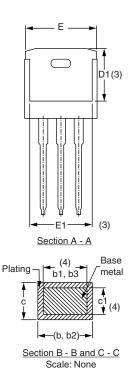


Lead tip -



Lead assignments

1. - Anode (two die)/open (one die) 2., 4. - Cathode 3. - Anode



MILLIMETERS INCHES SYMBOL NOTES MIN. MAX. MIN. MAX. А 4.06 4.83 0.160 0.190 2.03 A1 3.02 0.080 0.119 b 0.51 0.99 0.020 0.039 b1 0.51 0.89 0.020 0.035 4 b2 1.14 1.78 0.045 0.070 1.14 1.73 0.045 0.068 4 b3 0.38 0.74 0.015 0.029 С 0.38 0.58 0.015 0.023 4 c1 1.14 1.65 0.045 0.065 c2 D 8.51 9.65 0.335 0.380 2 D1 6.86 8.00 0.270 0.315 3 Е 9.65 10.67 0.380 0.420 2, 3 E1 7.90 8.80 0.311 0.346 3 0.100 BSC 2.54 BSC е L 13.46 14.10 0.530 0.555 L1 _ 1.65 0.065 3 _ 3.36 0.132 0.146 L2 3.71

Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Dimension D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outmost extremes of the plastic body

⁽³⁾ Thermal pad contour optional within dimension E, L1, D1 and E1

⁽⁴⁾ Dimension b1 and c1 apply to base metal only

(5) Controlling dimension: inches

(6) Outline conform to JEDEC TO-262 except A1 (maximum), b (minimum) , D1 (minimum) and L2 where dimensions derived the actual package outline

Revision: 11-Jul-2019

Document Number: 95419

1 For technical questions within your region: DiodesAmericas@vishay.com, DiodesAsia@vishay.com, DiodesEurope@vishay.com THIS DOCUMENT IS SUBJECT TO CHANGE WITHOUT NOTICE. THE PRODUCTS DESCRIBED HEREIN AND THIS DOCUMENT ARE SUBJECT TO SPECIFI Downloaded From Oneyac.com w.vishay.com/doc?91000



Vishay

Disclaimer

ALL PRODUCT, PRODUCT SPECIFICATIONS AND DATA ARE SUBJECT TO CHANGE WITHOUT NOTICE TO IMPROVE RELIABILITY, FUNCTION OR DESIGN OR OTHERWISE.

Vishay Intertechnology, Inc., its affiliates, agents, and employees, and all persons acting on its or their behalf (collectively, "Vishay"), disclaim any and all liability for any errors, inaccuracies or incompleteness contained in any datasheet or in any other disclosure relating to any product.

Vishay makes no warranty, representation or guarantee regarding the suitability of the products for any particular purpose or the continuing production of any product. To the maximum extent permitted by applicable law, Vishay disclaims (i) any and all liability arising out of the application or use of any product, (ii) any and all liability, including without limitation special, consequential or incidental damages, and (iii) any and all implied warranties, including warranties of fitness for particular purpose, non-infringement and merchantability.

Statements regarding the suitability of products for certain types of applications are based on Vishay's knowledge of typical requirements that are often placed on Vishay products in generic applications. Such statements are not binding statements about the suitability of products for a particular application. It is the customer's responsibility to validate that a particular product with the properties described in the product specification is suitable for use in a particular application. Parameters provided in datasheets and / or specifications may vary in different applications and performance may vary over time. All operating parameters, including typical parameters, must be validated for each customer application by the customer's technical experts. Product specifications do not expand or otherwise modify Vishay's terms and conditions of purchase, including but not limited to the warranty expressed therein.

Except as expressly indicated in writing, Vishay products are not designed for use in medical, life-saving, or life-sustaining applications or for any other application in which the failure of the Vishay product could result in personal injury or death. Customers using or selling Vishay products not expressly indicated for use in such applications do so at their own risk. Please contact authorized Vishay personnel to obtain written terms and conditions regarding products designed for such applications.

No license, express or implied, by estoppel or otherwise, to any intellectual property rights is granted by this document or by any conduct of Vishay. Product names and markings noted herein may be trademarks of their respective owners.



单击下面可查看定价,库存,交付和生命周期等信息

>>Vishay(威世)