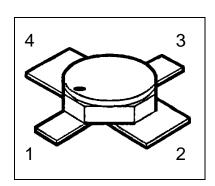


HiRel NPN Silicon Germanium RF Transistor

- HiRel Discrete and Microwave Semiconductor
- High gain ultra low noise RF transistor
- Outstanding noise figure F = 0.7 dB at 1.8 GHz
 Outstanding noise figure F = 1.0 dB at 6 GHz
- Hermetically sealed microwave package
- CONTROL Space Qualified

 ESCO Detail Space No.: 5611/0

ESCC Detail Spec. No.: 5611/011



ESD: Electrostatic discharge sensitive device, observe handling precautions!

Туре	Marking	Pin Configuration				Package
		1	2	3	4	
BFY740B-01	-	С	Е	В	E	Micro-X

Maximum Ratings

Parameter	Symbol	Values	Unit V	
Collector-emitter voltage T _a > 0 °C	V_{CEO}	4.0		
T _a ≤ 0 °C		3.5	V	
Collector-base voltage	V_{CBO}	13	V	
Emitter-base voltage	V_{EBO}	1.2	V	
Collector current 1)	I _C	30	mA	
Base current	I _B	3	mA	
Junction temperature	T _j	175	°C	
Operating temperature range	T _{op}	-65+175	°C	
Storage temperature range	T _{stg}	-65+175	°C	
Thermal Resistance	·		<u>.</u>	
Junction-soldering point 2)	R _{th JS}	400	K/W	

Notes.:

- 1) For $T_S \le 125$ °C. For $T_S > 125$ °C derating is required.
- 2) T_S is measured on the emitter lead at the soldering point to the pcb.



Electrical Characteristics

at T_A=25°C; unless otherwise specified

Parameter	Symbol	Values			Unit
		min.	typ.	max.	
DC Characteristics				•	
Collector-base cutoff current	I _{CBO}	-	-	10	μΑ
$V_{CB} = 5 \text{ V}, I_E = 0$					
Collector-emitter cutoff current 1)	I _{CEX}	-	-	200	μΑ
$V_{CE}=4.0~V,~I_{B}=0.1~\mu A$					
Emitter-base cuttoff current	I _{EBO}	-	-	5	μΑ
$V_{EB} = 1.2 \text{ V}, I_{C} = 0$					
DC current gain	h _{FE}	130	270	480	-
$I_{C} = 20 \text{ mA}, V_{CE} = 3 \text{ V}$					
AC Characteristics					
Collector-base capacitance	ССВ	-	0.07	-	pF
$V_{CB} = 2 \text{ V}, V_{BE} = \text{vbe} = 0, f = 1 \text{ MHz}$					
Collector-emitter capacitance	C_{CE}	-	0.45	-	pF
$V_{CE} = 2 \text{ V}, V_{BE} = \text{vbe} = 0, f = 1 \text{ MHz}$					
Emitter-base capacitance	C _{EB}	-	0.6	-	pF
$V_{EB} = 0.5V, V_{CB} = vcb = 0, f = 1 MHz$					
Noise Figure ($Z_S = Z_{sopt}$)	F				dB
$I_{C} = 8 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$		-	0.7	-	
$I_C = 8 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$	10 12	-	1.0	-	dD.
Insertion power gain ($Z_S = Z_L = 50 \Omega$)	$ S_{21e} ^2$		24		dB
$I_C = 20 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$ $I_C = 20 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$		-	14	-	
Power gain ($Z_S = Z_{Sopt}$, $Z_L = Z_{Lopt}$)	G _{ms} ²⁾				dB
$I_C = 20 \text{ mA}, V_{CE} = 3 \text{ V}, f = 1.8 \text{ GHz}$		-	26.5	-	
Power gain $(Z_S = Z_{Sopt}, Z_L = Z_{Lopt})$	G _{ma} ²⁾				dB
$I_C = 20 \text{ mA}, V_{CE} = 3 \text{ V}, f = 6.0 \text{ GHz}$		-	18.3	-	

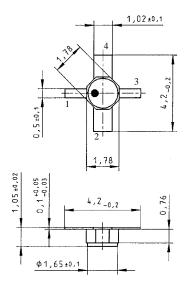
Notes.:
1) This Test assures V(BR)CE0 > 4.0V

2)
$$G_{ma} = \left| \frac{S21}{S12} \right| (k - \sqrt{k^2 - 1}), \quad G_{ms} = \left| \frac{S21}{S12} \right|$$

IFAG PMM RPD D HIR 2 of 3 V1, November 2012



Micro-X Package



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