

Applications Note:SY58761

Single Stage Boost PFC LED Driver Dimmable, High PF and Low BOM Cost

General Description

SY58761 is a single-stage Boost PFC driver for LED lighting applications. Good compatibility is achieved with Leading/Trailing edge dimmer and high PF is achieved without any dimmer.

SY58761 drives the converter in Quasi-Resonant mode to achieve high efficiency. Reliable Open LED protections are integrated.

SY58761 integrates high voltage power FET inside to save driver space further.

SY58761 is available in SOT23-5 package.

Ordering Information

Features

• Compatible with Leading Edge/Trailing Edge Dimmer

HGCEP

- High PF without Any Dimmer
- 350V MOSFET Integrated
- Quasi-Resonant Operation
- Reliable Open LED Protection
- Thermal Fold Back
- Low BOM Cost
- Compact Package: SOT23-

Applications



Figure 1. Typical application





Pinout (top view)



(SOT23-5)	\sim	K
Top Mark: Zrxyz (device code: Zr, x=year code, y=week code, z= lot number code)	\sim	/

Pin	PIN Number	Pin Description
Name		
VCC	1	Bias supply pin.
GND	2	Ground pin.
OVP	3	Voltage sense pin. Connect to a resistor divider of inductor or auxiliary winding to sense output voltage.
0.11		$V_{OVP} = K \times V_{OVP,REF}$, where K is the OVP resistor ratio coefficient.
	4	Current sense pin, connect a cap and sense res to GND pin.
CS		$R_{CS} = \frac{V_{REF}}{2I_0}$
Drain	5	Internal MOSFET drain node.

Block Diagram





Absolute Maximum Ratings (Note 1)

VCC	
VCC	4mA
S, OVP	
rain	350V
laximum Junction Temperature	165°C
ead Temperature (Soldering, 10 sec.)	260°C
torage Temperature Range	65°C to 165°C
	\sim
	A i
	\sim
	$\mathbf{X}^{(1)}$
$\langle O^* \rangle$	
$\langle O \rangle$	
CN'	



Electrical Characteristics

 $(V_{VCC} = 12V, T_A = 25^{\circ}C \text{ unless otherwise specified})$

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Power Supply Section						
VCC Turn-on Threshold	V_{VCC_ON}			14		V
VCC Turn-off Threshold	V_{VCC_OFF}			7		V
VCC Shunt Voltage	V_{VCC_Shunt}			14.5		V
Start up Current	I _{ST}			40		μA
Quiescent Current	IQ			250		μA
CS pin Section						
Current Reference	V _{REF}			216		mV
CS Limit	V_{CS_MAX}			1.65		V
OVP Pin Section						
OVP Voltage Reference	V _{OVP,REF}			1.2		V
Driver Section						
Min ON Time	t _{ON_MIN}			500		ns
Max ON Time	ton_max		0	10		μs
Min OFF Time	t _{OFF_MIN}		22	1.5		μs
Max OFF Time	t_{OFF_MAX}	\diamond		250		μs
Integrated MOSFET Section						
BV of HV MOSFET	$V_{\rm BV}$		350			V
HV MOS Drain Source Resistance	R_{DSON_H}	2		4.2		Ω
Thermal Section						
Thermal Fold Back Temperature	Тғв			160		°C
Thermal Shut Down Temperature	T_{SD}			T_{FB} +5		°C

Note 1: Stresses beyond the "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

Note 2: θ JA is measured in the natural convection at TA = 25°C on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard. Test condition: Device mounted on 2" x 2" FR-4 substrate PCB, 2oz copper, with minimum recommended pad on top layer and thermal vias to bottom layer ground plane Note 3: The device is not guaranteed to function outside its operating conditions.





Operation

SY58761 is a single stage Boost PFC regulator targeting at LED lighting applications.

It is mainly used in mains dimming application and has good compatibility with Leading/Trailing edge dimmer.

In order to reduce the switching losses and improve EMI performance, Quasi-Resonant switching mode is applied, which means to turn on the power MOSFET at the valley of drain voltage.

It also provides reliable open LED protections and over temperature protection.

The IC is available with SOT-23 package.

Applications Information

<u>Start up</u>

After AC supply is powered on, the capacitor C_{VCC} between VCC and GND pin is charged up by output voltage (peak value of input voltage at the first of power on). Once V_{VCC} rises up to V_{VCC_ON} , the internal blocks start to work and V_{CS} is pre-charged to certain value.

The whole start up procedure is divided into three sections as shown below. t_{ST1} is the C_{VCC} charged up time. t_{ST2} is the time V_{CS} is charged up to certain value. t_{ST3} is the time IC works at steady state. Usually t_{ST2} is much smaller than t_{ST1} .

If bias supply has more power than IC consumption, V_{VCC} is greater than V_{VCC_Shunt} , and then a shunt current starts to work.



Fig.3 Start up

The start up component R_{ST} and C_{VCC} are designed as below:

(a) Set start-up resistor R_{ST} , make sure that the operation current is enough through R_{ST} . The worst case occurs at minimum input voltage, because after start up, the bias supply current is from V_{OUT} which is higher than peak value of input voltage.

AN SY58761

$$R_{ST} < \frac{\sqrt{2}V_{AC,MIN}}{I_O}$$

Where $V_{AC_{MIN}}$ is the RMS value of minimum AC input voltage, I_Q is the operation current.

(b) Select C_{VCC} to obtain an ideal start up time t_{ST} , and to make sure that the V_{VCC} > V_{VCC_OPP} in t_{ST2} . The recommended formula is as below:



Where I_{ST} is the start up current. V_{VCC_ON} is the start up voltage of internal circuit.

<u>Shut down</u>

After AC supply is powered off, the energy stored in the output capacitor is discharged. When power supply for IC is not enough, V_{VCC} drops down. Once V_{VCC} is below V_{VCC_OFF} , the IC stops working.

LED current setting

LED current is set by the resistor R_{CS} . The formula to program I_{LED} is as below:

$$_{\text{LED}} = \frac{V_{\text{REF}}}{2 \times R_{\text{CS}}}$$

Where V_{REF} is the reference voltage.

L

Open LED protection

The protection voltage V_{OVP} for open LED is set by the resistor divider shown as below,



Then V_{OVP} is set by the formula,

Silergy Corp. Confidential- Prepared for Customer Use Only



AN_SY58761

 $V_{\rm OVP} = \frac{R_{\rm OVPH} + R_{\rm OVPL}}{R_{\rm OVPL}} V_{\rm OVP, REF}$

Where V_{OVP_REF} is 1.2V. When OVP triggers, Switching stops and VCC is pulled down until V_{VCC_OFF} , then IC starts up again and works in hiccup mode.

Thermal protection

Thermal fold back is adopted in this IC. Thermal fold back curve is shown as below.

When the junction temperature rises too high, internal current reference decreases first; if the junction temperature still rises up over T_{SD} , IC will be shut down.



Power Device Design

MOSFET and Diode

Inductor (L)

When the operation condition is with maximum output voltage, the voltage stress of MOSFET and output power diode is maximized. MOSFET is integrated with 350V BV.

 $V_{\rm DS_MAX} =$

The system operates in the peak current mode. The ON time increases with the input voltage decreasing. When the ON time reaches T_{ON_MAX}, the ON time is limit by T_{ON_MAX}.

The input voltage and inductor current waveforms are shown as below, where θ_1 and θ_2 are the first time and last time that inductor current touches the limit in each half line cycle. V_{IN1} is the instantaneous value of input voltage at θ_1 and θ_2 .



In Quasi-Resonant mode, each switching period t_s consists of three parts: inductor current rising time t_1 , falling time t_2 and quasi-resonant time t_3 .



The switching frequency is designed in rated input voltage considering conducted EMI test. Once the

voltage considering conducted EMI test. Once the switching frequency f_{SW} is set, the inductance of the inductor could be calculated.

The design flow is shown as below:

(a) Preset frequency f_{SW} at peak value of rated input voltage

(b) Compute relative t_s, t₁

$$t_{\rm S} = \frac{1}{f_{\rm SW}}$$
$$t_{\rm S} \times (V_{\rm OUT} - \sqrt{2}V_{\rm AC_RMS})$$

$$t_1 = \frac{t_S \times (V_{OUT} - \sqrt{2} V_{AC_RMS})}{V_{OUT}}$$

 $t_2 = t_S - t_1$ Where V_{AC RMS} is the RMS value of rated input voltage.

(c) Compute the peak current of inductor I_{PK} .

AN_SY58761 Rev.0.9



$$V_{IN1} = \sqrt{2} V_{AC_RMS} \frac{t_1}{t_{ON,MAX}}$$
$$\theta_1 = \arcsin(\frac{V_{IN1}}{\sqrt{2}V_{AC_RMS}})$$
$$I_{PK} \approx \frac{I_{OUT} \cdot V_{OUT} \cdot \pi}{\sqrt{2}V_{AC_RMS} \cdot \cos(\theta 1) \cdot \lambda}$$

Where V_{OUT} is the rated output voltage, I_{OUT} is rated output current. $t_{ON MAX}$ is maximum conducting time. λ is a coefficient that indicate the effect of negative resonant current and boost converter efficiency, and typically value is 0.8~0.9.

(d) Design inductance L

I

$$L = \frac{\sqrt{2}V_{AC_RMS} \times t_1}{I_{DV}}$$

Inductor design (N)

Necessary parameters:				
Inductance	L			
Io program resistor	R _{CS}			
Current low limit voltage	V _{CS,MIN}			

V_{CS MIN} is 500mV, The design rules are as followed:

$$M_{\text{MAX}} = \frac{V_{\text{CS}_{\text{MAX}}} - V_{\text{CS}_{\text{MIN}}}}{P}$$

AN SY58761

$$I_{L_{RMS}MAX} = \frac{1}{\sqrt{3}} I_{L_{PK}MAX}$$

(d) Compute turn N

I_{L PK M}

$$N = \frac{L}{\Delta B \times A_{e}} \times I_{L_{PK}MAX}$$

(e) Select an appropriate wire diameter with IL RMS MAX, select appropriate wire to make sure the current density ranges from 4A/mm² to 10A/mm².

(f) If the winding area of the core and bobbin is not enough, reselect the core style, go to (a) and redesign the inductor until the ideal inductor is achieved.

Choose proper output capacitance to satisfy current ripple. Output current ripple is set to ΔI_0 , then,

$$C_{OUT} = \frac{\sqrt{\left(\frac{2I_{OUT}}{\Delta I_{O}}\right)^{2} - 1}}{4\pi \times R_{LED} \times f_{AC}}$$

Where f_{AC} is the AC supply frequency; R_{LED} is the equivalent series resistor of LED load.

(a) Select the magnetic core type, identify the effective area A_e.

(b) Preset the maximum magnetic flux ΔB For PC40, ΔB selected to be 0.3~0.33T.

Silerey

(c) Compute inductor maximum peak current $I_{L PK MAX}$ and maximum RMS current IL RMS_MAX.

7





SOT23-5 Package outline & PCB layout design



Taping & Reel Specification

1. Taping orientation for packages (SOT23-5)





单击下面可查看定价,库存,交付和生命周期等信息

>>SILERGY(矽力杰)