

### General Description

WSD2065 combines a P-Channel enhancement mode power MOSFET which is produced with high cell density and DMOS trench technology and a low forward voltage schottky diode. the tiny and thin outline saves PCB consumption.

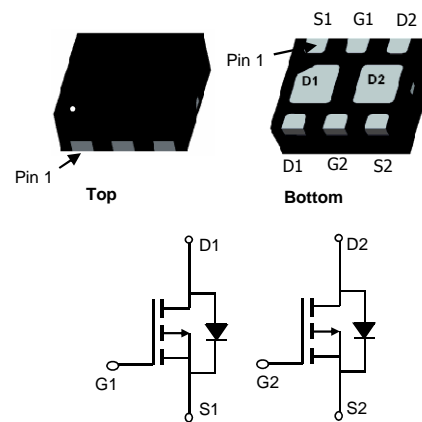
### Applications

- Bidirectional blocking switch;
- DC-DC conversion applications;
- Li-battery charging;

### Product Summary

V <sub>DSS</sub>	R <sub>DS(on)(typ.)</sub>	I <sub>D</sub>
-20V	60mΩ@-4.5V	-3.5A
	75mΩ@-2.5V	
	105mΩ@-1.8V	

### DFN2x2C-6\_EP2\_S Pin Configuration



### Absolute Maximum Ratings (T<sub>A</sub> = 25 °C Unless Otherwise Noted)

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	-20	V
V <sub>GS</sub>	Gate-Source Voltage	±8	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> = -4.5V <sup>1</sup>	-3.5	A
I <sub>DM</sub>	300μS Pulsed Drain Current, (V <sub>GS</sub> = -4.5V)	-25	A
V <sub>R</sub>	Schottky Reverse Voltage	20	V
I <sub>F</sub>	Schottky Continuous Forward Current	2	A
P <sub>D</sub>	Power Dissipation Derating above T <sub>A</sub> = 25°C (Note 2)	1.2	W
T <sub>STG</sub> , T <sub>J</sub>	Storage Temperature Range	-55 to 150	°C
R <sub>θJA</sub>	Thermal Resistance Junction-ambient <sup>1</sup>	80	°C/W
R <sub>θJC</sub>	Thermal Resistance Junction-Case <sup>1</sup>	50	°C/W

Note1: Devices mounted on FR4 PCB with minima soldering pad;

Note2: For a single chip.

**Electrical Characteristics (T<sub>J</sub>=25 °C, unless otherwise noted)**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV <sub>DSS</sub>	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =-250uA	-20	---	---	V
ΔBV <sub>DSS</sub> /ΔT <sub>J</sub>	BVDSS Temperature Coefficient	Reference to 25°C, I <sub>D</sub> =-1mA	---	-0.01	---	V/°C
R <sub>DS(ON)</sub>	Static Drain-Source On-Resistance <sup>2</sup>	V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1A	---	60	99	mΩ
		V <sub>GS</sub> =-2.5V, I <sub>D</sub> =-1A	---	75	120	
		V <sub>GS</sub> =-1.8V, I <sub>D</sub> =-1A	---	105	180	
V <sub>GS(th)</sub>	Gate Threshold Voltage	V <sub>GS</sub> =V <sub>DS</sub> , I <sub>D</sub> =-250uA	-0.5	-0.7	-1.2	V
ΔV <sub>GS(th)</sub>	V <sub>GS(th)</sub> Temperature Coefficient		---	3.13	---	mV/°C
I <sub>DSS</sub>	Drain-Source Leakage Current	V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V, T <sub>J</sub> =25°C	---	---	-1	uA
		V <sub>DS</sub> =-16V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C	---	---	-5	
I <sub>GSS</sub>	Gate-Source Leakage Current	V <sub>GS</sub> =±12V, V <sub>DS</sub> =0V	---	---	±100	nA
g <sub>fs</sub>	Forward Transconductance	V <sub>DS</sub> =-5V, I <sub>D</sub> =-1A	---	16	---	S
R <sub>g</sub>	Gate Resistance	V <sub>DS</sub> =0V, V <sub>GS</sub> =0V, f=1MHz	---	2	---	Ω
Q <sub>g</sub>	Total Gate Charge (-4.5V)	V <sub>DS</sub> =-10V, V <sub>GS</sub> =-4.5V, I <sub>D</sub> =-1A	---	5.2	---	nC
Q <sub>gs</sub>	Gate-Source Charge		---	0.7	---	
Q <sub>gd</sub>	Gate-Drain Charge		---	1.8	---	
T <sub>d(on)</sub>	Turn-On Delay Time	V <sub>DD</sub> =-10V, V <sub>GS</sub> =-4.5V, R <sub>G</sub> =6Ω I <sub>D</sub> =-1A,	---	20	---	ns
T <sub>r</sub>	Rise Time		---	18	---	
T <sub>d(off)</sub>	Turn-Off Delay Time		---	300	---	
T <sub>f</sub>	Fall Time		---	120	---	
C <sub>iss</sub>	Input Capacitance	V <sub>DS</sub> =-10V, V <sub>GS</sub> =0V, f=1MHz	---	420	---	pF
C <sub>oss</sub>	Output Capacitance		---	180	---	
C <sub>rss</sub>	Reverse Transfer Capacitance		---	90	---	

Note :

- 1.The data tested by surface mounted on a 1 inch<sup>2</sup> FR-4 board with 2OZ copper, t<sub>≤</sub>10sec.
- 2.The data tested by pulsed , pulse width ≤ 300us , duty cycle ≤ 2%
- 3.The power dissipation is limited by 150°C junction temperature
- 4.The data is theoretically the same as I<sub>D</sub> and I<sub>DM</sub> , in real applications , should be limited by total power dissipation.

Typical Performance Characteristics of P-Channel MOSFET

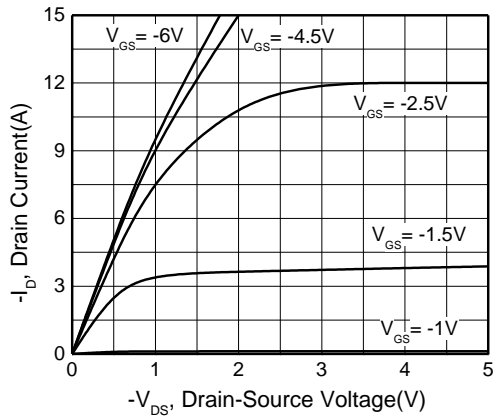


Fig 1. Output Characteristics

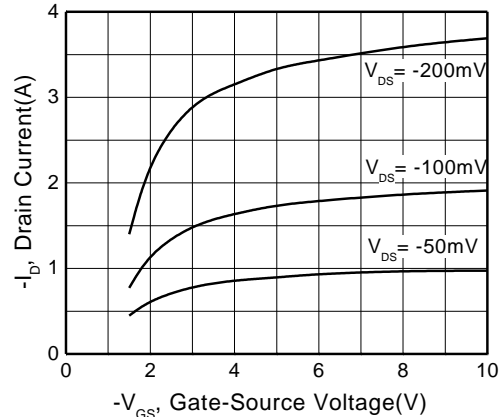


Fig 2. Transfer Characteristics

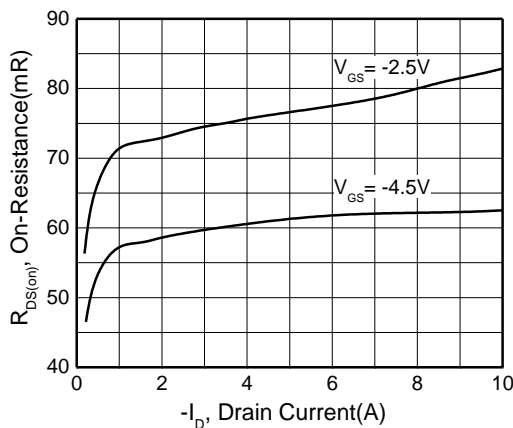


Fig 3. On-Resistance vs. Drain Current

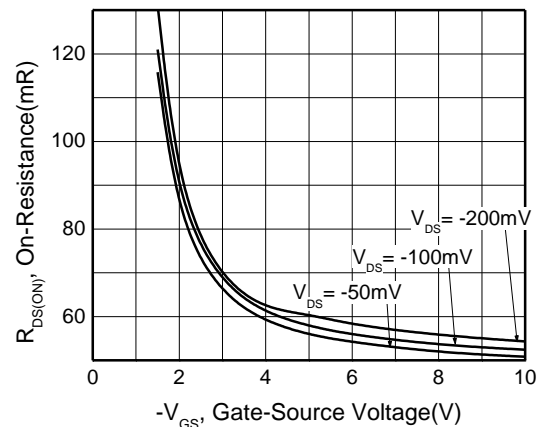


Fig 4. On-Resistance vs. Gate-Source Voltage

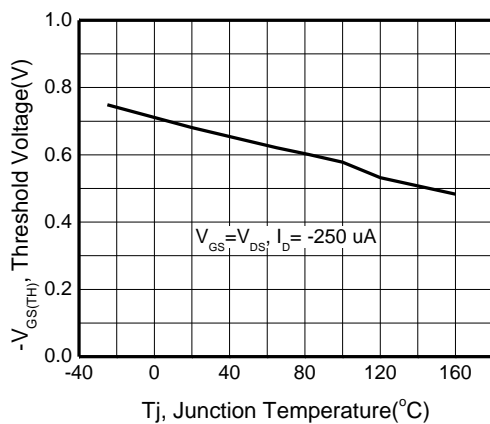


Fig 5. Threshold Voltage

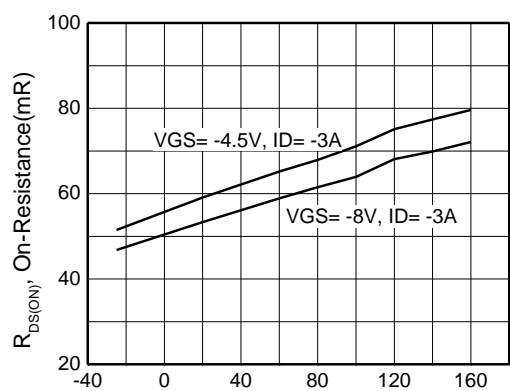
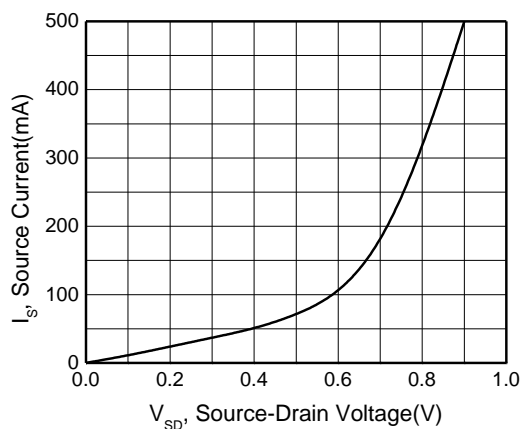
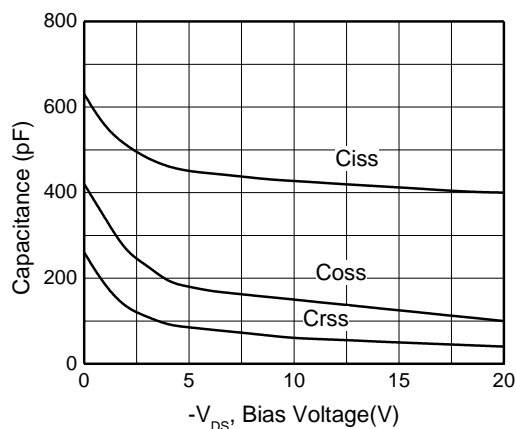


Fig 6. On-Resistance Temperature Coefficient

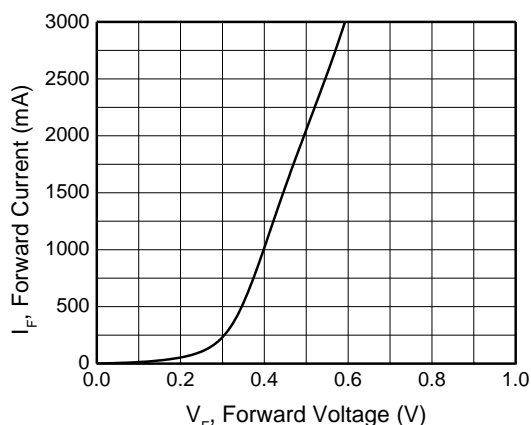


**Fig 7. Body Diode Forward Characteristics**

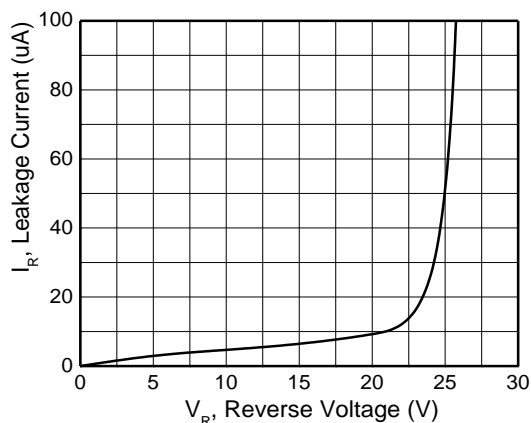


**Fig 8. Capacitance**

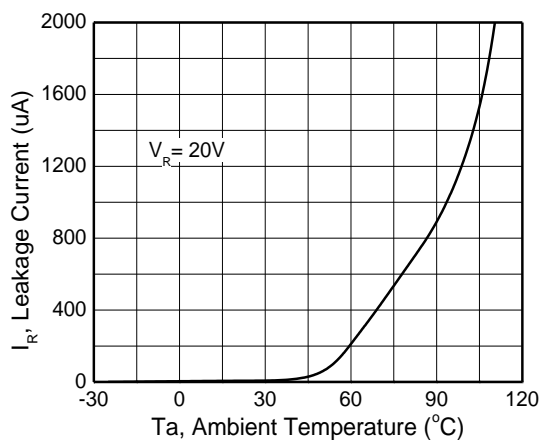
**Typical Performance Characteristics of Schottky**



**Figure 9. Schottky Forward Characteristics**

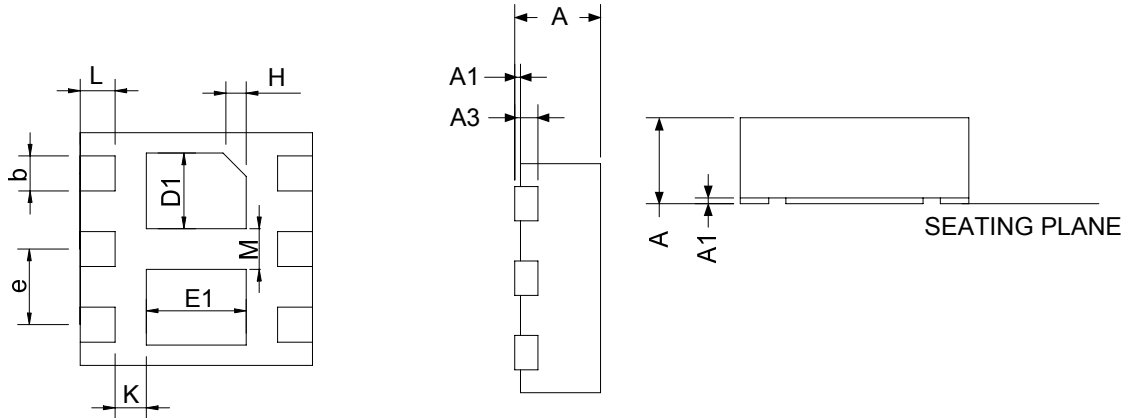
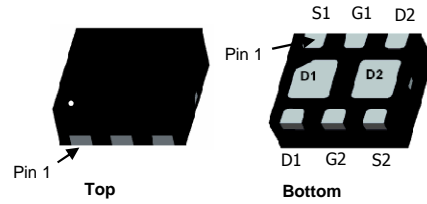
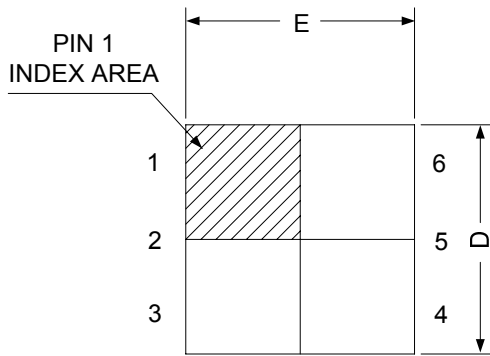


**Figure10. Schottky Reverse Characteristics**



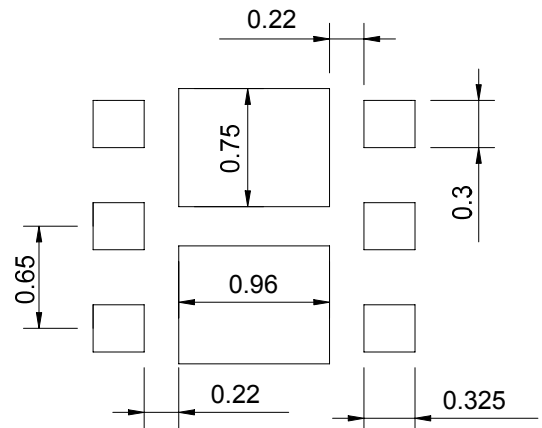
**Figure 11. Leakage Current Vs. Temperature**

**Package Information DFN2x2C-6\_EP2\_S**



SYMBOL	DFN2x2C-6_EP2_S			
	MILLIMETERS		INCHES	
	MIN.	MAX.	MIN.	MAX.
A	0.70	0.80	0.028	0.031
A1	0.00	0.05	0.000	0.002
A3	0.200 REF		0.008 REF	
b	0.25	0.35	0.010	0.014
D	1.90	2.10	0.075	0.083
D1	0.55	0.75	0.022	0.030
E	1.90	2.10	0.075	0.083
E1	0.76	0.96	0.030	0.038
e	0.65 BSC		0.026 BSC	
H	0.20 BSC		0.008 BSC	
K	0.17	0.37	0.007	0.015
L	0.25	0.35	0.010	0.014
M	0.25	0.45	0.010	0.018

**RECOMMENDED LAND PATTERN**



UNIT: mm

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