ISNID

ТО		文 件 编 号	HXA-L35-07(01)
TO:		发行日期	2015年12月25日
	承认规格	书	
	种 类: <u>功 率 电</u> 系 列 号: <u>HCDH5D28N-Ser</u> 客户料号:		
	客户承认档	2	
承	认日期 年	月日]

(贵司承认后请签署一份返回华信安电子,谢谢!)

厦门华信安电子科技有限公司技术质量部

承认	确认	作成
龙梅	梁峰	王亮

TEL: 0592-6301603 FAX: 0592-5205265 Http: www.xmisnd.com **Power Inductor**

HCDH5D28N-SERIES

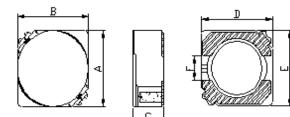
	ECN HISTORY LIST					
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN	
1.0	15/12/25	新發行	龙梅	梁峰	王亮	
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Power Inductor

1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% $\mbox{Lead}(\mbox{Pb})$ & Halogen-Free and RoHS compliant.

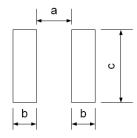
2. Dimension





HCDH5D28N-SERIES

Recommendend Land pattern



b(mm)

2.2 TYP

c(mm)

6.3 TYP

a(mm)

2.0 TYP

Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)
HCDH5D28N	6.0	6.0	3.0MAX	5.5	5.5	2.0

3. Part Numbering

HCDH	5D28	Ν	-	1 R0	Μ
А	В	С		D	Е
A: Series					
B: Dimension					
C: Control S/N					
D: Inductance		1R0=	=1.0uH	ł	
E: Inductance To	lerance	M=±	20%		

ISND Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) Max	l sat (A)	l rms (A)
HCDH5D28N-1R0M	1.0	±20%	0.25V100K	0.016	3.60	3.45
HCDH5D28N -1R8M	1.8	±20%	0.25V100K	0.022	2.85	2.70
HCDH5D28N -2R2M	2.2	±20%	0.25V100K	0.025	2.67	2.36
HCDH5D28N -3R3M	3.3	±20%	0.25V100K	0.032	2.41	2.25
HCDH5D28N -4R7M	4.7	±20%	0.25V100K	0.039	2.10	1.96
HCDH5D28N -6R8M	6.8	±20%	0.25V100K	0.052	1.84	1.65
HCDH5D28N -100M	10	±20%	0.25V100K	0.078	1.60	1.35
HCDH5D28N -150M	15	±20%	0.25V100K	0.115	1.32	1.10
HCDH5D28N -220M	22	±20%	0.25V100K	0.156	1.10	0.92
HCDH5D28N -330M	33	±20%	0.25V100K	0.206	0.95	0.80
HCDH5D28N -470M	47	±20%	0.25V100K	0.260	0.80	0.61
HCDH5D28N -560M	56	±20%	0.25V100K	0.338	0.65	0.56
HCDH5D28N -680M	68	±20%	0.25V100K	0.364	0.62	0.50
HCDH5D28N -101M	100	±20%	0.25V100K	0.520	0.54	0.42
HCDH5D28N -121M	120	±20%	0.25V100K	0.670	0.50	0.39
HCDH5D28N -221M	220	±20%	0.25V100K	1.300	0.40	0.28
HCDH5D28N -271M	270	±20%	0.25V100K	1.520	0.35	0.25

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ISND Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) Max	l sat (A)	l rms (A)
HCDH5D28N -331M	330	±20%	0.25V100K	2.210	0.30	0.23
HCDH5D28N -391M	390	±20%	0.25V100K	2.400	0.28	0.21
HCDH5D28N -471M	470	±20%	0.25V100K	2.600	0.25	0.19
HCDH5D28N -561M	560	±20%	0.25V100K	2.960	0.21	0.18
HCDH5D28N -681M	680	±20%	0.25V100K	3.700	0.20	0.17

Note:

Isat : Based on inductance change $(\ ^L/L0 : \le 35\%)$ @ ambient temp. 25°C

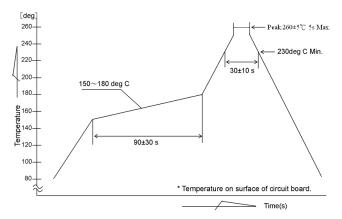
Irms : Based on temperature rise $(\ ^{\Delta}T : 40^{\circ}C \ typ.)$

5. Reliability and Test Condition

ltem	Performance	Test Method and Remarks
Operating Temperature	- 40 ~ +125°C.	Including self-generated heat
Storage Temperature	-40 ~ +85℃. - 5 to 40℃ for the product with taping.	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 0.25V
DC Resistance	-	DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change : Within±20%	Measurement of inductance shall be taken at temperature rang within–40°C to +85°C. With reference to inductance value at+20 °C, change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within–40°C to +125°C. With reference to inductance value at+20 °C, change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow. As illustrated below, apply force in the direction of the arrow indicating until deflection of the test board reaches to 2mm. Force Test Read Force Test Substrate size : 100x40x1.0 Substrate material : glass epoxy-resin Solder cream thickness : 0.15 4.0 1.5 2.1 1.5
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force : 10 N to X and Y directions. Duration : 5s Solder cream thickness : 0.15
Resistance to Vibration	Inductance change : Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time : 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.
Solderability	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall be dipped in flux, and then immersed in molten solder as shown in below. Flux : methanol solution containing rosin 25% Solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.

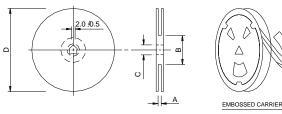
Item	Performance	Test Method and Remarks			
Resistance to soldering		The test sample shall be exposed to reflow oven at 230 ± 5 °C for 40 seconds, with peak temperature at 260 ± 5 °C for 5 seconds,2 times. Test board thickness: 1.0mm Test board material: glass epoxy-resin			
Thermolekisel		reflow. The test samp for specified to sequence.	oles shall be soldered to bles shall be placed at s ime by step 1 to step 4 ure cycles shall be repe	specified tempera as shown below	ature in
Thermal shock			Temperature("C)	Time(min.)	
		1	-40±3℃	30±3	
		2	RoomTemp	Within 3	
		3	85±2℃	30±3	
		4	RoomTemp	Within 3	
Damp heat life test		Test Method and Remarks The test samples shall be soldered to the test board by the reflow. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below. Temperature: $60\pm2^\circ\mathbb{C}$ Humidity: $90-95\%$ RH Time: $500+24/-0$ hrs			
Loading under damp heat life test		The test samples shall be soldered to the test board by the reflow. The test samples shall be placed in thermostatic oven set specified temperature and humidity and applied the rated current continuously as shown in below. Temperature: 60±2°C Humidity: 90-95%RH Applied current: Rated current Time: 500+24/-0 hrs			
Low temperature life test		The test samples shall be soldered to the test board reflow. After that, the test samples shall be placed at test co as shown in below. Temperature:-40±2℃ Time:500+24/-0 hrs			
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	o the test board b	y the

6. Soldering



7. Packaging Information

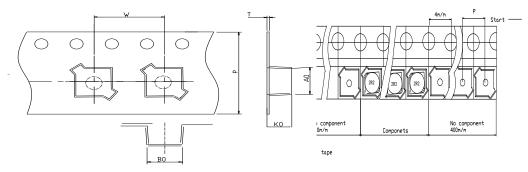
(1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
CDH5D28N	16.4±0.2	100±4.0	13.2±0.2	330±2.0

(2) Tape Dimension

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COVER TAPE

Туре	Ao(mm)	Bo(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HCDH5D28N	6.3±0.1	6.3±0.1	3.1±0.1	12.0±0.1	16±0.3	0.4±0.05

(3) Packaging Quantity

Туре	Chip / Reel
HCDH5D28N	1000

Application Notice

·Storage Conditions

- To maintain the solderability of terminal electrodes:
- 1. ISND products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^\circ C\,$ and 60% RH.
- Recommended products should be used within 12 months form the time of delivery.
 The packaging material should be kept where no chlorine or sulfur exists in the air.

·Transportation

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.



单击下面可查看定价,库存,交付和生命周期等信息

>>ISND(华信安)