

文件编号HXA-L15-10(02)发行日期2016年3月21日

## 承认规格书

种类:功率电感

系列号: <u>HCDH104N-Series</u>

客户料号:\_\_\_\_\_

李	客户 承 认 栏			
承认日期	年	月	日	

(贵司承认后请签署一份返回华信安电子,谢谢!)

## 厦门华信安电子科技有限公司技术质量部

承 认	确认	作成
龙梅	梁峰	王亮

TEL: 0592-6301603 FAX: 0592-5205265 Http: www.xmisnd.com ISND P1

## Power Inductor

**HCDH104N-SERIES** 

	ECN HISTORY LIST						
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN		
1.0	16/1/11	新 發 行	龙梅	梁峰	王亮		
2.0	16/3/21	产品升级,特性重新修订	龙梅	梁峰	王亮		
備註							

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ISND P2

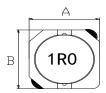
## **Power Inductor**

**HCDH104N-SERIES** 

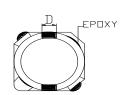
### 1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

#### 2. Dimension





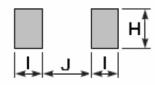


Series	A(mm)	B(mm)	C(mm)	D(mm)
HCDH104N	10.0±0.3	10.2±0.3	3.8±0.2	3.0 REF

# Halogen-free



#### **Recommendend Land pattern**



H(mm)	l(mm)	J(mm)
3.6 TYP	1.7 TYP	7.3 TYP

## 3. Part Numbering













Α

A: Series

B: Dimension

C: Control S/N

C: Control S/N

D: Inductance 1R5=1.5uH

E: Inductance Tolerance  $M=\pm20\%$ ; Y= $\pm30\%$ 

## 4. Specification

ISND Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) Max	l sat (A)	I rms (A)
HCDH104N -1R0Y	1.0	±30%	0.25V100K	0.010	10.0	7.20
HCDH104N -1R5Y	1.5	±30%	0.25V100K	0.010	9.5	6.55
HCDH104N -2R2Y	2.2	±30%	0.25V100K	0.013	7.5	5.32
HCDH104N -3R3Y	3.3	±20%	0.25V100K	0.017	7.0	4.93
HCDH104N -4R7M	4.7	±20%	0.25V100K	0.025	5.5	4.21
HCDH104N -6R8M	6.8	±20%	0.25V100K	0.032	5.0	3.92
HCDH104N -100M	10	±20%	0.25V100K	0.037	4.4	3.51
HCDH104N -150M	15	±20%	0.25V100K	0.060	3.6	3.2
HCDH104N -220M	22	±20%	0.25V100K	0.083	2.8	2.21
HCDH104N -330M	33	±20%	0.25V100K	0.106	2.2	1.74
HCDH104N -470M	47	±20%	0.25V100K	0.138	2.0	1.62
HCDH104N -560M	56	±20%	0.25V100K	0.200	1.6	1.26
HCDH104N -680M	68	±20%	0.25V100K	0.235	1.4	1.12
HCDH104N -101M	100	±20%	0.25V100K	0.340	1.3	1.10
HCDH104N -151M	150	±20%	0.25V100K	0.540	1.1	0.90
HCDH104N -221M	220	±20%	0.25V100K	0.780	0.9	0.73
HCDH104N -331M	330	±20%	0.25V100K	1.150	0.7	0.58

Note:

 $\label{lambda} \mbox{Isat}: \mbox{Based on inductance change} \quad (\triangle L/L0: \ \ \le \mbox{-}35\%) \ \ \ \mbox{@ ambient temp. } 25\% \\ \mbox{Irms}: \mbox{Based on temperature rise} \quad (\triangle T: \mbox{40}\% \ \mbox{typ.} \mbox{\sc www.xmisnd.com} \\ \mbox{\sc om}$ 

ISND P3

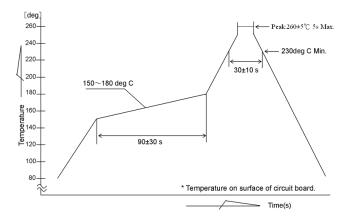
## 5. Reliability and Test Condition

ltem	Performance	Test Method and Remarks
Operating Temperature	- 40 ~ +125℃.	Including self-generated heat
Storage Temperature	-40 ~ +85℃. - 5 to 40℃ for the product with taping.	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 0.25V
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change: Within±20%	Measurement of inductance shall be taken at temperature rang within–40°C to +85°C. With reference to inductance value at+20°C,change rate shall be calculated.  Measurement of inductance shall be taken at temperature rang within–40°C to +125°C. With reference to inductance value at+20°C,change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow.  As illustrated below, apply force in the direction of the arrow indicating until deflection of the test board reaches to 2mm.  Provided Testing Board  Rest Board  As In 200x40x1.0  Substrate size: 100x40x1.0  Substrate material: glass epoxy-resin  Solder cream thickness: 0.15
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow.  10 N, 5 s  Applied force : 10 N to X and Y directions.  Duration : 5s  Solder cream thickness : 0.15
Resistance to Vibration	Inductance change : Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.
Solderability	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall be dipped in flux, and then immersed in molten solder as shown in below. Flux: methanol solution containing rosin 25% Solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.

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<u>ISND</u>					P4	
Item	Performance		Test Method and R	emarks		
Resistance to soldering		The test sample shall be exposed to reflow oven at 230±5 for 40 seconds, with peak temperature at 260±5℃ for 5 seconds,2 times.  Test board thickness: 1.0mm  Test board material: glass epoxy-resin				
Thermal shock		reflow. The test samp for specified to sequence.	oles shall be soldered to oles shall be placed at ime by step 1 to step 4 ure cycles shall be rep	specified tempera as shown below	ature in	
THE THAT SHOCK		Phase	Temperature(℃)	Time(min.)		
		1	-40±3℃	30±3		
		2	RoomTemp	Within 3		
		3 4	85±2℃ RoomTemp	30±3 Within 3	- 1	
		4	Room remp	AAIIIIII 2		
Damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	Test Method and Remarks The test samples shall soldered to the test board by the reflow. The test samples shall be placed in thermostatic c specified temperature and humidity as shown in b Temperature: 60±2°C Humidity: 90~95%RH Time: 500+24/-0 hrs				
Loading under damp heat life test		reflow. The test samp specified temp current contin Temperature: Humidity: 90~	95%RH nt: Rated current	hermostatic oven	set at	
Low temperature life test		reflow.	-40±2℃			
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	o the test board b	by the	

## 6. Soldering

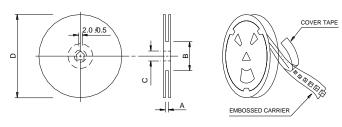


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<u>ISND</u> P5

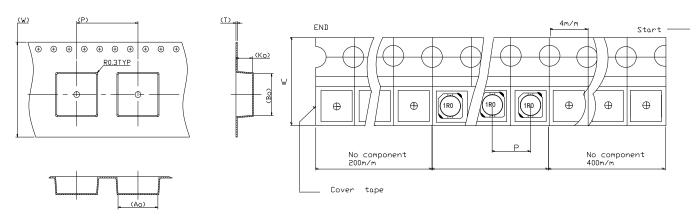
## 7. Packaging Information

#### (1) Reel Dimension



Туре	A(mm)	B(mm) C(mm) D		D(mm)
HCDH104N	24.4±0.2	100±4.0	13.2±0.2	330±2.0

#### (2) Tape Dimension



Туре	Ao(mm)	Bo(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HCDH104N	10.6±0.1	10.65±0.1	4.2±0.1	16.0±0.1	24±0.3	0.35±0.05

#### (3) Packaging Quantity

Туре	Chip / Reel
HCDH104N	1000

#### **Application Notice**

- Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. ISND products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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单击下面可查看定价,库存,交付和生命周期等信息

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