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		HXCD105N-S	eries	
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	(贵司承认后请	签署一份返回华信	运电子,谢谢!)	
	厦门华信安申	子科技有限公	司技术质量部	
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	Htt	tp: www.xmisnd.c	com	



SMD Power Inductor

HXCD1	05N-Series
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		ECN H	ISTORY L	IST	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	15/12/11	新发行	龙梅	梁峰	王亮
备					
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SMD Power Inductor

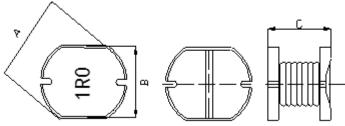
1. Features

- 1. Small and Low profile inductor
- 2. It corresponds to High current.
- 3. Simple and Shield structure.
- 4. Available tape and reel for auto insertion.
- 5. 100% Lead(Pb)-Free and RoHS compliant.

2. Applications

-For small DC/DC converter(cellular phone,LCD/LED/OLED display, HDD, DSC etc)

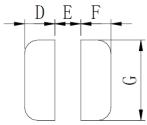
3. Dimensions



Recommendend Land pattern

Pb-free

Halogen-free



	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
HXCD105N	10±0.3	9.0±0.3	5.4±0.4	3.0	2.1	4.5	11

4. Part Numbering

HXCD	1 05	N	-	1R0	Μ	
А	В	С		D	E	
A: Series B: Dimen C: Type			A*E	3*C		
D: Induct E: Induct	ance ance Tolerar	ice	K= ma	±10%, M=:	n cannot decide polarity. Color:	Black, unidirectional.



HXCD105N-Series



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5. Specification

	Inductance			Rated	current	DCR
Part Number	L0 (uH)	Tolerance	Frequency (Hz/0.25V)	Saturation	Tempetature	(mΩ) @25℃
	@ 0 A			current I sat (A)	current I rms (A)	± 20%.
HXCD105N-1R0M	1.0	±20%	100K	8.70	8.0	8.45
HXCD105N-1R8M	1.8	±20%	100K	6.88	6.50	15.60
HXCD105N-3R3M	3.3	±20%	100K	5.70	5.00	19.50
HXCD105N-4R7M	4.7	±20%	100K	5.10	4.60	22.10
HXCD105N-6R8M	6.8	±20%	100K	4.30	3.60	26.00
HXCD105N-100M	10.0	±20%	100K	2.90	2.58	34.45
HXCD105N-150M	15.0	±20%	100K	2.70	2.26	45.50
HXCD105N-220M	22.0	±20%	100K	2.20	1.93	70.20
HXCD105N-330M	33.0	±20%	100K	1.98	1.46	92.30
HXCD105N-470M	47.0	±20%	100K	1.70	1.26	135.2
HXCD105N-680M	68.0	±20%	100K	1.45	1.10	200.2
HXCD105N-101K	100.0	±10%	100K	1.26	0.95	260.0
HXCD105N-151K	150.0	±10%	100K	1.02	0.74	403.0
HXCD105N-221K	220.0	±10%	100K	0.90	0.65	572.0
HXCD105N-331K	330.0	±10%	100K	0.80	0.50	845.0
HXCD105N-471K	470.0	±10%	100K	0.63	0.40	1180
HXCD105N-681K	680.0	±10%	100K	0.58	0.28	1990
HXCD105N-821K	820.0	±10%	100K	0.43	0.24	2030

Note:

1. All test data referenced to 25°C ambient.

2. Testing Instrument : L/Q: HP4284A,CH11025,CH3302,CH1320 ,CH1320S LCR METER / Rdc:CH16502,Agilent33420A MICRO OHMMETER.

3. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\ {}{\vartriangle} t \text{ of } 40^\circ\! \text{C}$ (keep 1min.).

4. Saturation Current (Isat) will cause L0 to drop 30% typical. (keep quickly).

5. The part temperature (ambient + temp rise) should not exceed 125 °C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

6. Special inquiries besides the above common used types can be met on your requirement.



6. Material List



2

NO	Items	Materials
1	Core	Ferrite core.
2	Wire	Polyester Wire or equivalent.
3	Ink	Halogen-free ketone

7. Reliability and Test Condition

ltem	Performance	Test Condition
Operating temperature	-40~+125°C	
Storage temperature and Humidity range	-10~+40°C,50~60%RH (Product without taping)	
Electrical Performance Te	st	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR		CH16502, Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	△L30% typical.	Saturation DC Current (Isat) will cause L0 to drop $\triangle L(\%)$ (keep quickly).
Heat Rated Current (Irms)	Approximately ∆T≦40℃	Heat Rated Current (Irms) will cause the coil temperature rise $\Delta T(^{\circ}C)$ without core loss. 1.Applied the allowed DC current(keep 1 min.). 2.Temperature measured by digital surface thermometer
Reliability Test		
High Temperature Exposure Test		Temperature:125±2℃. Duration:1000±12hrs. Measured at room temperature after placing for 2 to 3hrs. (MIL-PRF-27)
Low Temperature Life Test		Temperature:-40±2°C. Duration:500±12hrs. Measured at room temperature after placing for 2 to 3hrs.
Biased Humidity Test		Humidity:85±3%RH. Temperature:85±2°C. Duration:1000±12hrs. Measured at room temperature after placing for 2 to 3hrs (AEC-Q200-REV C)
Thermal shock test	Electric specifications should be satisfied	Condition for 1 cycle Step 1:-40+0 / -2°C 15±1 min. Step 2:Room temperature within ≤ 0.2 min. Step 3:+125+2 / -0°C 15±1 min. Number of cycles:300 Measured at room temperature after placing for 2 to 3 hrs. (AEC-Q200-REV C)
Vibration test		Frequency: 10-2000-10Hz for 20 min. Amplitude: Parts mounted within 2" from any secure point. Directions and times: X, Y, Z directions for 20 min. This cycle shall be performed 12 times in each of three mutually perpendicular directions (Total 12hours). (MIL-STD-202 Method 204 D Test condition B)
Reflow test		Pre-heat: 150±5°C Duration: 5 minutes Temperature: 260±5°C, 5~10 seconds (IPC/JEDEC J-STD-020C)
Solder test	Terminals should be covered by over 95% solder on visual inspection	After dip into flux, dip into solder 235±5℃, 4±1seconds Flux 、 solder for lead free (ANSI /J-STD-002C Method B)



8. Soldering and Mounting

(1) Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. ISND terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

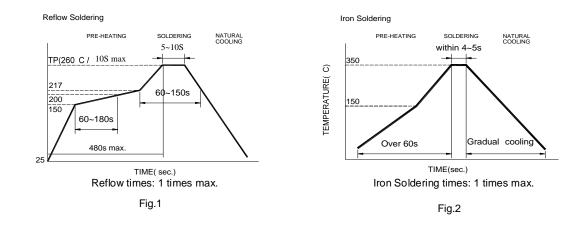
(2) Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

(3) Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

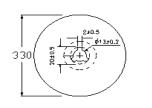
 Preheat circuit and products to 150°C
 •Never contact the ceramic with the iron tip 355°C tip temperature (max)
 •1.0mm tip diameter (max)
 Use a 20 watt soldering iron with tip diameter of 1.0mm Limit soldering time to 4~5sec.

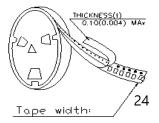




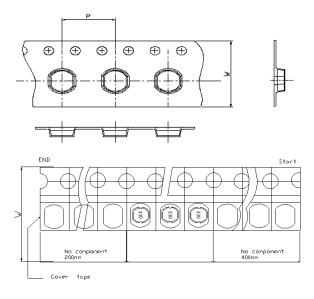
9. Packaging Information

(1) Reel Dimension





(2) Tape Dimension



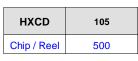
W	24
Р	12
A0	9.50
B0	10.40
K0	5.80

Unreeling

Directio

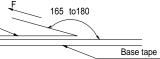


(3) Packaging Quantity



(4) Tearing Off Force





The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-C-2003 of 4.11 stadnard).

Room Temp.	Room Humidity	Room atm	Tearing Speed
(°C)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

torage Condition	S
To maintain the	solderability of terminal electrodes:
1. ISND product	s meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
2. Temperature	and humidity conditions: Less than 40 $^\circ C$ and 60% RH.
3. Recommende	ed products should be used within 12 months form the time of delivery.
4. The packaging	material should be kept where no chlorine or sulfur exists in the air.
Fransportation	
1. Products sho	uld be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tw	eezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling	should ensure that abrasion and mechanical shock are minimized.





单击下面可查看定价,库存,交付和生命周期等信息

>>ISND(华信安)