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# 承认规格书

种 类: Ferrite Chip Inductor

系列号: <u>HXCI1005F-Series</u>

客户料号:\_\_\_\_\_

客户承认栏							
·							
承认日期	年	月	日				

(贵司承认后请签署一份返回华信安电子,谢谢!)

# 厦门华信安电子科技有限公司技术质量部

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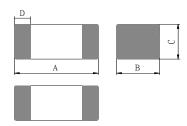
# Ferrite Chip Inductor(Lead Free)

HXCI1005F-Series

# 1. Features

- 1. Monolithic inorganic material construction.
- 2.Closed magnetic circuit avoids crosstalk.
- 3.S.M.T. type.
- 4. Suitable for flow and reflow soldering.
- 5. Shapes and dimensions follow E.I.A. spec.
- 6. Available in various sizes.
- 7.Excellent solderability and heat resistance.
- 8. High reliability.
- 9.This component is compliant with RoHS legislation and also support lead-free soldering.

# 2. Dimension



Chip size						
Size	A(mm)	B(mm)	C(mm)	D(mm)		
1005	1.0±0.1	0.5±0.1	0.5±0.1	0.25±0.1		

# 3. Part Numbering



A: Series

B: Dimension L x W

C: Material Lead Free Material
D: Inductance 2R2=2.2uH

E: Inductance Tolerance K=±10%, J=±5%, L=±15%, M=±20%, N=±25%

# Ag(100%) Ni(100%)-1.5um (min.) Sn(100%)-3.0um (min.)

# 4. Specification

ISND Part Number	Inductance (uH)	Q min.	Test Frequency (MHz)	Rated Current (mA)	DC Resistance (Ohm) max.	SRF (MHz) min.
HXCI1005F-R22□	0.22	10	25	25	1.20	110
HXCI1005F-R33□	0.33	15	25	18	1.25	85
HXCI1005F-1R0□	1.0	20	10	15	0.90	40
HXCI1005F-1R8□	1.8	20	10	15	1.45	30
HXCI1005F-2R2□	2.2	20	10	10	1.70	28

TOLERANCE J: +/-5% K:+/-10% L:+/-15% M:+/-20%



# 5. Reliability and Test Condition

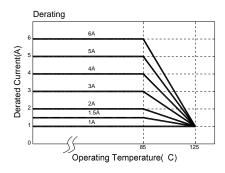
Item					Perfori	manc	е				Test Condition
Series No.	нхсв	HXCM	HXCB	НХРВ	HXFB	HXCA	HXCI	НХНІ	нхсн	HXCI	
Operating Temperature			-55~-	-125°C				-40~	-+85°C		
Storage Temperature			-55~-	+125°C				-40~	+85°C		
mpedance (Z)											
nductance (Ls)											HP4291A, HP4287A+16092A
) Factor	Refer	to stand	lard elect	rical ch	aracterist	ics list					
OC Resistance										HP4338B	
Rated Current									**		
Temperature Rise Test	30°C	max. (Δ	T)								Applied the allowed DC current.     Temperature measured by digital surface thermometer.
Solder heat Resistance			o significa		-		mechanic maining te	_		'0% min.	Preheat: 150°C,60Sec. Solder: Sn-Ag3.0-Cu0.5 Solder tamperature: 260±5°C Flux for lead free: rosin Dip time: 10±0.5sec.  Preheating Dipping Natural cooling 260 C  150 C  150 C  10.0.5  10.0.5
Solderability		ode sho	6 of the t			230 C		oping Natural	n		Preheat: 150°C,60sec. Solder: Sn-Ag3.0-Cu0.5 Solder tamperature: 230±5°C Flux for lead free: rosin Dip time: 4±1sec.
Terminal strength	not be		ed by the		dielectric				<b></b> \	N	For HXCB HXCM HXCI: Size Force (Kfg) Time(sec) 1005 0.2 1608 0.5 2012 0.6 3216 1.0 >25 3225 1.0 4516 1.0 4532 1.5 5750 2.0 For FCA: Size Force (Kfg) Time(sec) 3216 0.5 >25
Flexture strength	not be		ed by the		dielectrical		<u> </u>	100(3.937		Bending   40(1.575)	Solder a chip on a test substrate, bend the substrate by 2mm (0.079in)and return.
Bending Strength			ould not l			<u>R</u> (	0.5(0.02	1.0(0.039 ) — — — chip			Size         mm(inches)         P-Kgf           1608         0.80(0.033)         0.3           2012         1.40(0.055)         1.0           3216         3225         2.00(0.079)         2.5           4516         4532         2.70(0.106)         2.5           5750         2.70(0.106)         2.5
Random Vibration Test	chara	cteristics	Cracking s should rithin±30°	not be a	-	ny othe	r defects I	narmful t	o the		Frequency: 10-55-10Hz for 1 min. Amplitude: 1.52mm Directions and times: X, Y, Z directions for 2 hours. A period of 2 hours in each of 3 mutually perpendicular directions (Total 6 hours).
Drop	Drop	10 times	on a co	ncrete fl	oor from	a heigl	ht of 75cm	ı			a: No mechanical damage b: Impedance change: ±30%



Item	Perfor	rmace	Test Condition
Loading at High Temperature	Appearance: no damage.		Temperature: 125±5°C(bead),85±5°C(inductor) Applied current: rated current. Duration: 500±12hrs. Measured at room temperature after placing for 2 to 3hrs.
Humidity	Impedance: within±30% of initial value. Inductance: within±10% of initial value. Q: within±30% of initial value. (FCI FHI Q: within±20% of initial value. (HCI)	FCH)	Humidity: 90~95%RH. Temperature: 40±2°C. Temperature: 60±2°C.(HCl) Duration: 500±12hrs. Measured at room temperature after placing for 2 to 3hrs.
Thermal shock	Appearance: no damage.  Impedance: within±30%of initial value. Inductance: within±10%of initial value. Q: within±30%of initial value. (FCI FHI FCH) Q: within±20%of initial value. (HCI)	For Bead :  Phase	For HXCB FXCM HXCB HXPB HXFB FXCA: Condition for 1 cycle Step1: -55±2°C 30±3 min. Step2: +125±5°C 30±3 min. Number of cycles: 5 For FCI FHI FCH HCI: Condition for 1 cycle Step1: -40±2°C 30±3 min. Step2: +85±5°C 30±3 min. Number of cycles: 100 Measured at room temperature after placing for 2 to 3 hrs.
Low temperature storage test		2 ±85±5°C 30±3  Measured: 100 times	Temperature: -55±2°C. Duration: 500±12hrs. Measured at room temperature after placing for 2 to 3hrs.
Drop	Drop 10 times on a concrete floor from	a height of 75cm	a: No mechanical damage b: Impedance change: ±30%

# \*\*Derating Curve

For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 85°C, the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



# 6. Soldering and Mounting

# 6-1. Recommended PC Board Pattern

			Pattern ow Solde	• . • .				
Series	Туре	A(mm)	B(mm)	C(mm)	D(mm)	L(mm)	G(mm)	H(mm)
HXCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	2.60	0.60	0.80
НХСВ	2012	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	3.00	1.00	1.00
HXCI	3216	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	4.40	2.20	1.40



PC board should be designed so that products are not sufficient under mechanical stress as warping the board.

Products shall be positioned in the sideway direction against the mechanical stress to prevent failure.

## 6-2. Soldering

Mildly activated rosin fluxes are preferred. The minimum amount of solder can lead to damage from the stresses caused by the difference in coefficients of expansion between solder, chip and substrate. The terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

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#### 6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1.

## 6-2.2 Solder Wave:

Wave soldering is perhaps the most rigorous of surface mount soldering processes due to the steep rise in temperature seen by the circuit when immersed in the molten solder wave , typical at 230°C. Due to the risk of thermal damage to products, wave soldering of large size products is discouraged. Recommended temperature profile for wave soldering is shown in Figure 2.

## 6-2.3 Soldering Iron(Figure 3):

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

Note: Preheat circuit and products to 150°C 350°C tip temperature for Ferrite chip bead (max) ·Never contact the ceramic with the iron tip ·1.0mm tip diameter (max)

·Use a 20 watt soldering iron with tip diameter of 1.0mm ·Limit soldering time to 3 sec.

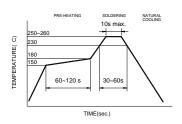


Figure 1. Re-flow Soldering(Lead Free)

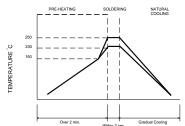


Figure 2. Wave Soldering

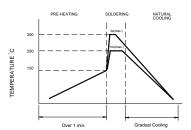


Figure 3. Hand Soldering

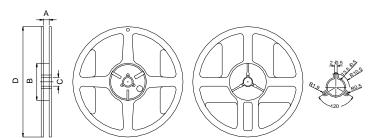
#### 6-2.4 Solder Volume:

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:



# 7.Packaging Information

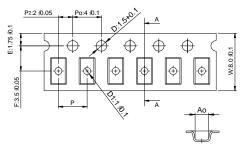
# 7-1. Reel Dimension

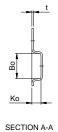


Туре	A(mm)	B(mm)	C(mm)	D(mm)	
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2	
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

# 7-2.1 Tape Dimension / 8mm

7"x8mm





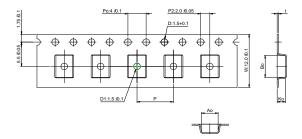
7"x12mm

Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
FCB,FCM	100505	1.12±0.05	0.67±0.05	0.54±0.05	2.0±0.1	0.23±0.05	none
HXCB	160808	1.80±0.10	1.01±0.10	1.02±0.10	4.0±0.1	0.22±0.05	none
HXFB	201209	2.25±0.10	1.42±0.10	1.04±0.10	4.0±0.1	0.22±0.05	1.0±0.1
HXCI	201212	2.35±0.10	1.50±0.10	1.45±0.10	4.0±0.1	0.22±0.05	1.0±0.1

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# 7-2.2 Tape Dimension / 12mm

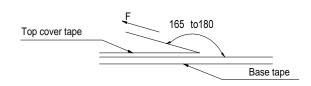


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
HXCB,	451616	4.95±0.1	1.93±0.1	1.93±0.1	4.0±0.1	0.24±0.05	1.5±0.1
HXCM	453215	4.95±0.1	3.66±0.1	1.85±0.1	8.0±0.1	0.24±0.05	1.5±0.1
HXCI	575018	6.10±0.1	5.40±0.1	2.00±0.1	8.0±0.1	0.30±0.05	1.5±0.1

## 7-3. Packaging Quantity

Chip Size	575018	453215	451616	322513	321611	201212	201209	160808	100505
Chip / Reel	1000	1000	2000	2500	3000	2000	4000	4000	10000
Inner box	4000	4000	8000	12500	15000	10000	20000	20000	50000
Middle box	20000	20000	40000	62500	75000	50000	100000	100000	250000
Carton	40000	40000	80000	125000	150000	100000	200000	200000	500000
Bulk (Bags)	7000	12000	20000	30000	50000	100000	150000	200000	300000

## 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed	
(°C)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060	300	

# **Application Notice**

# ·Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. Temperature and humidity conditions: -10~  $40^{\circ}$ C and  $30\sim70\%$  RH.
- 2. Recommended products should be used within 6 months from the time of delivery.
- 3. The packaging material should be kept where no chlorine or sulfur exists in the air.

# $\cdot Transportation \\$

- 1.Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价,库存,交付和生命周期等信息

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