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承 认 规 格 书

种 类: __Power Inductor__

系列号: HXNR3015B-Series_

客户料号:_____

	室	客户 承 认	人栏		
承 认	日期		年	月	日

(贵司承认后请签署一份返回华信安电子, 谢谢!)

厦门华信安电子科技有限公司技术质量部

承认	确认	作成
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Power Inductor

HXNR3015B-SERIES

	ECN HISTORY LIST								
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN				
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Power Inductor

HXNR3015B-SERIES

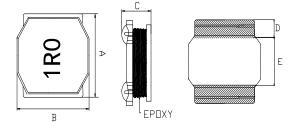
1. Features

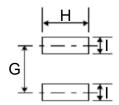
- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

2. Dimension









Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	G(mm)	H(mm)	l(mm)
HXNR3015B	3.0±0.1	3.0±0.1	1.5 max.	0.9±0.3	1.2±0.3	2.2 ref.	2.7 ref.	0.8 ref.

Units: mm

3. Part Numbering

HXNR	3015	B	-	2R2	M
Α	В	С		D	Е

A: Series

B: Dimension

C: Control S/N

D: Inductance 2R2=2.2uH

E: Inductance Tolerance $M=\pm 20\%$; $Y=\pm 30\%$

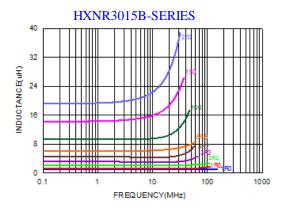
4. Specification

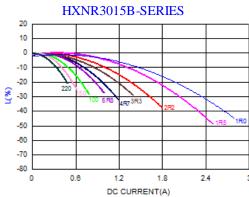
ISND Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	SRF (MHz) min.	DCR (Ω) ±30%	I sat (A)	I rms (A)
HXNR3015B-1R0Y	1.0	±30%	1V100K	100	0.038	2.20	2.15
HXNR3015B-1R5Y	1.5	±30%	1V100K	87	0.050	2.07	1.95
HXNR3015B-2R2M	2.2	±20%	1V100K	64	0.060	1.55	1.50
HXNR3015B-3R3M	3.3	±20%	1V100K	49	0.090	1.30	1.21
HXNR3015B-4R7M	4.7	±20%	1V100K	40	0.130	1.08	1.01
HXNR3015B-6R8M	6.8	±20%	1V100K	36	0.180	0.90	0.88
HXNR3015B-100M	10	±20%	1V100K	28	0.260	0.72	0.71
HXNR3015B-150M	15	±20%	1V100K	23	0.350	0.66	0.56
HXNR3015B-220M	22	±20%	1V100K	20	0.460	0.51	0.47
HXNR3015B-330M	33	±20%	1V100K	14	0.820	0.42	0.38
HXNR3015B-470M	47	±20%	1V100K	10	1.350	0.38	0.32

Note:

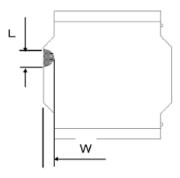
Isat : Based on inductance change $\ (\triangle L/L0 : \le 30\%) \ @$ ambient temp. 25°C

Irms : Based on temperature rise $\ (\ ^{\Delta}T : 40^{\circ}C \ typ. \)$





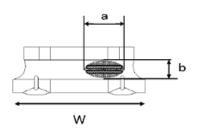
CoFbe hipping agance standard of the chipping size in top side, of bottom side ferrite core is following dimension.



Туре	L	w
HXNR3015B	0.6mm Max.	0.6mm Max.

Void appearance tolerance Limit

Size of voids occurring to coating resin is space afficient to toltow ance limit of coating resin part on product side.

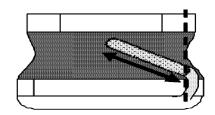


Size of exposed wire occurring to coating resin is specified below.

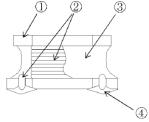
- Width direction (dimension a): Acceptable when a≦w/2
 Nonconforming when a > w/2
- 2. Length direction (dimension b): Dimension b is not specified.
- When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

External appearance criterion for exposed wire

Exposed end of the winding wire at the secondary side should be 2mm and below.



5. Material List



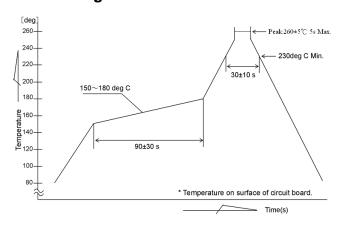
No.	Item	Material
1	Core	Ni-Zn ferrite
2	Wire	Copper Wire
3	Coating	Ероху
4	Solder	Lead free

6. Reliability and Test Condition

Item	Performance	Test Method and Remarks
Operating Temperature	- 25 ~ +120℃.	Including self-generated heat
Storage Temperature	-40 ~ +85°C. - 5 to 40°C for the product with taping.	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 1V
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change: Within±20%	Measurement of inductance shall be taken at temperature rang within–25℃ to +85℃. With reference to inductance value at+20 ℃,change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within–40℃ to +125℃. With reference to inductance value at+20 ℃,change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow. As illustrated below, apply force in the direction of the arroy indicating until deflection of the test board reaches to 2mm. The test samples shall be soldered to the testing board by the arroy indicating until deflection of the test board reaches to 2mm. The test samples shall be soldered to the testing board by the arroy indicating until deflection of the test board reaches to 2mm. The test samples shall be soldered to the testing board by the reflection of the arroy indicating until deflection of the test board reaches to 2mm. The test samples shall be soldered to the testing board by the reflection of the arroy indicating until deflection of the test board reaches to 2mm. The test samples shall be soldered to the testing board by the reflection of the test board reaches to 2mm. The test samples shall be soldered to the test board reaches to 2mm. The test samples shall be soldered to the test board reaches to 2mm. The test samples shall be soldered to the test board reaches to 2mm. The test samples shall be soldered to the test board reaches to 2mm. The test samples shall be soldered to the test board reaches to 2mm. The test samples shall be
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force: 10 N to X and Y directions. Duration: 5s
Resistance to Vibration	Inductance change: Within±10% No abnormality observed in appearance.	Solder cream thickness: 0.15 The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.
Solderability	At least 90% of surface of terminal electrode is covered by new sold	The test samples shall be dipped in flux, and then immersed in molten solder as shown in below. Flux: methanol solution containing rosin 25% es. Solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.
Resistance to soldering	Inductance change: Within±10% No abnormality observed in appearance.	The test sample shall be exposed to reflow oven at 230±5°C for 40 seconds, with peak temperature at 260±5°C for 5 seconds,2 times. Test board thickness: 1.0mm Test board material: glass epoxy-resin

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Item	Performance		Test Method and R	lemarks		
Thermal shock		The test samples shall be soldered to the test boareflow. The test samples shall be placed at specified tem for specified time by step 1 to step 4 as shown be sequence. The temperature cycles shall be repeated 100 cyc Phase Temperature(*C) Time(min 1 -40±3**C 30±3 2 RoomTemp Within 3 3 85±2**C 30±3 4 RoomTemp Within 3				
Damp heat life test		soldered to the	95%RH	low. ' thermostatic oven		
Loading under damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	reflow. The test samp specified temp current contine Temperature: Humidity: 90~	95%RH nt: Rated current	thermostatic oven and applied the ra	set at	
Low temperature life test		reflow.	-40±2℃			
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	to the test board b	by the	

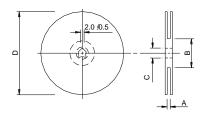
7. Soldering

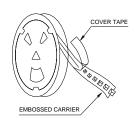


(2) Tape Dimension

8. Packaging Information

(1) Reel Dimension





φ1.5±β1 A B K0 Electrode

Туре	A(mm) B(ı		C(mm)	D(mm)	
HXNR3015B	8.4±0.2	60±0.3	13±0.1	179±0.5	

Туре	Ao(mm)	Bo(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HXNR3015B	3.2±0.1	3.2±0.1	1.7±0.1	4.0±0.1	8.0±0.3	0.30±0.05

(3) Packaging Quantity

Туре	Chip / Reel
HXNR3015B	2000

Application Notice

·Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. ISND products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^{\circ}\text{C}\,$ and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- $\label{eq:continuous} \textbf{4.} \ \textbf{The packaging material should be kept where no chlorine or sulfur exists in the air.}$
- ·Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价,库存,交付和生命周期等信息

>>ISND(华信安)