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承认规格书

种 类: <u>Power Inductor</u>

系列号: HXNR252012NF-Series

客户料号:_____

客户承认栏	

年

月

日

(贵司承认后请签署一份返回华信安电子,谢谢!) 厦门华信安电子科技有限公司技术质量部

承认日期

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Power Inductor

HXNR252012NF-SERIES

	ECN HISTORY LIST							
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN			
1.0	15/12/25	新 發 行	龙梅	梁峰	王亮			
2.0	16/03/09	产品升级,特性重新修订	龙梅	梁峰	王亮			
備註								

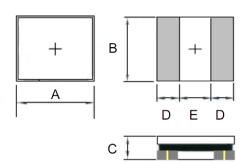
Power Inductor

HXNR252012NF-SERIES

1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

2. Dimension



Hallegen
Halogen-free



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
HXNR252012NF	2.5-0.1/+0.3	2.0-0.05/+0.35	1.2 max	0.85 ref	0.80 ref

3. Part Numbering

HXNR 252012 NF - 2R2 N

A: Series

B: Dimension

C: Control S/N

D: Inductance 2R2=2.2uH

E: Inductance Tolerance M=±20%; Y=±30%

4. Specification

ISND Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) ±20%	I sat (A).	l rms (A)
HXNR252012NF-R47M	0.47	±20%	0.1V1M	0.028	4.00	3.70
HXNR252012NF-R68M	0.68	±20%	0.1V1M	0.036	3.00	3.30
HXNR252012NF-1R0M	1.0	±20%	0.1V1M	0.049	2.70	2.60
HXNR252012NF-1R5M	1.5	±20%	0.1V1M	0.063	2.30	2.20
HXNR252012NF-2R2M	2.2	±20%	0.1V1M	0.080	2.15	1.85
HXNR252012NF-3R3M	3.3	±20%	0.1V1M	0.120	1.70	1.45
HXNR252012NF-4R7M	4.7	±20%	0.1V1M	0.176	1.50	1.20
HXNR252012NF-6R8M	6.8	±20%	0.1V1M	0.250	1.15	1.00
HXNR252012NF-100M	10	±20%	0.1V1M	0.410	0.85	0.75
HXNR252012NF-150M	15	±20%	0.1V1M	0.540	0.63	0.60
HXNR252012NF-220M	22	±20%	0.1V1M	0.850	0.56	0.50

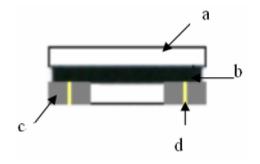
Note:

Isat : Based on inductance change $\ (\triangle L/L0 : \le -30\%) \ @$ ambient temp. 25°C

Irms : Based on temperature rise $\ (\triangle T:40\% \ typ.)$

5. Material List

No.	Description	Specification
a.	Core	Ferrite N4 Core
b.	Coating	Ероху
С	Termination	Tin Pb Free
d	Wire	Enameled Copper Wire



6. Reliability and Test Condition

Item	Performance	Test Condition
Operating Temperature	- 25 ~ +120℃.	Including self-generated heat
Storage Temperature	- 40 ~ +85°C. - 5 to 40°C for the product with taping.	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 1V
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change : Within±20%	Measurement of inductance shall be taken at temperature rang within − 25°C to +85°C. With reference to inductance value at+20°C,change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within − 40°C to +125°C. With reference to inductance value at+20°C,change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow. As illustrated below, apply force in the direction of the arrow indicating until deflection of the test board reaches to 2mm. Process Testing 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 10 20 1
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force: 10 N to X and Y directions. Duration: 5s Solder cream thickness: 0.10
Resistance to Vibration	Inductance change: Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.

ISND				F	94
Solderability	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall be dipped in flux, and then immerse in molten solder as shown in below. Flux: methanol solution containing rosin 25% Solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.			
Resistance to soldering	Inductance change: Within±10% No abnormality observed in appearance.	The test sample shall be exposed to reflow oven at 230 for 40 seconds, with peak temperature at 260±5℃ for seconds,2 times. Test board thickness: 1.0mm Test board material: glass epoxy-resin			
Item	Performance		Test Condition	on	
Thermal shock		reflow. The test samp for specified to sequence.	oles shall be soldered to oles shall be placed at time by step 1 to step 4 ure cycles shall be reported. Temperature("C") -40±3"C" Room Temp 85±2"C Room Temp	specified temperat as shown below ir	ture n
Damp heat life test		Test Method and Remarks The test samples shall be soldered to the test board by the reflow. The test samples shall be placed in thermostatic oven set at specified temperature and humidity as shown in below. Temperature: 6042°C Humidity: 90–95%RH Time: 500+24/-0 hrs			
Loading under damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by reflow. The test samples shall be placed in thermostatic oven specified temperature and humidity and applied the rat current continuously as shown in below. Temperature: 60±2°C Humidity: 90-95%RH Applied current: Rated current Time: 500+24/-0 hrs			
Low temperature life test		The test samples shall be soldered to the test board by the reflow. After that, the test samples shall be placed at test conditions as shown in below. Temperature:-40±2°C Time:500+24/-0 hrs			
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	o the test board by	/ the

7. Soldering

7-1. Soldering

7-2. Recommended PC Board Pattern

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems.

If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-1.1 Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

7-1.2 Soldering Iron(Figure 2):

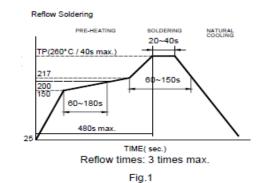
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

• Preheat circuit and products to 150 °C • Never contact the ceramic with the iron tip • Use a 20 watt soldering iron with tip diameter of 1.0mm

355°C tip temperature (max)

• 1.0mm tip diameter (max)

Limit soldering time to 4~5 sec.



7-2. Recommended PC Board Pattern

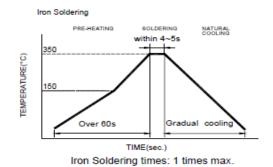
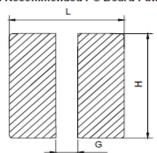


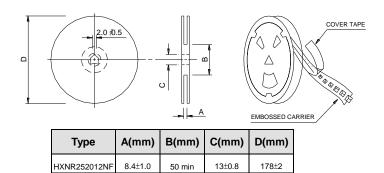
Fig.2



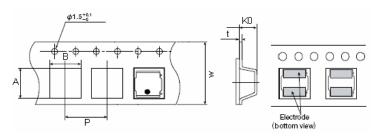
L(mm)	G(mm)	H(mm)
2.9	0.8	2.4

8. Packaging Information

(1) Reel Dimension



(2) Tape Dimension

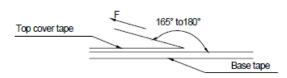


Туре	A(mm)	B(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HXNR252012NF	3.1±0.1	3.1±0.1	1.6±0.1	4.0±0.1	8.0±0.2	0.23±0.05

(3) Packaging Quantity

Туре	Chip / Reel
HXNR252012NF	2000

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

- Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. ISND products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价,库存,交付和生命周期等信息

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