# General Purpose Transistor NPN Silicon

These transistors are designed for general purpose amplifier applications. They are housed in the SOT-323/SC-70 package which is designed for low power surface mount applications.

### Features

- AEC-Q101 Qualified and PPAP Capable
- S Prefix for Automotive and Other Applications Requiring Unique Site and Control Change Requirements
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant\*

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector – Emitter Voltage	V <sub>CEO</sub>	40	Vdc
Collector – Base Voltage	V <sub>CBO</sub>	75	Vdc
Emitter-Base Voltage	V <sub>EBO</sub>	6.0	Vdc
Collector Current – Continuous	Ι <sub>C</sub>	600	mAdc

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Total Device Dissipation FR-5 Board $T_A = 25^{\circ}C$	PD	150	mW
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Junction and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

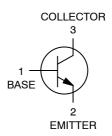


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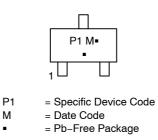
http://onsemi.com



CASE 419 STYLE 3



### MARKING DIAGRAM



(Note: Microdot may be in either location)

### **ORDERING INFORMATION**

Device	Package	Shipping <sup>†</sup>
MMBT2222AWT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel
SMMBT2222AWT1G	SC-70 (Pb-Free)	3,000 / Tape & Reel

+For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

\*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

Charact	Characteristic			Max	Unit
OFF CHARACTERISTICS					
Collector – Emitter Breakdown Voltage (No $(I_C = 1.0 \text{ mAdc}, I_B = 0)$	V <sub>(BR)CEO</sub>	40	-	Vdc	
Collector – Base Breakdown Voltage (I <sub>C</sub> = 10 μAdc, I <sub>E</sub> = 0)	V <sub>(BR)CBO</sub>	75	-	Vdc	
Emitter – Base Breakdown Voltage $(I_E = 10 \ \mu Adc, I_C = 0)$	V <sub>(BR)EBO</sub>	6.0	-	Vdc	
Base Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>BL</sub>	-	20	nAdc	
Collector Cutoff Current (V <sub>CE</sub> = 60 Vdc, V <sub>EB</sub> = 3.0 Vdc)	I <sub>CEX</sub>	_	10	nAdc	
ON CHARACTERISTICS (Note 1)					
DC Current Gain (Note 1) ( $I_C = 0.1 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$ ) ( $I_C = 1.0 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$ ) ( $I_C = 10 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$ ) ( $I_C = 150 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$ ) ( $I_C = 500 \text{ mAdc}, V_{CE} = 10 \text{ Vdc}$ )			35 50 75 100 40	- - 300 -	-
Collector – Emitter Saturation Voltage (Not ( $I_C = 150$ mAdc, $I_B = 15$ mAdc) ( $I_C = 500$ mAdc, $I_B = 50$ mAdc)	V <sub>CE(sat)</sub>	- -	0.3 1.0	Vdc	
Base – Emitter Saturation Voltage (Note 1) ( $I_C = 150$ mAdc, $I_B = 15$ mAdc) ( $I_C = 500$ mAdc, $I_B = 50$ mAdc)	V <sub>BE(sat)</sub>	0.6	1.2 2.0	Vdc	
SMALL-SIGNAL CHARACTERISTICS					
Current – Gain – Bandwidth Product (I <sub>C</sub> = 20 mAdc, V <sub>CE</sub> = 20 Vdc, f = 100 M	Hz)	f <sub>T</sub>	300	-	MHz
Output Capacitance (V <sub>CB</sub> = 10 Vdc, I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	_	8.0	pF	
Input Capacitance (V <sub>EB</sub> = 0.5 Vdc, I <sub>C</sub> = 0, f = 1.0 MHz)		C <sub>ibo</sub>	-	30	pF
Input Impedance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kH	h <sub>ie</sub>	0.25	1.25	kΩ	
Voltage Feedback Ratio (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kHz)			-	4.0	X 10 <sup>-4</sup>
Small – Signal Current Gain (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kHz)			75	375	-
Output Admittance (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 10 mAdc, f = 1.0 kHz)			25	200	μmhos
Noise Figure (V <sub>CE</sub> = 10 Vdc, I <sub>C</sub> = 100 $\mu$ Adc, R <sub>S</sub> = 1.0 kΩ, f = 1.0 kHz)			-	4.0	dB
SWITCHING CHARACTERISTICS					
Delay Time	(V <sub>CC</sub> = 3.0 Vdc, V <sub>BE</sub> = -0.5 Vdc,	t <sub>d</sub>	-	10	
Rise Time	$I_{\rm C} = 150 \text{ mAdc}, I_{\rm B1} = 15 \text{ mAdc})$	t <sub>r</sub>	-	25	ns
Storage Time	(V <sub>CC</sub> = 30 Vdc, I <sub>C</sub> = 150 mAdc,	t <sub>s</sub>	_	225	
Fall Time	$I_{B1} = I_{B2} = 15 \text{ mAdc}$	t <sub>f</sub>	-	60	ns

1. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

### SWITCHING TIME EQUIVALENT TEST CIRCUITS

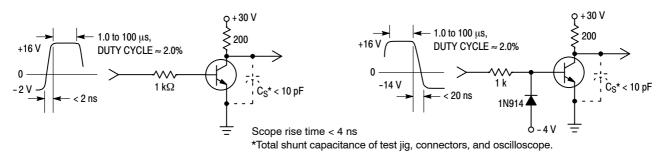
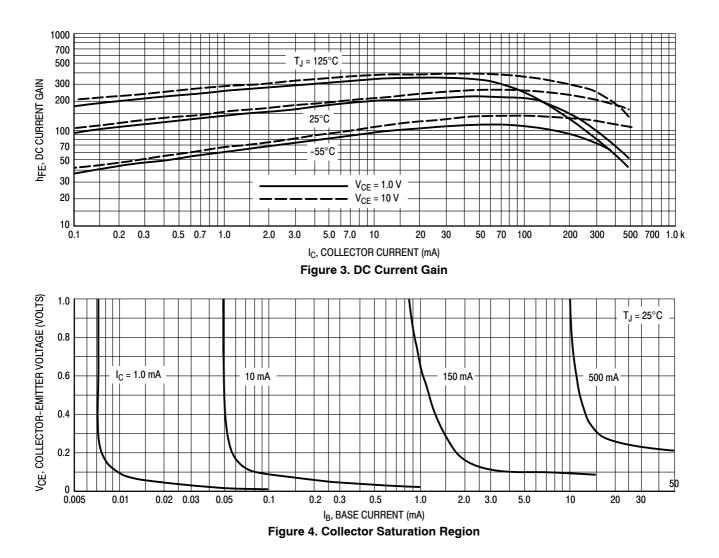




Figure 2. Turn-Off Time



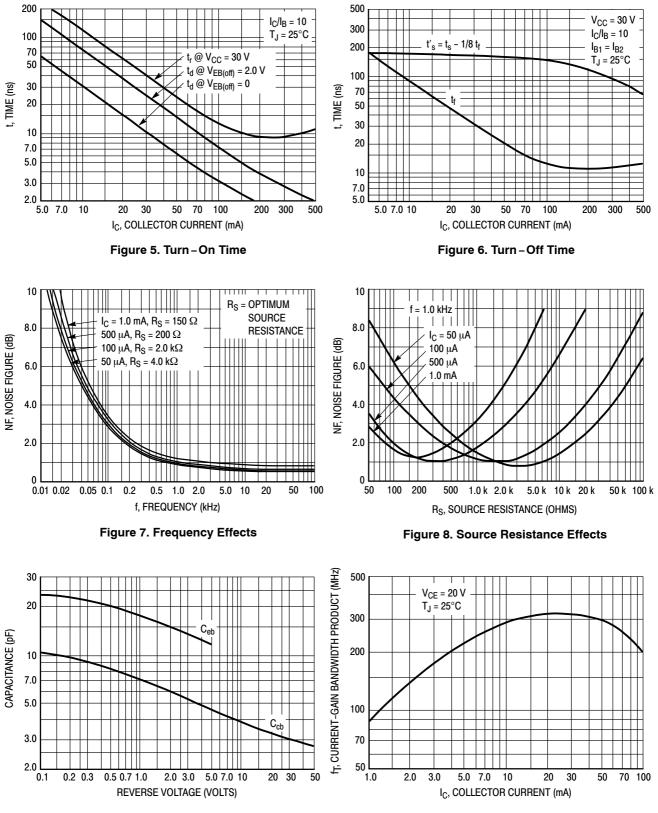
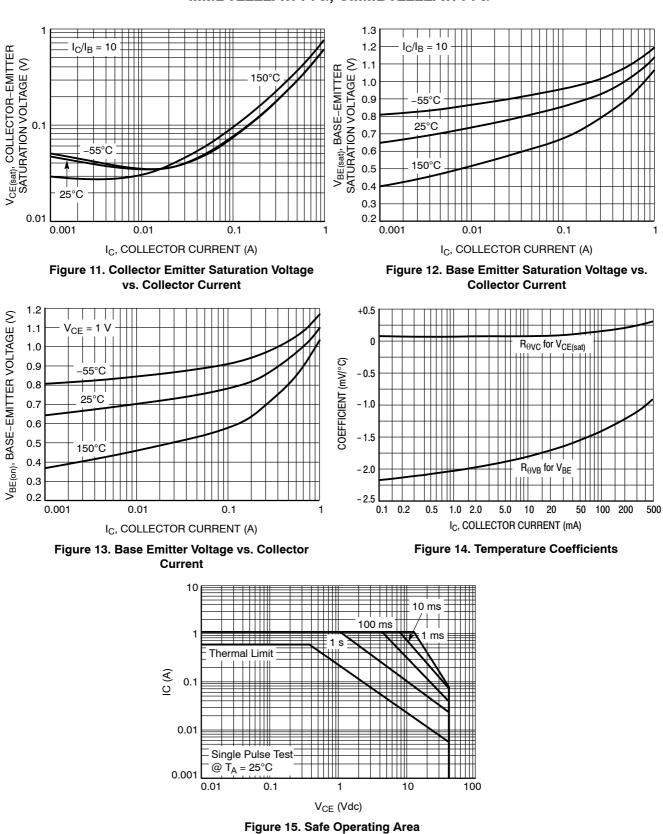


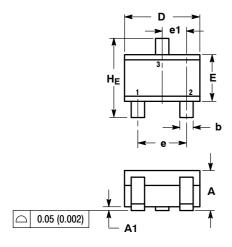
Figure 9. Capacitances

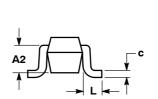
Figure 10. Current–Gain Bandwidth Product



### PACKAGE DIMENSIONS

SC-70 (SOT-323) CASE 419-04 ISSUE N





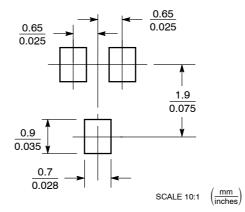
NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	MILLIMETERS			INCHES		
DIM	MIN	NOM	MAX	MIN	NOM	MAX
Α	0.80	0.90	1.00	0.032	0.035	0.040
A1	0.00	0.05	0.10	0.000	0.002	0.004
A2		0.70 REF 0.028 REF			-	
b	0.30	0.35	0.40	0.012	0.014	0.016
c	0.10	0.18	0.25	0.004	0.007	0.010
D	1.80	2.10	2.20	0.071	0.083	0.087
Е	1.15	1.24	1.35	0.045	0.049	0.053
e	1.20	1.30	1.40	0.047	0.051	0.055
e1	0.65 BSC			0.026 BSC		
L	0.20	0.38	0.56	0.008	0.015	0.022
HE	2.00	2.10	2.40	0.079	0.083	0.095



### SOLDERING FOOTPRINT\*



\*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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