

Spe	ecification	<b>for A</b>	oprov	val
	Customer :			
	TAI-TECH P/N: HPC602	20B-SERIES		
	CUSTOMER P/N:			
	DESCRIPTION:			
	QUANTITY:	pcs		
R	EMARK:			
	Customer App	oroval Feedback		
	西 北 臺 慶 科 技 <u>TAI-TECH Advance</u>			
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**Power Inductor** 

## HPC6020B-SERIES

	ECN HISTORY LIST								
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN				
1.0	12/07/03	新發行	楊祥忠	詹偉特	蕭羽恬				
備									
註									

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# **Power Inductor**

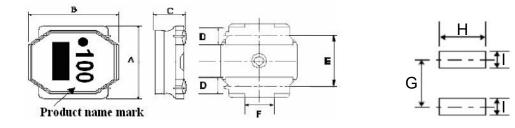
### HPC6020B-SERIES

### 1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

## 2. Dimension





Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)	H(mm)	l(mm)
HPC6020B	6.0±0.2	6.0±0.2	2.0 max.	1.35±0.2	4.0±0.2	2.3 typ.	4.7 ref.	5.7 ref.	1.6 ref.
									Linito: mm

Units: mm

## 3. Part Numbering

HPC	<mark>6020</mark>	В	-	<b>100</b>	Μ
А	В	С		D	Е
A: Series					
B: Dimension					
C: Control S/N					
D: Inductance		100=	=10.0ul	Н	
E: Inductance To	lerance	M=±	20%;	Y=±30%	

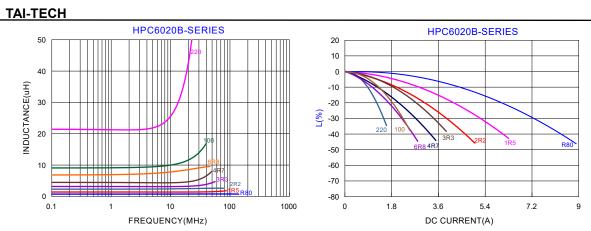
## 4. Specification

TAI-TECH Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	SRF (MHz) min.	DCR (Ω) ±20%	l sat (A)	l rms (A)
HPC6020B-R80Y	0.8	±30%	1V100K	110	0.020	6.40	4.10
HPC6020B-1R5Y	1.5	±30%	1V100K	93	0.026	4.30	3.60
HPC6020B-2R2Y	2.2	±30%	1V100K	73	0.034	3.20	2.90
HPC6020B-3R3Y	3.3	±30%	1V100K	55	0.040	2.80	2.75
HPC6020B-4R7Y	4.7	±30%	1V100K	43	0.058	2.40	2.15
HPC6020B-6R8Y	6.8	±30%	1V100K	30	0.085	2.00	1.80
HPC6020B-100M	10	±20%	1V100K	18	0.125	1.90	1.50
HPC6020B-220M	22	±20%	1V100K	11	0.290	1.25	0.95

Note:

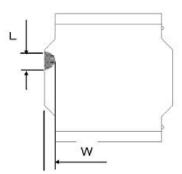
lsat: Based on inductance change  $(\triangle L/L0: \leq -30\%)$  @ ambient temp. 25°C

Irms: Based on temperature rise ( $\triangle T$ : 40°C typ.)



Core chipping

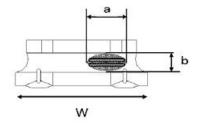
The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension.



Туре	L	w
HPC6020B	1.5mm Max.	1.5mm Max.

Void appearance tolerance Limit

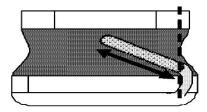
Size of voids occurring to coating resin is specified below.



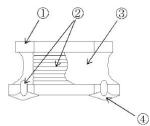
Exposed wire tolerance limit of coating resin part on product side. Size of exposed wire occurring to coating resin is specified below. 1. Width direction (dimension a): Acceptable when  $a \le w/2$ 

- Width direction (dimension a): Acceptable when a≤w/2 Nonconforming when a>w/2
- 2. Length direction (dimension b): Dimension b is not specified.
- 3. When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

External appearance criterion for exposed wire Exposed end of the winding wire at the secondary side should be 3mm and below.



# 5. Material List



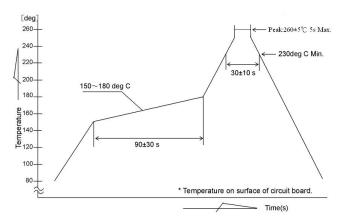
No.	Item	Material
1	Core	Ni-Zn ferrite
2	Wire	Copper Wire
3	Coating	Ероху
4	Solder	Lead free

# 6. Reliability and Test Condition

ltem	Performance	Test Method and Remarks			
Operating Temperature	- 25 ~ +125°C.	Including self-generated heat			
Storage Temperature	-40 ~ +85℃. - 5 to 40℃ for the product with taping.				
Rated current					
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 1V			
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent			
Temperature characteristics	Inductance change: Within±20%	Measurement of inductance shall be taken at temperature rang within–25°C to +85°C. With reference to inductance value at+20 °C,change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within–40°C to +125°C. With reference to inductance value at+20 °C,change rate shall be calculated.			
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow. As illustrated below, apply force in the direction of the arrow indicating until deflection of the test board reaches to 2mm. Proceeding and			
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force: 10 N to X and Y directions. Duration: 5s Solder cream thickness: 0.15			
Resistance to Vibration	Inductance change: Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method: 10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.			
Solderability	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall be dipped in flux, and then immersed in molten solder as shown in below. Flux: methanol solution containing rosin 25% Solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.			
Resistance to soldering	Inductance change: Within±10% No abnormality observed in appearance.	The test sample shall be exposed to reflow oven at $230\pm5^{\circ}$ for 40 seconds, with peak temperature at $260\pm5^{\circ}$ for 5 seconds,2 times. Test board thickness: 1.0mm Test board material: glass epoxy-resin			

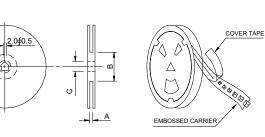
Item	Performance		Test Method and R	emarks
Thermal shock		reflow. The test samp for specified t sequence.	ples shall be soldered to ples shall be placed at ime by step 1 to step 4 ure cycles shall be rep Temperature(C) -40±3°C Room Temp 85±2°C Room Temp	specified temperature as shown below in
Damp heat life test		Test Method a soldered to the The test same	and Remarks The test te test board by the refi ples shall be placed in perature and humidity 60±2°C -95%RH	samples shall be low. thermostatic oven set
Loading under damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	reflow. The test sam specified tem current contin Temperature: Humidity: 90~	-95%RH nt: Rated current	thermostatic oven set and applied the rated
Low temperature life test		reflow.	-40±2℃	
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	to the test board by th

# 7. Soldering



## 8. Packaging Information

#### (1) Reel Dimension



Туре	A(mm) B(m		C(mm)	D(mm)	
HPC6020B	13.5±1.0	80±2.0	13±0.5	330±3.0	

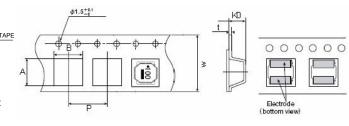
#### (3) Packaging Quantity

Туре	Chip / Reel
HPC6020B	2500

#### Application Notice

- · Storage Conditions
  - To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40  $^\circ\!\!\mathbb{C}$  and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

#### (2) Tape Dimension



Туре	A(mm)	B(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HPC6020B	6.3±0.1	6.3±0.1	2.3±0.1	8.0±0.1	12±0.3	0.4±0.1



单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)

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