

Specification for Approval

Date: 2012/07/03

	<u>Custor</u>	mer :								
	TAI-TECH P/N:	н	PC604	5B-S	ERIE	S				
	CUSTOMER P/N	l:								
	DESCRIPTION:									
	QUANTITY:				pcs	<u>-</u>				
REM	MARK:									
	C	Custon	ner App	roval F	eedba	ack				
	西北	臺 慶	科技	股化	 分 有	限	公	司		

TAI-TECH Advanced Electronics Co., Ltd

■ 西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd Headquarter:

NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI,

TAO-YUAN HSIEN, TAIWAN, R.O.C.

TEL: +886-3-4641148 FAX: +886-3-4643565 http://www.tai-tech.com.tw E-mail: sales@tai-tech.com.tw

□ 東莞臺慶精密電子有限公司

DONGGUAN TAI-TECHADVANCED ELECTRONICS CO., LTD JITIGANG MANAGEMENT DISTRICT, HUANGJIANG, DONGGUAN, **GUANGDONG, CHINA**

TEL: +86-769-3365488 FAX: +86-769-3366896

E-mail: sales@tai-tech.net

Office:

金亨國際有限公司

KAMHENG INTERNATIONAL LIMITED

TEL: +86-852-25772033 FAX: +86-852-28817778

□ 臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA

TEL: +86-512-57619396 FAX: +86-512-57619688

E-mail: sales@tai-tech.cn

Office:

北欣國際有限公司

NORTH STAR INTERNATIONAL LIMITED TEL: +86-512-57619396 FAX: +86-512-57619688

Sales Dep.

APPROVED	CHECKED
楊登欽	殷婉媚
Peter Yang	Amy Yin

R&D Center

APPROVED	CHECKED	DRAWN
楊祥忠	詹偉特	蕭羽恬
Mikey Yang	Jack Chan	Yu-tian Siao

Power Inductor

HPC6045B-SERIES

	ECN HISTORY LIST						
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN		
1.0	12/07/03	新 發 行	楊祥忠	詹偉特	蕭羽恬		
備							
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TAI-TECH

Power Inductor

HPC6045B-SERIES

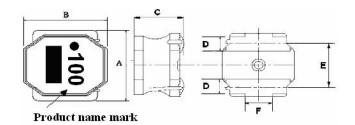
1. Features

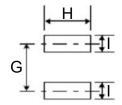
- 1. This specification applies Low Profile Power Inductors.

2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.



2. Dimension





Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)	H(mm)	l(mm)
HPC6045B	6.0±0.2	6.0±0.2	4.5max.	1.35±0.2	4.0±0.2	2.3 typ.	4.7 ref.	5.7 ref.	1.6 ref.

Units: mm

3. Part Numbering



A: Series

B: Dimension

C: Control S/N

D: Inductance 100=10.0uH E: Inductance Tolerance M=±20%; Y=±30%

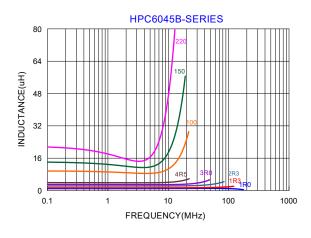
4. Specification

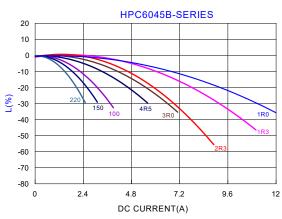
TAI-TECH Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	SRF (MHz) min.	DCR (Ω) ±30%	l sat (A)	I rms (A)
HPC6045B-1R0Y	1.0	±30%	1V100K	110	0.014	9.80	4.50
HPC6045B-1R3Y	1.3	±30%	1V100K	95	0.016	8.20	4.20
HPC6045B-2R3Y	2.3	±30%	1V100K	60	0.022	6.40	3.60
HPC6045B-3R0Y	3.0	±30%	1V100K	45	0.024	5.60	3.30
HPC6045B-4R5M	4.5	±20%	1V100K	25	0.030	4.40	3.10
HPC6045B-100M	10	±20%	1V100K	12	0.046	3.10	2.40
HPC6045B-150M	15	±20%	1V100K	10	0.070	2.50	1.90
HPC6045B-220M	22	±20%	1V100K	7	0.107	2.00	1.60

Note:

Isat: Based on inductance change (\triangle L/L0: \le -30%) @ ambient temp. 25°C

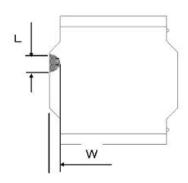
Irms: Based on temperature rise $(\triangle T: 40^{\circ}C \text{ typ.})$





Core chipping

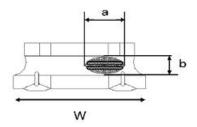
The appearance standard of the chipping size in top side, of bottom side ferrite core is following dimension.



Туре	L	w
HPC6045B	1.5mm Max.	1.5mm Max.

Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.



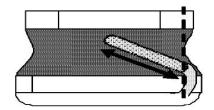
Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below.

- Width direction (dimension a): Acceptable when a ≤ w/2
 Nonconforming when a > w/2
- 2. Length direction (dimension b): Dimension b is not specified.
- 3. When total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, that is acceptable.

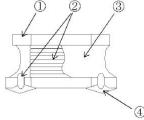
External appearance criterion for exposed wire

Exposed end of the winding wire at the secondary side should be 3mm and below.



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5. Material List



No.	Item	Material		
1	Core	Ni-Zn ferrite		
2	Wire	Copper Wire		
3	Coating	Ероху		
4	Solder	Lead free		

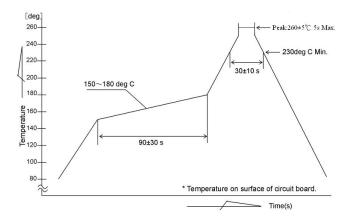
6. Reliability and Test Condition

Item	Performance	Test Method and Remarks
Operating Temperature	- 25 ~ +125℃.	Including self-generated heat
Storage Temperature	-40 ~ +85℃. - 5 to 40℃ for the product with taping.	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 1V
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change: Within±20%	Measurement of inductance shall be taken at temperature rang within–25° to +85°C. With reference to inductance value at+20°C, change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within–40°C to +125°C. With reference to inductance value at+20°C, change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board be the reflow. As illustrated below, apply force in the direction of the arror indicating until deflection of the test board reaches to 2mm. Porce Proces Porce Proces Sample Sample Sample Sample Sample Sample Substrate size: 100x40x1.0 Substrate material: glass epoxy-resin Solder cream thickness: 0.15
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force: 10 N to X and Y directions. Duration: 5s
Resistance to Vibration	Inductance change: Within±10% No abnormality observed in appearance.	Solder cream thickness: 0.15 The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.
Solderability	At least 90% of surface of terminal electrode is covered by new solde	The test samples shall be dipped in flux, and then immersed in molten solder as shown in below. Flux: methanol solution containing rosin 25% solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.
Resistance to soldering	Inductance change: Within±10% No abnormality observed in appearance.	The test sample shall be exposed to reflow oven at 230±5°C for 40 seconds, with peak temperature at 260±5°C for 5 seconds,2 times. Test board thickness: 1.0mm Test board material: glass epoxy-resin

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Item	Performance		Test Method and R	emarks	
Thermal shock		reflow. The test samp for specified to sequence.	bles shall be soldered to bles shall be placed at time by step 1 to step 4 ure cycles shall be rep Temperature(C) -40±3*C Room Temp 85±2*C Room Temp	specified temperal	ture n
Damp heat life test		soldered to th The test samp	95%RH	low. thermostatic oven	
Loading under damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	reflow. The test samp specified temp current contin Temperature: Humidity: 90~	95%RH nt: Rated current	thermostatic oven and applied the rat	set at
Low temperature life test		reflow.	-40±2℃		
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	to the test board by	/ the

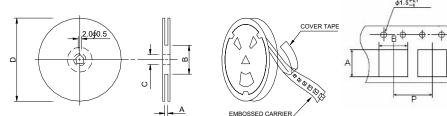
7. Soldering



(2) Tape Dimension

8. Packaging Information

(1) Reel Dimension



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PE B + + - +		000000
A])		
P		Electrode (bottom view)

Туре	A(mm)	B(mm)	C(mm)	D(mm)
HPC6045B	13.5±1.0	80±2.0	13±0.5	330±3.0

Туре	A(mm)	B(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HPC6045B	6.3±0.1	6.3±0.1	4.7±0.1	8.0±0.1	12±0.3	0.4±0.1

(3) Packaging Quantity

Туре	Chip / Reel		
HPC6045B	1500		

Application Notice

- Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价,库存,交付和生命周期等信息

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