

Specification for Approval

Date: 2019/03/21

Customer:深圳台慶

	TAI-TECH P/N:	UHP252012BV-SE	RIES		
	CUSTOMER P/N:				
	DESCRIPTION:				
	QUANTITY:	pcs	<u>; </u>		
REM	MARK:				
	Cu	stomer Approval Feedba	ack		

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Power Inductor

UHP252012BV-SERIES

		ECN HISTORY LIS	ST		
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	19/03/21	新 發 行	羅宜春	梁周虎	張麗麗
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Power Inductor

UHP252012BV-SERIES

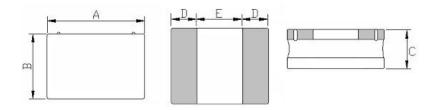
1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 3. High reliability -Reliability tests comply with AEC-Q200
- 4. Operating temperature: -55~+125 $^{\circ}$ C (Including self temperature rise)



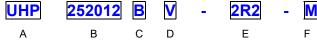


2. Dimension



Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
UHP252012BV	2.5 -0.1/+0.2	2.0 -0.1/+0.2	1.2 max.	0.85 ref.	0.80 ref.

3. Part Numbering



A: Series

B: Dimension

C: Lead Free

 $\begin{array}{lll} \mbox{D:Cate gory Code} & \mbox{V=Vehicle} \\ \mbox{E: Inductance} & 2\mbox{R2=2.2uH} \\ \mbox{F: Inductance Tolerance} & \mbox{M=$\pm 20\%} \\ \end{array}$

4. Specification

TAI-TECH Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (Ω) ±20%	I sat (A) typ.	I sat (A) Max.	I rms (A) typ	I rms (A) Max.
UHP252012BV-R47Y	0.47	±30%	0.1V/1M	0.028	4.00	3.60	3.70	3.35
UHP252012BV-R68M	0.68	±20%	0.1V/1M	0.036	3.00	2.70	3.30	3.00
UHP252012BV-1R0Y	1.0	$\pm 30\%$	0.1V/1M	0.049	2.70	2.45	2.60	2.30
UHP252012BV-1R5Y	1.5	±30%	0.1V/1M	0.063	2.30	2.05	2.20	1.95
UHP252012BV-2R2M	2.2	±20%	0.1V/1M	0.080	2.15	1.95	1.85	1.65
UHP252012BV-3R3M	3.3	±20%	0.1V/1M	0.120	1.70	1.50	1.45	1.30
UHP252012BV-4R7M	4.7	±20%	0.1V/1M	0.176	1.50	1.35	1.20	1.05
UHP252012BV-6R8M	6.8	±20%	0.1V/1M	0.250	1.15	1.00	1.00	0.90
UHP252012BV-100M	10	±20%	0.1V/1M	0.410	0.85	0.75	0.75	0.65
UHP252012BV-150M	15	±20%	0.1V/1M	0.540	0.63	0.56	0.60	0.54
UHP252012BV-220M	22	±20%	0.1V/1M	0.850	0.56	0.50	0.50	0.45

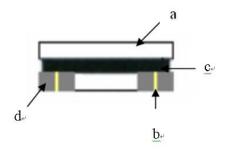
Note:

Isat: Saturation Current (Isat) will cause L0 to drop approximately 30%

Irms: Heat Rated Current (Irms) will cause the coil temperature rise approximately $~^{\Delta}40\,^{\circ}\!\mathrm{C}$

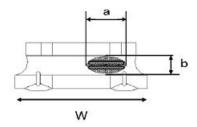
5. Material List

No.	Description	Specification
a.	Core	Ferrite Core
b.	Wire	Enameled Copper Wire
С	Glue	Epoxy with magnetic powder
d	Terminal	Ag/Ni/Sn



Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.

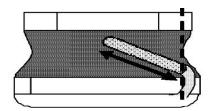


Appearance of exposed wire tolerance limit :

- 1. Width direction (dimension a): Acceptable when $a \le w/2$ Nonconforming when a > w/2
- 2. Length direction (dimension b): Dimension b is not specified.
- 3. The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.

External appearance criterion for exposed wire

Exposed end of the winding wire at the secondary side should be 2mm and below.



6. Reliability and Test Condition

Item	Performance	Test Condition					
Operating temperature	-55~+155 [°] C(Including self - temperature rise)						
Storage temperature and Humidity range	110~+40 °C,50~60%RH (Product with taping) 255~+155 °C (on board)						
Electrical Performance Test	lectrical Performance Test						
Inductance	Defects standard electrical characteristics list	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.					
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.					
Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0 to drop △L(%)					
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise \triangle T(\mathbb{C}). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer					
Reliability Test		and the second of the second o					
High Temperature Exposure(Storage) AEC-Q200		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Temperature: 155±2°C (Inductor) Duration: 1000hrs Min. Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles					
Temperature Cycling AEC-Q200		Condition for 1 cycle Step1: -55±2°C 30min Min.(Inductor) Step2: 155±2°C 17 30min Min. Step3: 155±2°C 30min Min. Step4: Low temp. transition time 1 min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs					
Moisture Resistance	Appearance: No damage. Impedance: within \pm 15% of initial value Inductance: within \pm 10% of initial value Q: Shall not exceed the specification value. RDC: within \pm 15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles 1.Baked at50 $^{\circ}\mathrm{C}$ for 25hrs, measured at room temperature after placing for 4 hrs. 2.Raise temperature to $65\pm2^{\circ}\mathrm{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}\mathrm{C}$ in 2.5hrs. 3.Raise temperature to $65\pm2^{\circ}\mathrm{C}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}\mathrm{C}$ in 2.5hrs,keep at $25^{\circ}\mathrm{C}$ for 2hrs then keep at $-10^{\circ}\mathrm{C}$ for 3hrs 4.Keep at $25^{\circ}\mathrm{C}$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for $1{\sim}2$ hrs.					
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity: 85±3%R.H, Temperature: 85°C±2°C Duration: 1000hrs Min Measured at room temperature after placing for24±2hrs					
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Temperature: 155±2°C (Inductor) Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for24±2hrs					
External Visual	Appearance: No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.					
Physical Dimension According to the product specification size measurement		According to the product specification size measurement					
Resistance to Solvents	Appearance: No damage.	Add aqueous wash chemical - OKEM clean or equivalent.					
Mechanical Shock		SMD 100 6 Half-sine 12.3					

Item	Performance	Test Condition		
Vibration		IPC/JEDEC J-STD-020DClassification Reflow Profiles Oscillation Frequency: 10~2K~10Hz for 20 minute Equipment: Wibration checker Total Amplitude:1.52mm±10% Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations).		
Resistance to Soldering Heat	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the	Test condition: Temperature(°C) Time(s) Temperature ramp/immersion and emersion rate 260±5(solder temp) 10±1 25mm/s ±6 mm/s 1		
Thermal shock (AEC-Q200)	specification value	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -55±2°C 15±1min(Inductor) Step2: 155±2°C vithin 20Sec. Step3: 155±2°C 15±1min Number of cycles: 300 Measured at room fempraturc after placing fo24±2hrs		
ESD	Appearance: No damage.	lp 600s t		
Solderability	More than 95% of the terminal electrode should be covered with solder.	a. Method B, 4 hrs @155°C dry heat @235°C±5°C b. Method B @ 215°C±5°C category 3.(8hours ± 15 min) c. Method D category 3. (8hours ± 15 min)@ 260°C±°C Preheat: 150°C,60sec. Solder: Sn96.5% Ag3% Cu0. 5% Temperature: 245±5°C。 Flux for lead free: Rosin. 9.5%。 Dip time: 4±1sec. Depth: completely cover the termination		
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .		
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.		
Board Flex	Appearance: No damage	Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed circuit board before testing Probe to exert bending force Radius 340		
J-STD-020D Classification Reflow Profiles With the component mounted on a PCB with the tested, apply a 17.7 N (1.8 Kg) force to the side tested. This force shall be applied for 60 +1 se		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020D Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. **Tadius 0,5 mm** DUT wide thickness		

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

7. Soldering and Mounting

7-1. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

7-1.1 Solder re-flow:

Reflow Soldering

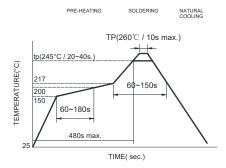
Recommended temperature profiles for re-flow soldering in Figure 1.

7-1.2 Soldering Iron(Figure 2):

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

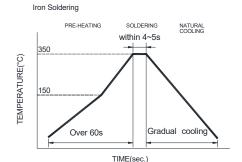
- · Never contact the ceramic with the iron tip · Preheat circuit and products to 150℃ · Use a 20 watt soldering iron with tip diameter of 1.0mm
- · 355℃ tip temperature (max) · 1.0mm tip diameter (max)

· Limit soldering time to 4~5 sec.



Reflow times: 3 times max.

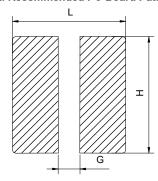
Fig.1



Iron Soldering times: 1 times max.

Fig.2

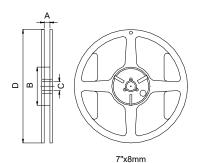
7-2. Recommended PC Board Pattern



L(mm)	G(mm)	H(mm)
2.6	0.8	2.1

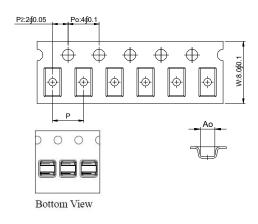
8. Packaging Information

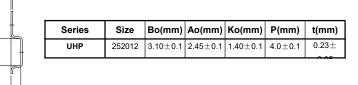
8-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	8.4±1.0	50 min.	13±0.8	178±2

8-2. Tape Dimension / 8mm

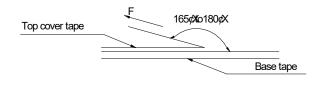




8-3. Packaging Quantity

Chip size	252012
Chip / Reel	2000

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

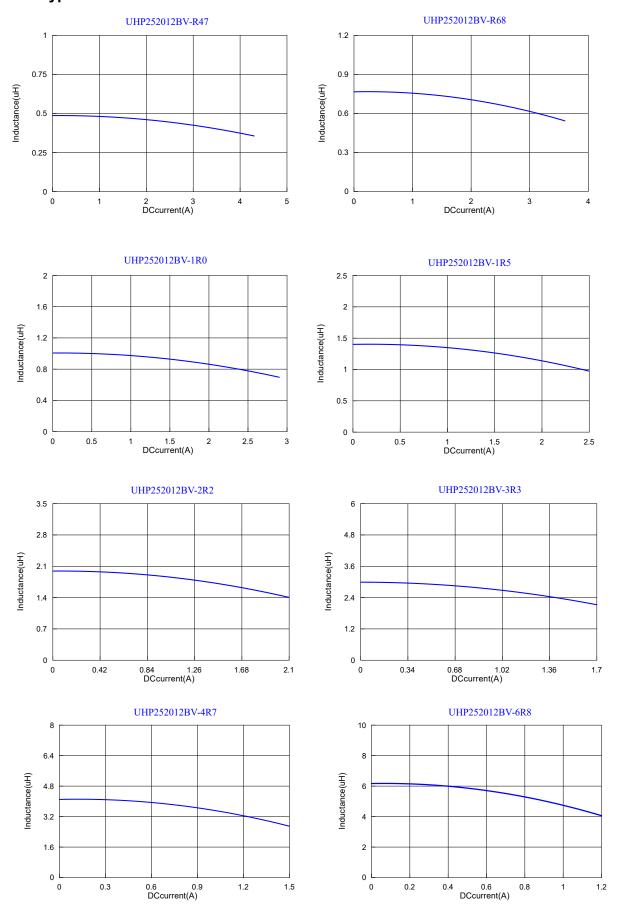
Room Temp. (℃)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

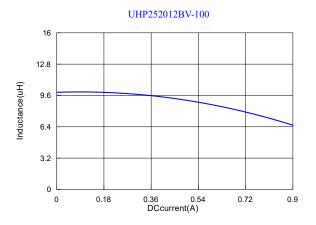
Application Notice

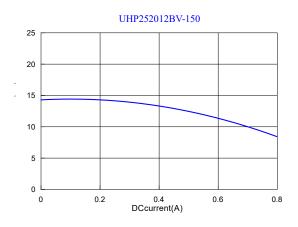
- · Storage Conditions(component level)
 - To maintain the solderability of terminal electrodes:
 - 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.

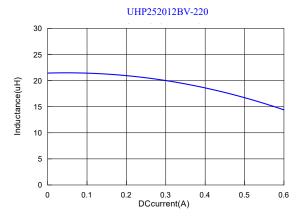
 - 3. Recommended products should be used within 12 months form the time of delivery.
 - 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- · Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

9. Typical Performance Curves









单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)