

Data sheet

SAW Tx filter
M2M
LTE Band 87

Part number: B8705

Ordering code: B39411B8705P810

Date: September 09, 2021

Version: 2.0

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Please read **Cautions and warnings** and **Important notes** at the end of this document.

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1 Application

- Low-loss SAW Tx filter for LTE band 87 application
- Usable pass band 5 MHz
- Tx = Uplink = 410 415 MHz

2 Features

- Package size 2.5±0.1 mm × 2.0±0.1 mm
- Package height 0.53 mm (max.)
- Approximate weight 9 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 3 (MSL3)



Figure 1: Picture of component with example of product marking.

Pin configuration

3

1, 2, 4, 5,

6, 7, 9

8

Output

Ground

Input

3 Package

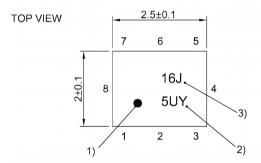
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BOTTOM VIEW (0.1) (0.

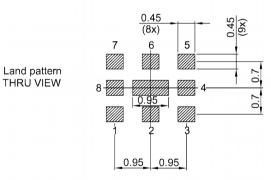
Pad and pitch tolerance ±0.05

SIDE VIEW





- 1) Marking for pad number 1
- 2) Example of encoded lot number
- 3) Example of encoded filter type number



Landing pad tolerance -0.02

Figure 2: Drawing of package with package height A = 0.53 mm (max.). See Sec. Package information (p. 18).



5 Matching circuit

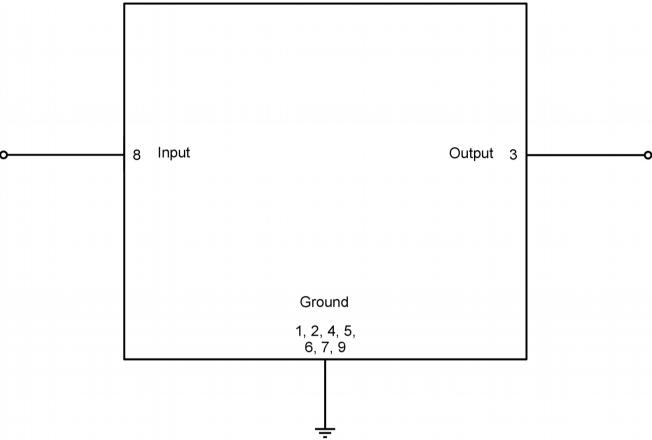


Figure 3: Schematic of matching circuit. No external matching components required.



6 Characteristics

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Temperature range for specification $T_{\text{SPEC}} = -30 \,^{\circ}\text{C} \dots +85 \,^{\circ}\text{C}$

Input terminating impedance $Z_{\rm IN} = 50~\Omega$ Output terminating impedance $Z_{\rm OUT} = 50~\Omega$

Characteristics				$\begin{array}{c} \text{min.} \\ \text{for } T_{\text{SPEC}} \end{array}$	typ. @ +25 °C	$\begin{array}{c} \text{max.} \\ \text{for } T_{\text{\tiny SPEC}} \end{array}$	
Center frequency			f _C	_	412.5	_	MHz
Maximum insertion attenuation			$\boldsymbol{\alpha}_{\text{max}}$				
	410 415	MHz		_	1.5	2.5	dB
Amplitude ripple (p-p)			Δα				
	410 415	MHz		_	0.8	1.8	dB
Maximum group delay			$ au_{ ext{max}}$				
	410 415	MHz		_	130	180	ns
Group delay ripple			$\Delta au_{ m var}$				
	410 415	MHz		_	50	110	ns
Maximum VSWR			$VSWR_{max}$				
@ input port	410 415	MHz		_	1.3	2.1	
@ output port	410 415	MHz		_	1.3	2.1	
Minimum attenuation			$\boldsymbol{\alpha}_{\text{min}}$				
	50 390	MHz		20	23	_	dB
	400 405	MHz		15	23	_	dB
	420 425	MHz		401)	44	_	dB
	439 1100	MHz		20	24	_	dB
	1200 1500	MHz		22	28	_	dB
	1500 1600	MHz		22	29	_	dB
	1600 4000	MHz		22	30	_	dB

Valid for temperature $T = -20 \,^{\circ}\text{C...} + 85 \,^{\circ}\text{C.}$



7 **Maximum ratings**

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Operable temperature	T _{OP} = −40 °C +85 °C	
Storage temperature	T _{STG} ¹⁾ = −40 °C +85 °C	
DC voltage	$ V_{DC} ^{2)} = 0 V$	
ESD voltage		
	$V_{\rm ESD}^{3)} = 150 \rm V$	Machine model.
	V _{ESD} ⁴⁾ = 250 V	Human body model.
@ input port: 410 415 MHz Input power	$P_{IN} = 29 \text{ dBm}^{5), 6}$	Continuous wave for 5000 h @ 55 °C.

¹⁾ Not valid for packaging material. Storage temperature for packaging material is −25 °C to +40 °C.

²⁾

In case of applied DC voltage blocking capacitors are mandatory.

According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.

⁴⁾ According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

Expected lifetime according to accelerated power durability simulations and wear-out models. T_{spec} is the ambient temperature of the PCB at component position. Specified min./max values from Section 6 "Characteristics" for maximum input power of 29dBm are valid for temperature up to 60°C.

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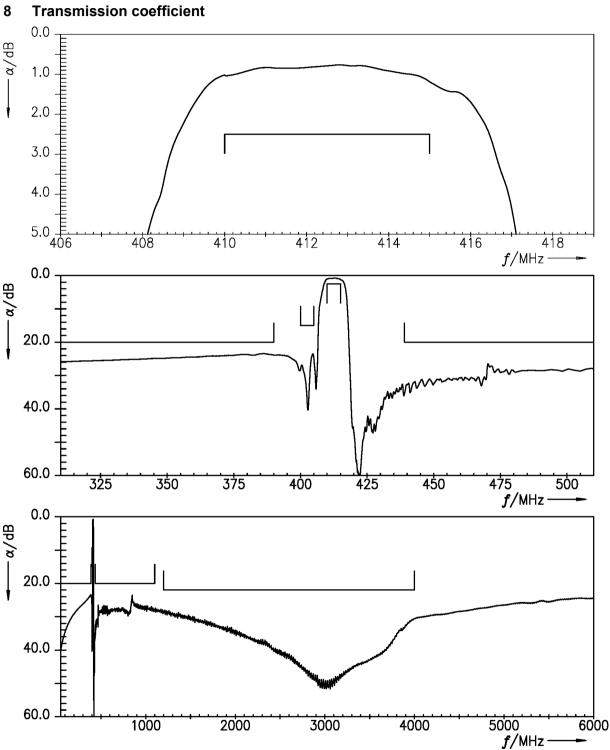
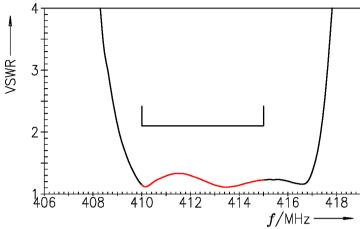


Figure 4: Attenuation.



9 Reflection coefficients



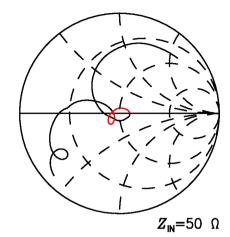
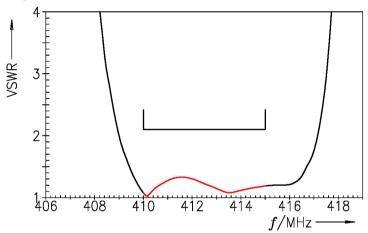


Figure 5: Reflection coefficient at input port.



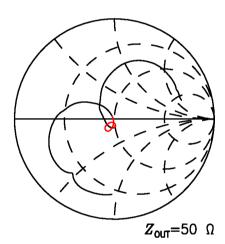


Figure 6: Reflection coefficient at output port.

10 Group delay

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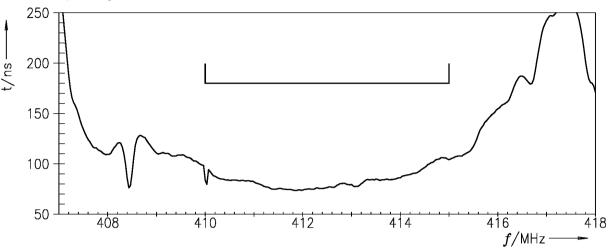


Figure 7: Group delay.



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11 Packing material

11.1 Tape

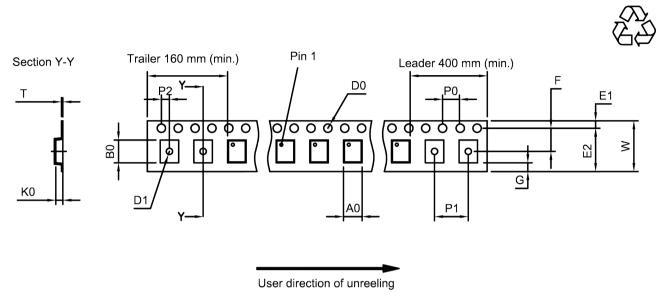


Figure 8: Drawing of tape (first-angle projection) for illustration only and not to scale. The valid tape dimensions are listed in Table 1.

A ₀	2.25±0.05 mm	_	E ₂	6.25 mm (min.)	· -	P ₁	4.0±0.1 mm
B ₀	2.75±0.05 mm		F	3.5±0.05 mm		P_2	2.0±0.05 mm
D_0	1.5+0.1/-0 mm		Ŋ	0.75 mm (min.)		Т	0.25±0.03 mm
D ₁	1.0 mm (min.)		K ₀	0.6±0.05 mm	_	W	8.0+0.3/-0.1 mm
E ₁	1.75±0.1 mm		P ₀	4.0 _{±0.1} mm	_		

Table 1: Tape dimensions.

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11.2 Reel with diameter of 180 mm

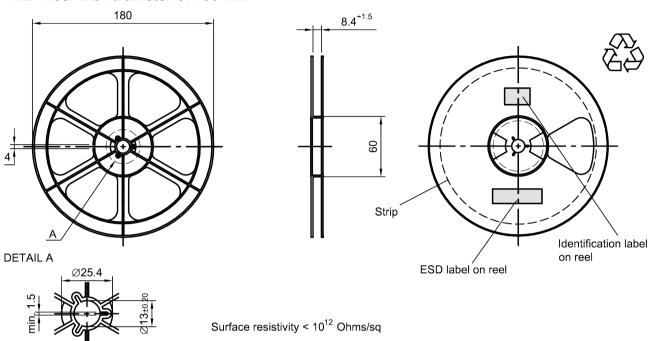


Figure 9: Drawing of reel (first-angle projection) with diameter of 180 mm.

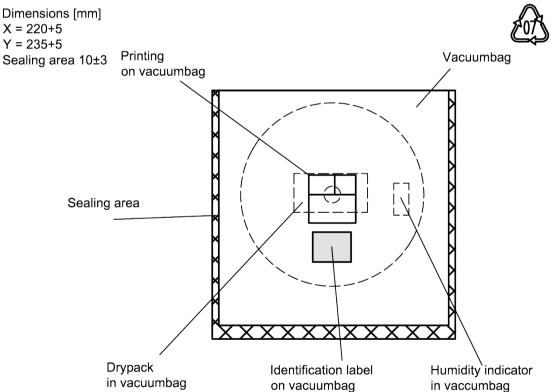


Figure 10: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

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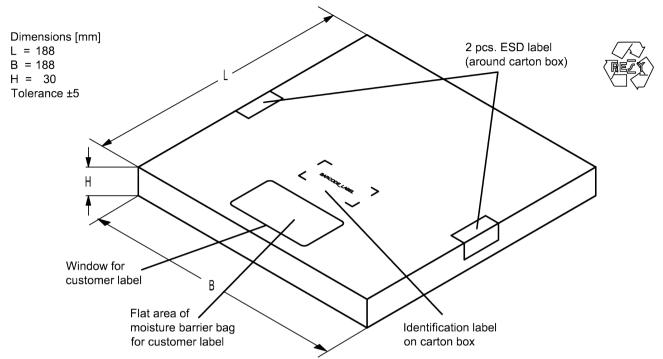


Figure 11: Drawing of folding box for reel with diameter of 180 mm.



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12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

■ Type number:

The 4 digit type number of the ordering code, e.g., B3xxxxB1234xxxx, is encoded by a special BASE32 code into a 3 digit marking.

Example of decoding type number marking on device in decimal code.

16J => 1234 1 x 32^2 + 6 x 32^1 + 18 (=J) x 32^0 = 1234

The BASE32 code for product type B8705 is 8G1.

■ Lot number:

The last 5 digits of the lot number, e.g., are encoded based on a special BASE47 code into a 3 digit marking.

Example of decoding lot number marking on device in decimal code.

5UY => 12345

 $5 \times 47^2 + 27 (=U) \times 47^1 + 31 (=Y) \times 47^0 = 12345$

Adopted BASE32 code for type number					
Decimal	Base32	Decimal	Base32		
value	code	value	code		
0	0	16	G		
1	1	17	Н		
2	2	18	J		
3	3	19	K		
4	4	20	M		
5 5		21	N		
6	6 6		Р		
7	7	23	Q		
8	8	24	R		
9	9 9		S		
10	Α	26	Т		
11	В	27	V		
12	12 C 13 D		W		
13			Х		
14	E	30	Y		
15	15 F		Z		

value code value co	se47 ode R
0 0 24	R
0 0 24	
1 1 25	S
2 2 26	Т
3 3 27	U
4 4 28	V
5 5 29 V	W
6 6 30	X
7 7 31	Υ
8 8 32	Z
9 9 33	b
10 A 34	d
11 B 35	f
12 C 36	h
13 D 37	n
14 E 38	r
15 F 39	t
16 G 40	V
17 H 41	١
18 J 42	?
19 K 43	{
20 L 44	}
21 M 45	<
22 N 46	>
23 P	

Table 2: Lists for encoding and decoding of marking.



13 Soldering profile

The recommended soldering process is in accordance with IEC $60068-2-58-3^{rd}$ edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature T _{peak}	250 °C +0/-5 °C
wetting temperature T _{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads
-	1

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

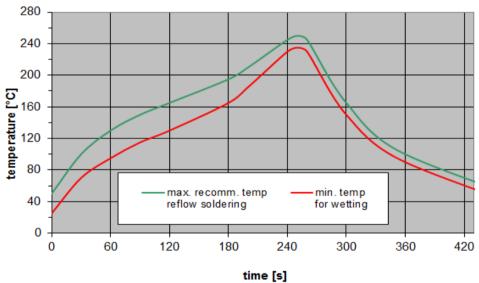


Figure 12: Recommended reflow profile for convection and infrared soldering – lead-free solder.



14 Annotations

14.1 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

14.2 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

14.3 Ordering codes / product IDs and packing units

Ordering code / product ID	RF360 label
B39411B8705P810	5000 pcs

Table 4: Ordering codes / product IDs and packing units.



15 Cautions and warnings

15.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under https://rffe.gualcomm.com/.

15.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

15.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

15.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Projection method

Unless otherwise specified first-angle projection is applied.



16 ESD protection of acoustic devices

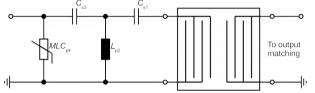
Acoustic devices are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies must be applied.

In general, "ESD matching" must be ensured at that electrical port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the acoustic device must be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band acoustic devices the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and input port. The required component values must be determined from case to case.



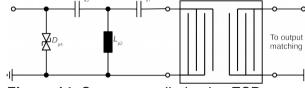


Figure 13: MLC varistor plus ESD matching.

Figure 14: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

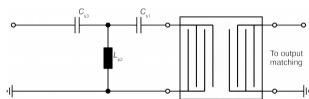


Figure 15: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: **"ESD protection for SAW filters".** This report can be found under https://rffe.qualcomm.com.



17 Important notes

The following applies to all products named in this publication:

- 1. Some parts of this publication contain statements about the suitability of our products for certain areas of application. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application. As a rule, RF360 Europe GmbH and its affiliates are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an RF360 product with the properties described in the product specification is suitable for use in a particular customer application.
- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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