

TO	文件编号	HXA-L30-11 (01)
TO :	发行日期	2020年05月27日

承认规格书

种 类: ____Power Inductor___

系列号: <u>HNRV201610NF-Series</u>

客户料号:_____

客	户 承	认栏		
承认日期		年	月	日

(贵司承认后请签署一份返回华信安电子,谢谢!)

厦门华信安电子科技有限公司技术质量部

承认	确认	作成
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Power Inductor

HNRV201610NF-SERIES

		ECN HISTORY	LIST		
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	20/05/27	新 發 行	龙梅	梁峰	王亮
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Power Inductor

HNRV201610NF-SERIES

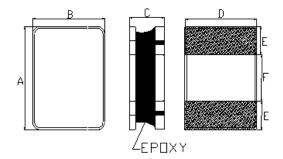
1. Features

- 1. This specification applies Low Profile Power Inductors.
- 2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

2. Dimension







Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)
HNRV201610NF	2.1+0.3/-0.2	1.7+0.3/-0.2	1.05 max	1.7+0.35/-0.2	0.675±0.3	0.7±0.3

3. Part Numbering

HNRV 201610 NF - 2R2 M
A B C D E

A: Series

B: Dimension

C: Control S/N

D: Inductance 2R2=2.2uH

E: Inductance Tolerance M=±20%; Y=±30%

4. Specification

ISND Part Number	Inductance (uH)	Tolerance (%)	Test Frequency (Hz)	DCR (mΩ) MAX	DCR (mΩ) TYP	l sat (A)	I rms (A).
HNRV201610NF-R24Y	0.24	±30%	0.1V1M	55	45	4.50	3.00
HNRV201610NF-R33Y	0.33	±30%	0.1V1M	64	52	4.20	2.90
HNRV201610NF-R47Y	0.47	±30%	0.1V1M	73	60	4.00	2.75
HNRV201610NF-R68Y	0.68	±30%	0.1V1M	85	70	3.50	3.15
HNRV201610NF-1R0M	1.0	±20%	0.1V1M	120	100	3.35	2.30
HNRV201610NF-1R5M	1.5	±20%	0.1V1M	140	118	1.95	1.30
HNRV201610NF-2R2M	2.2	±20%	0.1V1M	185	155	1.90	1.20
HNRV201610NF-3R3M	3.3	±20%	0.1V1M	335	280	1.40	0.95
HNRV201610NF-4R7M	4.7	±20%	0.1V1M	425	330	1.20	0.80
HNRV201610NF-6R8M	6.8	±20%	0.1V1M	590	480	0.90	0.60

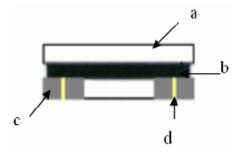
Note:

Isat : Based on inductance change (△L/L0 : ≦-30%) @ ambient temp. 25°C

Irms : Based on temperature rise $\ (\ ^{\Delta}T : 40^{\circ}C \ typ. \)$

5. Material List

No.	Description	Specification
a.	Core	Ferrite Core
b.	Coating	Epoxy with magnetic powder
С	Termination	Tin Pb Free
d	Wire	Enameled Copper Wire



6. Reliability and Test Condition

Item	Performance	Test Condition
Operating Temperature	- 40 ~ +125°C.	Including self-generated heat
Storage Temperature	- 40 ~ +85°C. - 5 to 40°C for the product with taping.	
Rated current		
Inductance (L)	Within the specified tolerance	LCR Meter: HP 4285A or equivalent, 100kHz, 1V
DC Resistance		DC Ohmmeter: HIOKI3227 or equivalent
Temperature characteristics	Inductance change: Within±20%	Measurement of inductance shall be taken at temperature rang within–25°C to +85°C. With reference to inductance value at+20 °C,change rate shall be calculated. Measurement of inductance shall be taken at temperature rang within–40°C to +125°C. With reference to inductance value at+20 °C,change rate shall be calculated.
Resistance to flexure substrate	No damage.	The test samples shall be soldered to the testing board by the reflow. As illustrated below, apply force in the direction of the arrow indicating until deflection of the test board reaches to 2mm. Proced 20 20 20 20 20 20 20 20 20 20 20 20 20
Adhesion of Terminal electrode	Shall not come off PC board.	The test samples shall be soldered to the testing board and by the reflow. 10 N, 5 s Applied force: 10 N to X and Y directions. Duration: 5s Solder cream thickness: 0.10
Resistance to Vibration	Inductance change: Within±10% No abnormality observed in appearance.	The test samples shall be soldered to the test board by the reflow. Then it shall be submitted to below test conditions. Frequency: 10-55Hz Total Amplitude: 1.5mm (May not exceed acceleration 196m/S2) Sweeping Method:10Hz to 55Hz to 10Hz for 1min. Time: 2 hours each in X,Y, and Z Direction. Recovery: At least 2hrs of recovery under the standard condition after the test, followed by the measurement within 48hrs.
Solderability	At least 90% of surface of terminal electrode is covered by new solder.	The test samples shall be dipped in flux, and then immersed in molten solder as shown in below. Flux: methanol solution containing rosin 25% Solder temperature: 245±5°C Time: 5±1.0 sec. Immersion depth: All sides of mounting terminal shall be immersed.

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Resistance to soldering	Inductance change: Within±10% No abnormality observed in appearance.	for 40 second seconds,2 tim Test board thi	ole shall be exposed to s, with peak temperatu es. ckness: 1.0mm aterial: glass epoxy-res	re at 260±5°C fo	
Item	Performance		Test Condition	on	
Thermal shock		reflow. The test samp for specified to sequence.	oles shall be soldered to oles shall be placed at ime by step 1 to step 4 ure cycles shall be rep Temperature("C") -40±3"C Room Temp 85±2"C Room Temp	specified tempera as shown below	ature in
Damp heat life test		soldered to th The test samp	95%RH	ow. hermostatic over	n set at
Loading under damp heat life test	Inductance change: Within±10% No abnormality observed in appearance.	reflow. The test samp specified temp current contin Temperature: Humidity: 90~	95%RH nt: Rated current	thermostatic over and applied the ra	set at
Low temperature life test		reflow.	-40±2℃		•
Loading at high temperature life test		reflow. Temperature:	nt: Rated current	o the test board t	by the

7. Soldering

7-1. Soldering

7-2. Recommended PC Board Pattern

Mildly activated rosin fluxes are preferred. ISND terminations are suitable for all wave and re-flow soldering systems.

If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

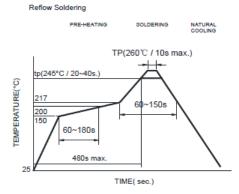
7-1.1 Solder re-flow:

Recommended temperature profiles for re-flow soldering in Figure 1.

7-1.2 Soldering Iron(Figure 2):

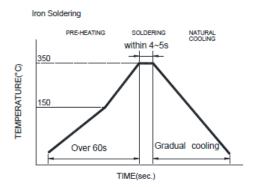
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.

·Preheat circuit and products to 150°C ·Never contact the ceramic with the iron tip ·Use a 20 watt soldering iron with tip diameter of 1.0mm



Reflow times: 3 times max.

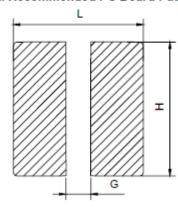
Fig.1



Iron Soldering times: 1 times max.

Fig.2

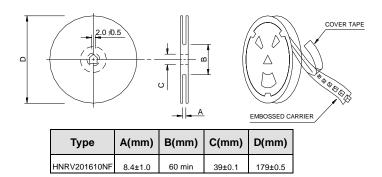
7-2. Recommended PC Board Pattern



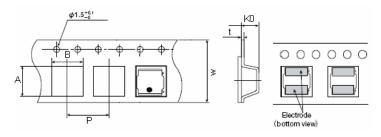
L(mm)	G(mm)	H(mm)
2.3	0.7	1.7

8. Packaging Information

(1) Reel Dimension



(2) Tape Dimension

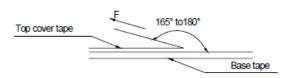


Туре	A(mm)	B(mm)	Ko(mm)	P(mm)	W(mm)	t(mm)
HNRV201610NF	3.1±0.1	3.1±0.1	1.6±0.1	4.0±0.1	8.0±0.2	0.23±0.05

(3) Packaging Quantity

Туре	Chip / Reel
HNRV201610NF	2000

8-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

·Storage Conditions

- To maintain the solderability of terminal electrodes:
- 1. ISND products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

 $\cdot Transportation \\$

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价,库存,交付和生命周期等信息

>>ISND(华信安)