**Product data sheet** 

# 1. General description

N-channel enhancement mode Field-Effect Transistor (FET) in a small SOT457 (SC-74) Surface-Mounted Device (SMD) plastic package using Trench MOSFET technology.

### 2. Features and benefits

- · Very fast switching
- Extended temperature range T<sub>i</sub> = 175 °C
- · Trench MOSFET technology
- AEC-Q101 qualified

## 3. Applications

- Relay driver
- · High-speed line driver
- · Low-side load switch
- Switching circuits

### 4. Quick reference data

#### Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	-	60	V
$V_{GS}$	gate-source voltage			-20	-	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	-	4.7	А
Static charac	teristics						
R <sub>DSon</sub>	drain-source on-state resistance	$V_{GS} = 10 \text{ V}; I_D = 4.7 \text{ A}; T_j = 25 \text{ °C}$		-	29	36	mΩ

[1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.



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# 5. Pinning information

### **Table 2. Pinning information**

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	D	drain	<u> </u>	D
2	D	drain		
3	G	gate	0 1 1 2 3	G—(F)
4	S	source	SC-74; TSOP6 (SOT457)	mbb076 S
5	D	drain		
6	D	drain		

# 6. Ordering information

### **Table 3. Ordering information**

Type number	Package				
	Name	Description	Version		
PMN40SNA	·	plastic, surface-mounted package (SC-74; TSOP6); 6 leads	SOT457		

# 7. Marking

#### Table 4. Marking codes

Type number	Marking code
PMN40SNA	н6

# 8. Limiting values

#### **Table 5. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
$V_{DS}$	drain-source voltage	T <sub>j</sub> = 25 °C		-	60	V
V <sub>GS</sub>	gate-source voltage			-20	20	V
I <sub>D</sub>	drain current	V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 25 °C	[1]	-	4.7	А
		V <sub>GS</sub> = 10 V; T <sub>amb</sub> = 100 °C	[1]	-	3	А
I <sub>DM</sub>	peak drain current	T <sub>amb</sub> = 25 °C; single pulse; t <sub>p</sub> ≤ 10 μs		-	19	А
P <sub>tot</sub>	total power dissipation	T <sub>amb</sub> = 25 °C	[2]	-	650	mW
			[1]	-	1.7	W
		T <sub>sp</sub> = 25 °C		-	7.5	W
Tj	junction temperature			-55	175	°C
T <sub>amb</sub>	ambient temperature			-55	175	°C
T <sub>stg</sub>	storage temperature			-65	175	°C
Source-drain	diode				•	
Is	source current	T <sub>amb</sub> = 25 °C	[1]	-	1.7	Α
ESD maximu	m rating				•	
V <sub>ESD</sub>	electrostatic discharge voltage	НВМ	[3]	-	500	V
Avalanche ru	iggedness			'		,
E <sub>DS(AL)S</sub>	non-repetitive drain- source avalanche energy	$T_{j(init)}$ = 25 °C; $I_D$ = 1 A; DUT in avalanche (unclamped)		-	30	mJ

- [1] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.
- [3] Measured between all pins.

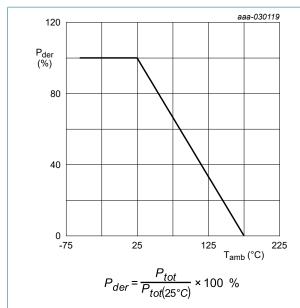


Fig. 1. Normalized total power dissipation as a function of ambient temperature

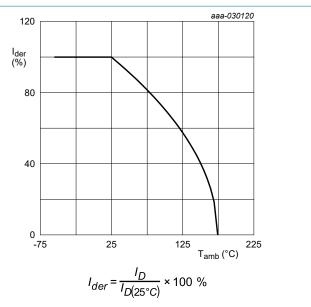


Fig. 2. Normalized continuous drain current as a function of ambient temperature

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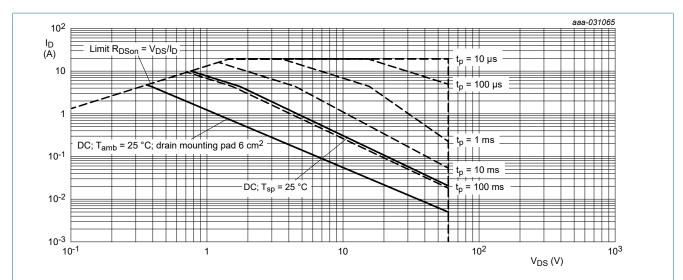


Fig. 3. Safe operating area; junction to ambient; continuous and peak drain currents as a function of drain-source voltage

60 V, N-channel Trench MOSFET

### 9. Thermal characteristics

**Table 6. Thermal characteristics** 

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
$R_{th(j-a)}$	thermal resistance from	in free air	[1]	-	200	230	K/W
junction to a	junction to ambient		[2]	-	78	90	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	15	20	K/W

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and mounting pad for drain 6 cm<sup>2</sup>.

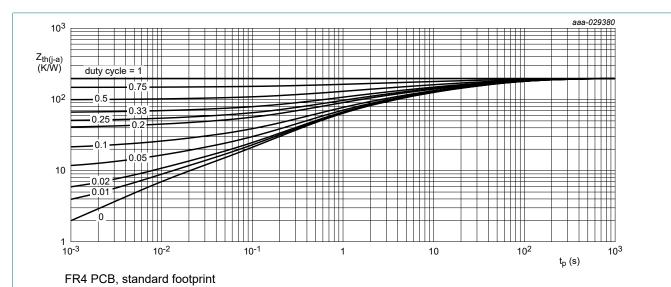


Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

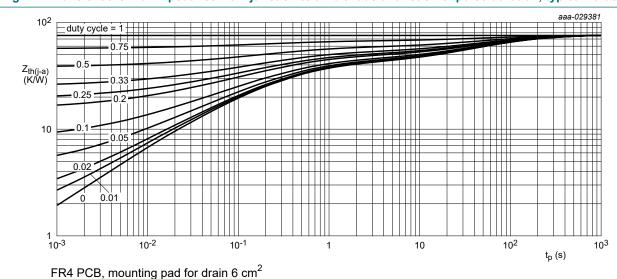


Fig. 5. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

**60 V, N-channel Trench MOSFET** 

# 10. Characteristics

### **Table 7. Characteristics**

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
Static chara	acteristics					
V <sub>(BR)DSS</sub>	drain-source breakdown voltage	$I_D = 250 \mu A; V_{GS} = 0 V; T_j = 25 °C$	60	-	-	V
$V_{GSth}$	gate-source threshold voltage	$I_D = 250 \mu A; V_{DS} = V_{GS}; T_j = 25 \text{ °C}$	2.4	3	4	V
I <sub>DSS</sub>	drain leakage current	V <sub>DS</sub> = 60 V; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	1	μΑ
I <sub>GSS</sub>	gate leakage current	V <sub>GS</sub> = 20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	100	nA
		V <sub>GS</sub> = -20 V; V <sub>DS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	-	-100	nA
R <sub>DSon</sub>	drain-source on-state	$V_{GS} = 10 \text{ V}; I_D = 4.7 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	29	36	mΩ
	resistance	V <sub>GS</sub> = 10 V; I <sub>D</sub> = 4.7 A; T <sub>j</sub> = 175 °C	-	61	76	mΩ
9 <sub>fs</sub>	forward transconductance	$V_{DS} = 10 \text{ V}; I_D = 4.4 \text{ A}; T_j = 25 ^{\circ}\text{C}$	-	8	-	S
$R_G$	gate resistance	f = 1 MHz	-	1.7	-	Ω
Dynamic ch	aracteristics					
Q <sub>G(tot)</sub>	total gate charge	$V_{DS} = 30 \text{ V}; I_D = 4.4 \text{ A}; V_{GS} = 10 \text{ V};$	-	9	14	nC
Q <sub>GS</sub>	gate-source charge	T <sub>j</sub> = 25 °C	-	1.7	-	nC
Q <sub>GD</sub>	gate-drain charge	- 	-	2.8	-	nC
C <sub>iss</sub>	input capacitance	V <sub>DS</sub> = 30 V; f = 1 MHz; V <sub>GS</sub> = 0 V;	-	453	-	pF
C <sub>oss</sub>	output capacitance	T <sub>j</sub> = 25 °C	-	62	-	pF
C <sub>rss</sub>	reverse transfer capacitance		-	42	-	pF
t <sub>d(on)</sub>	turn-on delay time	V <sub>DS</sub> = 30 V; I <sub>D</sub> = 4.2 A; V <sub>GS</sub> = 10 V;	-	6	-	ns
t <sub>r</sub>	rise time	$R_{G(ext)} = 6 \Omega; T_j = 25 °C$	-	7	-	ns
t <sub>d(off)</sub>	turn-off delay time	]	-	11	-	ns
t <sub>f</sub>	fall time	]	-	5	-	ns
Source-drai	in diode		1	1		
V <sub>SD</sub>	source-drain voltage	I <sub>S</sub> = 1.7 A; V <sub>GS</sub> = 0 V; T <sub>j</sub> = 25 °C	-	8.0	1.2	V
t <sub>rr</sub>	reverse recovery time	I <sub>S</sub> = 1.7 A; dI <sub>S</sub> /dt = -100 A/μs;	-	12	-	ns
Q <sub>r</sub>	recovered charge	$V_{GS} = 0 \text{ V}; V_{DS} = 30 \text{ V}; T_j = 25 \text{ °C}$	-	6	-	nC

### 60 V, N-channel Trench MOSFET

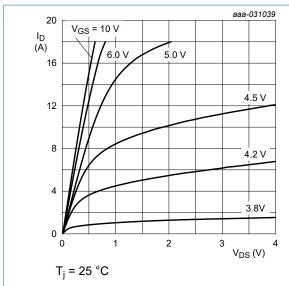


Fig. 6. Output characteristics: drain current as a function of drain-source voltage; typical values

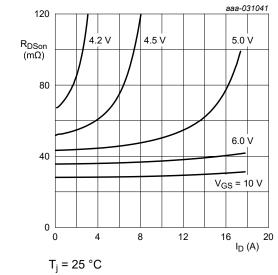


Fig. 8. Drain-source on-state resistance as a function of drain current; typical values

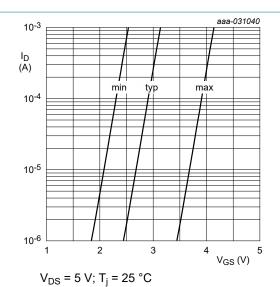


Fig. 7. Sub-threshold drain current as a function of gate-source voltage

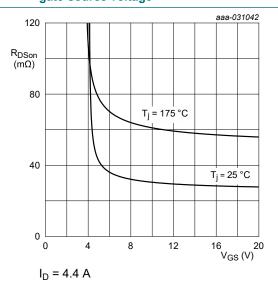


Fig. 9. Drain-source on-state resistance as a function of gate-source voltage; typical values

### 60 V, N-channel Trench MOSFET

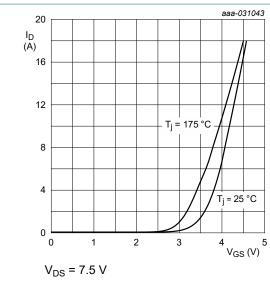


Fig. 10. Transfer characteristics: drain current as a function of gate-source voltage; typical values

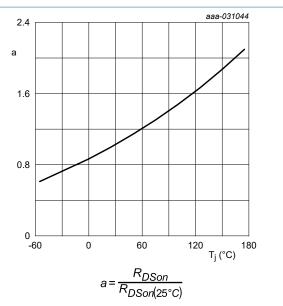


Fig. 11. Normalized drain-source on-state resistance as a function of junction temperature; typical values

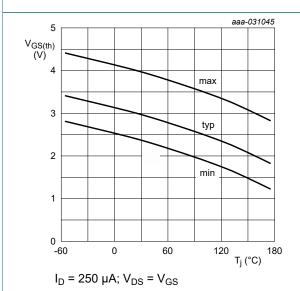


Fig. 12. Gate-source threshold voltage as a function of junction temperature

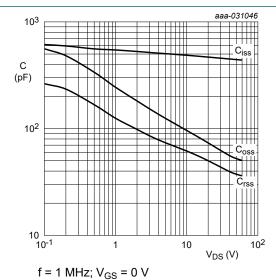


Fig. 13. Input, output and reverse transfer capacitances as a function of drain-source voltage; typical values

### **60 V, N-channel Trench MOSFET**

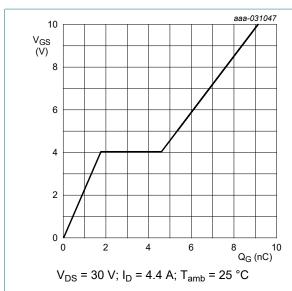


Fig. 14. Gate-source voltage as a function of gate charge; typical values

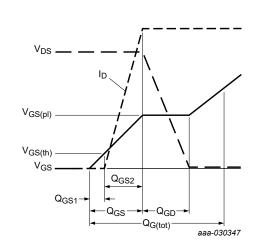


Fig. 15. Gate charge waveform definitions

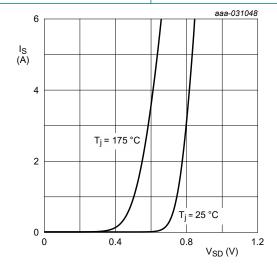
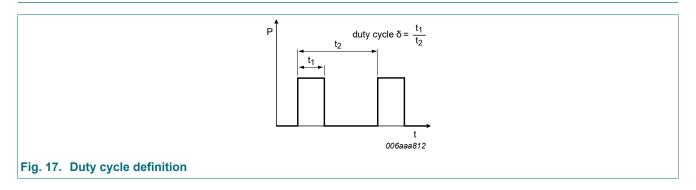


Fig. 16. Source current as a function of source-drain voltage; typical values

 $V_{GS} = 0 V$ 

**60 V, N-channel Trench MOSFET** 

## 11. Test information



### **Quality information**

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

### **60 V, N-channel Trench MOSFET**

# 12. Package outline

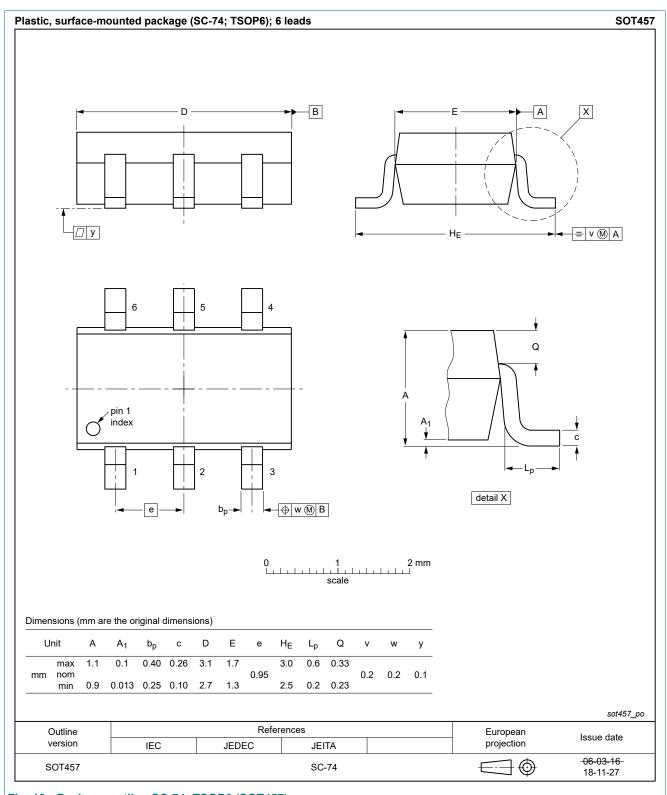
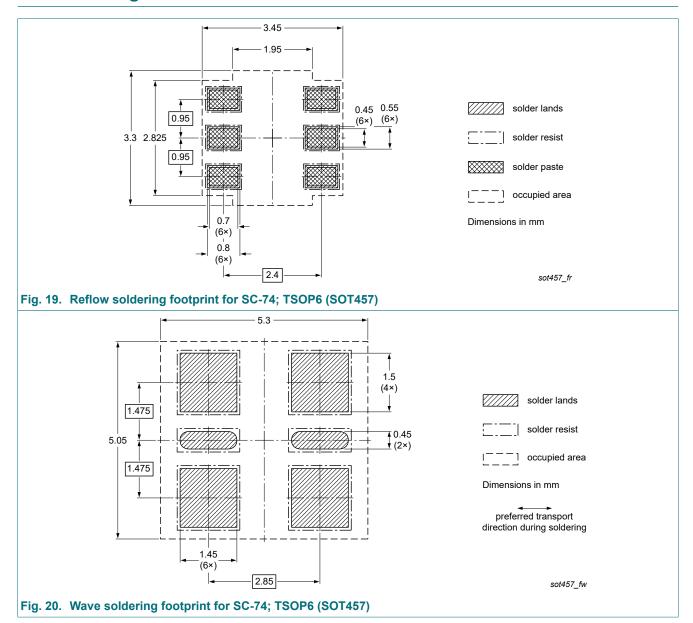


Fig. 18. Package outline SC-74; TSOP6 (SOT457)

### **60 V, N-channel Trench MOSFET**

# 13. Soldering



**60 V, N-channel Trench MOSFET** 

# 14. Revision history

### **Table 8. Revision history**

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes				
PMN40SNA v.2	20210408	Product data sheet	-	PMN40SNA v.1				
Modifications:	Chapter "Limiting value"	Chapter "Limiting values": Typo correction at parameter P <sub>tot</sub>						
PMN40SNA v.1	20200311	Product data sheet	-	-				

#### 60 V, N-channel Trench MOSFET

### 15. Legal information

#### Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

- Please consult the most recently issued document before initiating or completing a design.
- [2] The term 'short data sheet' is explained in section "Definitions".
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