



ES1A-ES1J

Feature

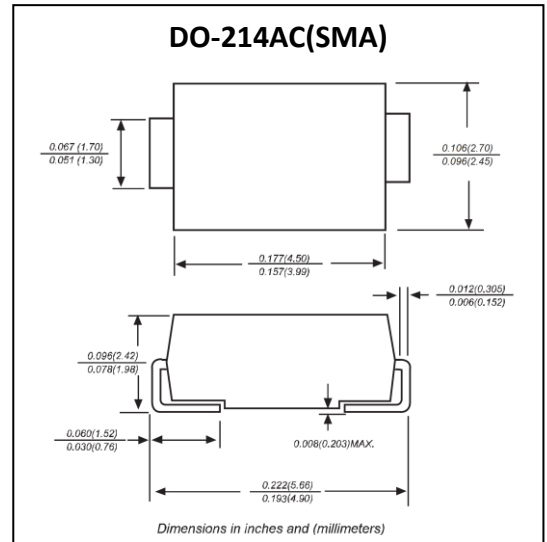
- I_o 1A
- V_{RRM} 50V-600V
- Glass passivated chip
- High surge current capability

Application

- Rectifier

Marking

- ES1X X:From A To J



ABSOLUTE MAXIMUM RATINGS ($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbo I	ES1								Unit
		A	B	C	D	E	G	H	J	
Repetitive Peak Reverse Voltage	V_{RRM}	50	100	150	200	300	400	500	600	V
Maximum RMS Voltage	V_{RMS}	35	70	105	140	210	280	350	420	V
Average Forward Current 60HZ Half-sine wave, Resistance load, $T_L=120^{\circ}\text{C}$	$I_{F(AV)}$	1								A
Non-repetitive Peak Forward Surge Current 60Hz Half-sine wave ,1 cycle , $T_a =25^{\circ}\text{C}$	I_{FSM}	30								A
Junction Temperature	T_J	-55 ~ +150								$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 ~ +150								$^{\circ}\text{C}$

ELECTRICAL CHARACTERISTICS ($T_a=25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Test Condition	ES1							Unit
			A	B	C	D	E	G	H	
Peak Forward Voltage	V_F	$I_F = 1\text{A}$	0.95			1.25		1.70		V
Maximum reverse recovery time	t_{rr}	$I_F=0.5\text{A}, I_R=1.0\text{A}, I_{rr}=0.25\text{A}$	35							ns
Peak Reverse Current	I_{RRM1}	$V_{RM}=V_R$	5							μA
	I_{RRM2}	R_M	100							μA
Thermal Resistance(Typical)	$R_{\theta J-A}$	Between junction and ambient	85							$^{\circ}\text{C}/\text{W}$
	$R_{\theta J-L}$	Between junction and terminal	35							$^{\circ}\text{C}/\text{W}$

Typical Characteristics

FIG.1: FORWARD CURRENT DERATING CURVE

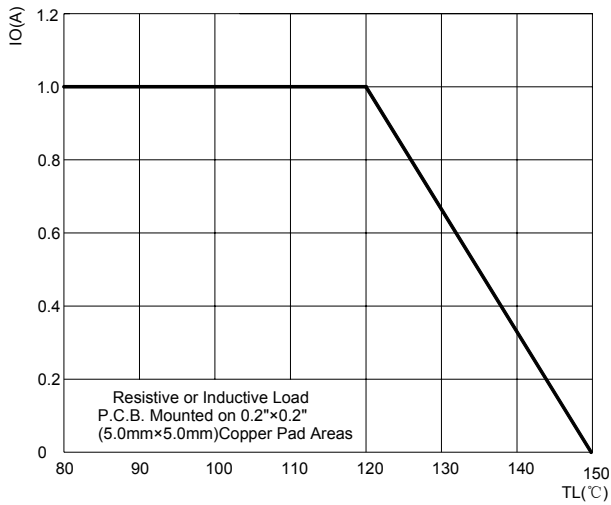


FIG.2: MAXIMUM NON-REPETITIVE FORWARD URGE CURRENT

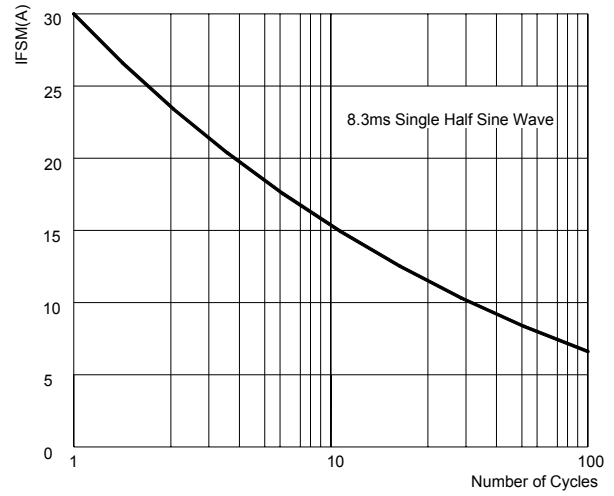


FIG.3: TYPICAL FORWARD CHARACTERISTICS

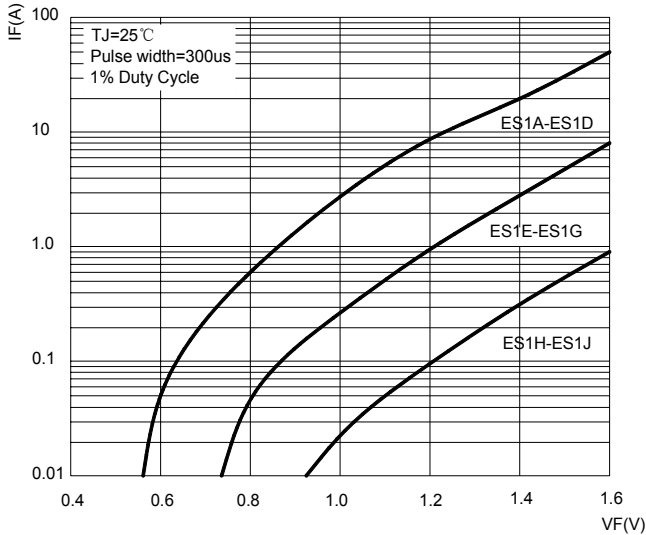
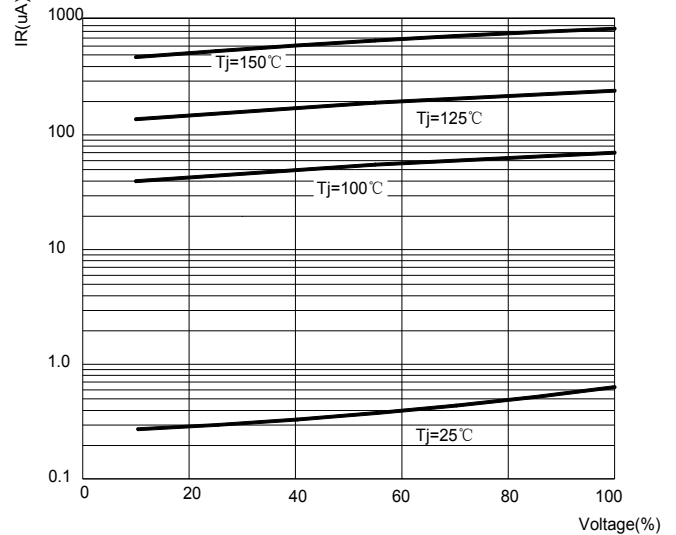
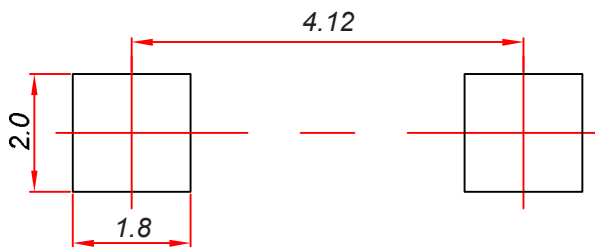


FIG.4: TYPICAL REVERSE CHARACTERISTICS



SMA Suggested Pad Layout



Note:

1. Controlling dimension: in millimeters.
2. General tolerance: ± 0.05mm.
3. The pad layout is for reference purposes only.

单击下面可查看定价，库存，交付和生命周期等信息

[>>GP\(格瑞宝\)](#)