

Specification for Approval

Date: 2022/08/03

Customer : _____

TAI-TECH P/N: HPC5040NV-Series-HD

CUSTOMER P/N: _____

DESCRIPTION: _____

QUANTITY: _____ pcs

REMARK:		
Customer Approval Feedback		

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SMD Power Inductor	HPC5040NV-Series-HD
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ECN HISTORY LIST					
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	22/08/03	New Issue	Sky Luo	Mr.Liang	Xu yaoyao
備 注					

SMD Power Inductor

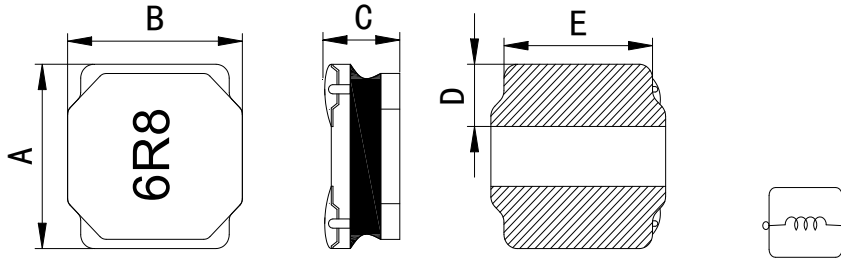
HPC5040NV-Series-HD

1. Features

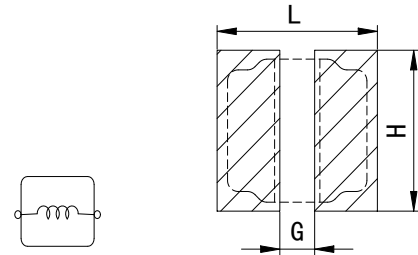
1. This specification applies Low Profile Power Inductors.
2. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
3. High reliability -Reliability tests comply with AEC-Q200
4. Operating temperature : -55~+125°C (Including self - temperature rise)



Dimension



Recommend Land pattern

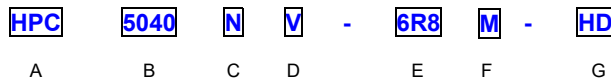


Series	Inductance	A(mm)	B(mm)	C(mm)	D(mm)	E (mm)
HPC5040NV	≤10 uH	4.95±0.2	4.95±0.2	3.9±0.2	1.3±0.3	4.2±0.2
	>10 uH			3.8±0.2		

L(mm)	G(mm)	H(mm)
5.5	1.8	5.5

Note: 1.PCB layout is referred to standard IPC-7351B
 2. The above PCB layout reference only.
 3. Recommend solder paste thickness at 0.12mm and above.

3. Part Numbering



- A: Series
 - B: Dimension
 - C: Type
 - D: :Category Code
 - E: Inductance
 - F: Inductance Tolerance
 - G: Code
- A/B*C
 V=Vehicle
 6R8=6.8uH
 K=±10%, L=±15%, M=±20%, Y=±30%.
 marking direction cannot decide polarity. Color: Black, unidirectional.
 magnetic shielding

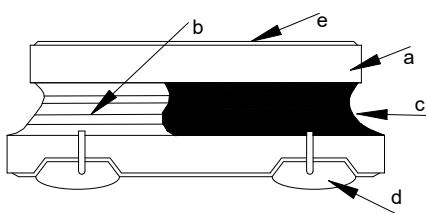
4. Specification

Part Number	Inductance L0 (uH) @ 0 A	Rated current		DCR (mΩ) @25°C ±20%
		Temperature current I rms (A)	Saturation current I sat (A)	
HPC5040NV-R60M-HD	0.60	8.00	11.0	8
HPC5040NV-1R0M-HD	1.00	5.00	7.50	12
HPC5040NV-1R5M-HD	1.50	4.50	6.50	15
HPC5040NV-1R8M-HD	1.80	4.20	6.10	18
HPC5040NV-2R2M-HD	2.20	3.80	5.70	21
HPC5040NV-3R3M-HD	3.30	3.50	4.40	24
HPC5040NV-4R7M-HD	4.70	3.20	3.90	32
HPC5040NV-6R8M-HD	6.80	2.50	3.30	43
HPC5040NV-100M-HD	10.0	2.20	2.52	56
HPC5040NV-150M-HD	15.0	1.80	2.00	80
HPC5040NV-220M-HD	22.0	1.50	1.62	123
HPC5040NV-330M-HD	33.0	1.20	1.30	180
HPC5040NV-470M-HD	47.0	1.00	1.10	270
HPC5040NV-680M-HD	68.0	0.80	0.90	400
HPC5040NV-820M-HD	82.0	0.75	0.78	490
HPC5040NV-101M-HD	100	0.72	0.75	560

Note:

1. All test data referenced to 25°C ambient , Ls:100KHz/1V.
2. Testing Instrument : HP4284A,CH11025,CH3302,CH1320 ,CH1320S LCR METER / Rdc:CH502BC MICRO OHMMETER.
3. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40°C
4. Saturation Current (Isat) will cause L0 to drop approximately 30%.
5. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions.Circuit design,component,PCB trace size and thickness,airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
6. Special inquiries besides the above common used types can be met on your requirement.
7. Rated DC current: The lower value of I rms and Isat.

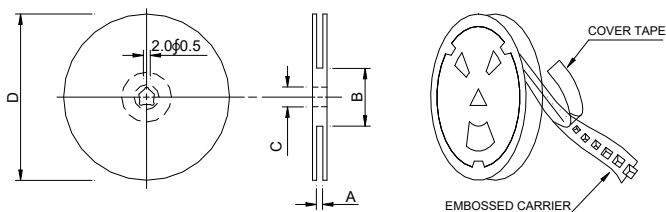
5. Material List



NO	Items	Materials
a	Core	Ferrite Core
b	Wire	Enameled Copper Wire
c	Glue	Epoxy with magnetic powder
d	Terminal	Ag/Ni/Sn + Sn Solder
e	Ink	Halogen-free ketone

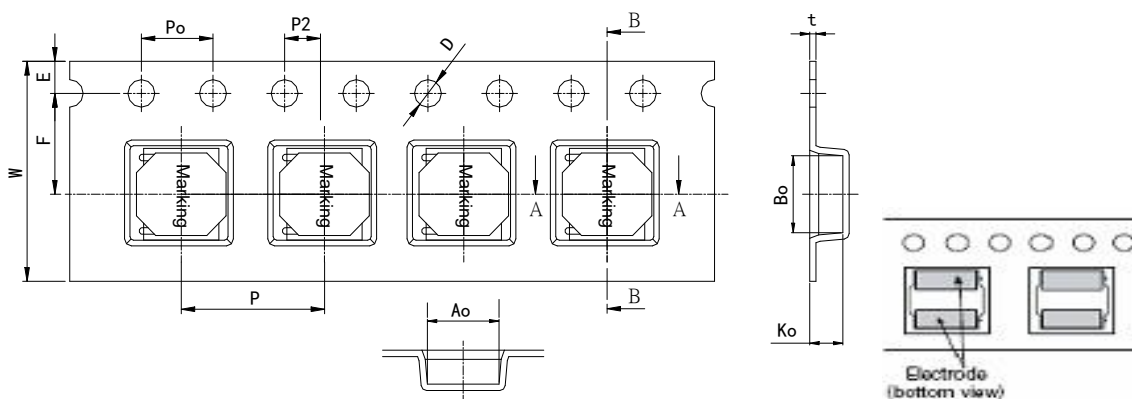
6. Packaging Information

(1) Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x12mm	12.4+2/-0	80±2.0	13+0.5/-0.2	330±3.0

(2) Tape Dimension

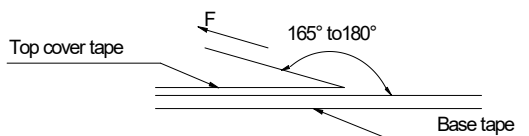


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	w(mm)	t(mm)	Emm)	F(mm)	D(mm)	Po(mm)	P2(mm)
HPC	5040	5.4±0.1	5.4±0.1	4.3±0.1	8.0±0.1	12±0.3	0.4±0.1	1.75±0.1	7.5±0.1	1.5±0.1	4.0±0.1	2.00±0.1

(3) Packaging Quantity

HPC	5040
Reel	1500

(4) Tearing Off Force

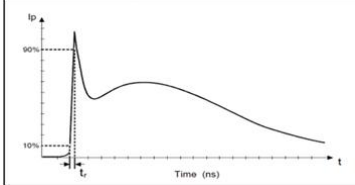
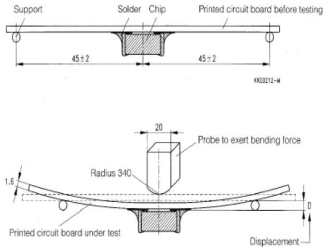
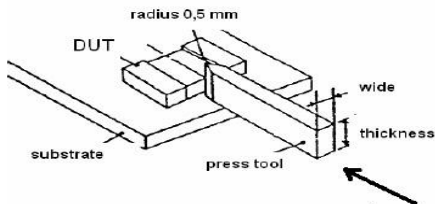


The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 standard).

Tearing Speed mm	Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)
300±10%	5~35	45~85	860~1060

7.Reliability and Test Condition

Item	Performance	Test Condition															
Operating temperature	-55~+125°C (Including self - temperature rise)																
Storage temperature and Humidity range	1. -10~+40°C ,50~60%RH (Product with taping) 2. -55~+125°C (on board)																
Electrical Performance Test																	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.															
DCR		CH16502,Agilent33420A Micro-Ohm Meter.															
Saturation Current (Isat)	Approximately $\Delta L30\%$	Saturation DC Current (Isat) will cause L0 to drop $\Delta L(\%)$															
Heat Rated Current (Irms)	Approximately $\Delta T40^{\circ}C$	Heat Rated Current (Irms) will cause the coil temperature rise $\Delta T(^{\circ}C)$. 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer															
Reliability Test																	
High Temperature Exposure(Storage) AEC-Q200	Appearance : No damage. Inductance : with in $\pm 10\%$ of initial value Q : Shall not exceed the specification value. RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 125 $\pm 2^{\circ}C$ (Inductor) Duration : 1000hrs Min. Measured at room temperature after placing for 24 ± 2 hrs															
Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55 $\pm 2^{\circ}C$ 30min Min.(Inductor) Step2: 125 $\pm 2^{\circ}C$ transition time 1min MAX. Step3: 125 $\pm 2^{\circ}C$ 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24 ± 2 hrs															
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Humidity : 85 $\pm 3\%$ R.H, Temperature: 85 $\pm 2^{\circ}C$ Duration : 1000hrs Min Measured at room temperature after placing for 24 ± 2 hrs															
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 125 $\pm 2^{\circ}C$ (Inductor) Duration : 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24 ± 2 hrs															
External Visual		Appearance : No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.														
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement															
Resistance to Solvents	Appearance : No damage.	Add aqueous wash chemical - OKEM clean or equivalent.															
Mechanical Shock	Appearance : No damage. Inductance : with in $\pm 10\%$ of initial value Q : Shall not exceed the specification value. RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	<table border="1"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>100</td> <td>6</td> <td>Half-sine</td> <td>12.3</td> </tr> <tr> <td>Lead</td> <td>100</td> <td>6</td> <td>Half-sine</td> <td>12.3</td> </tr> </tbody> </table> <p>shocks in each direction along 3 perpendicular axes.(18 shocks).</p>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	100	6	Half-sine	12.3	Lead	100	6	Half-sine	12.3
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec													
SMD	100	6	Half-sine	12.3													
Lead	100	6	Half-sine	12.3													

Item	Performance	Test Condition						
Vibration		IPC/JEDEC J-STD-020D Classification Reflow Profiles Oscillation Frequency: 10~2K~10Hz for 20 minute Equipment : Vibration checker Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) :						
Resistance to Soldering Heat	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value Resistance to Soldering Heat	Test condition:(MIL-STD-202 Condition B) Number of heat cycles:1 <table border="1" data-bbox="967 405 1337 499"> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> </tr> <tr> <td>260±5 (solder temp)</td> <td>10±1</td> <td>25mm/s ± 6mm/s</td> </tr> </table> Depth: completely cover the termination	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	260±5 (solder temp)	10±1	25mm/s ± 6mm/s
Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate						
260±5 (solder temp)	10±1	25mm/s ± 6mm/s						
Thermal shock (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55±2°C 15±1min(Inductor) Step2: 125±2°C within 20Sec. Step3: 125±2°C 15±1min Number of cycles : 300 Measured at room fempraturc after placing fo24±2hrs						
ESD	Appearance : No damage.	 <p>Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target Test method: AEC-Q200-002 Test mode : Contact Discharge Discharge level : 4 KV (Level: 2)</p>						
Solderability	More than 95% of the terminal electrode should be covered with solder :	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.						
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .						
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.						
Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. 						
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 						

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

8.Soldering Specifications

(1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

(3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

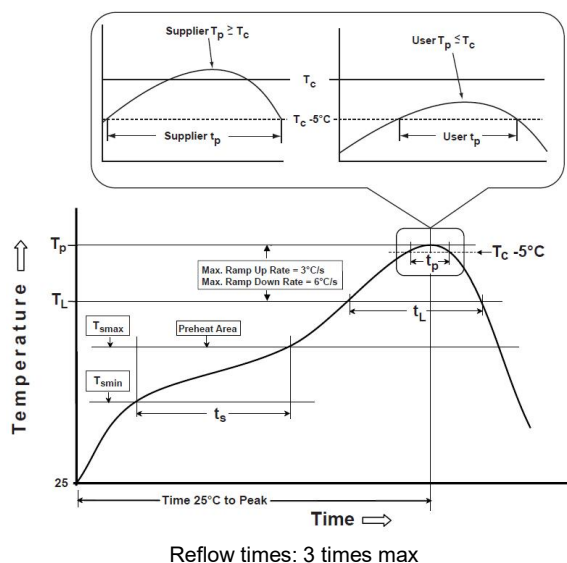


Fig.2 Iron soldering temperature profiles

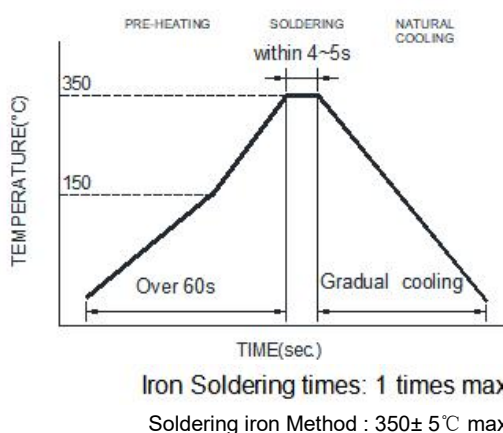


Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T_{smin})	150°C
-Temperature Max(T_{smax})	200°C
-Time(t_s)from(T_{smin} to T_{smax})	60-120seconds
Ramp-up rate(T_L to T_p)	3°C/second max.
Liquidus temperature(T_L)	217°C
Time(t_L)maintained above T_L	60-150 seconds
Classification temperature(T_c)	See Table (1.2)
Time(t_p) at $T_c - 5^\circ C$ (T_p should be equal to or less than T_c .)	* < 30 seconds
Ramp-down rate(T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

T_p : maximum peak package body temperature, T_c : the classification temperature.

For user (customer) T_p should be equal to or less than T_c .

* Tolerance for peak profile temperature (T_p) is defined as a supplier minimum and a user maximum.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E ◦

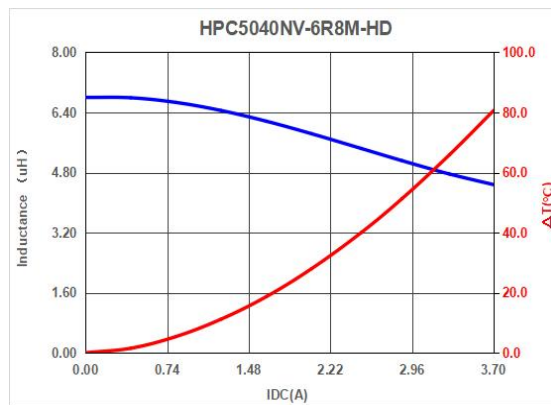
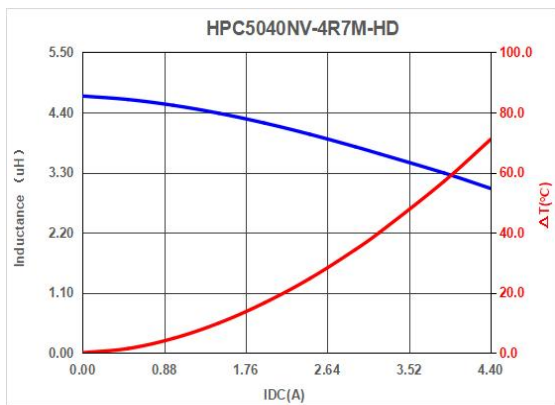
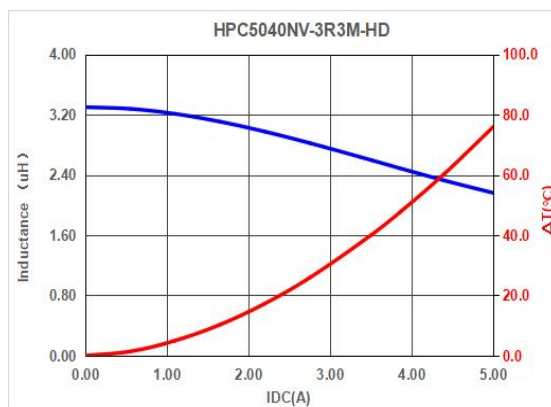
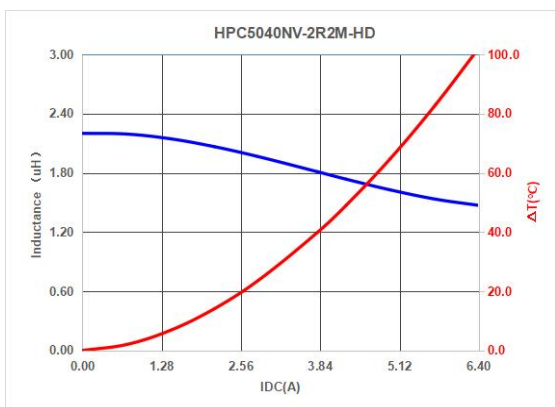
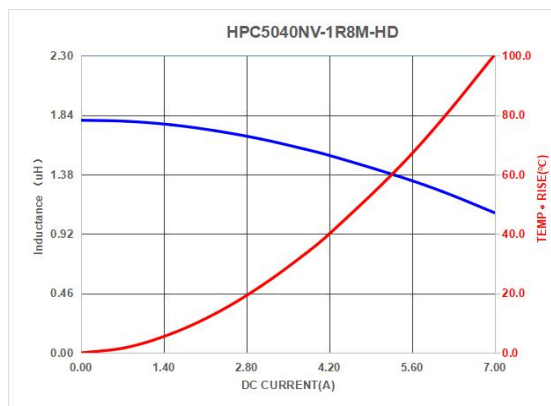
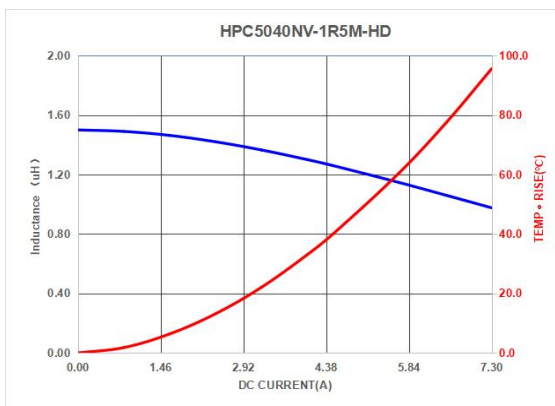
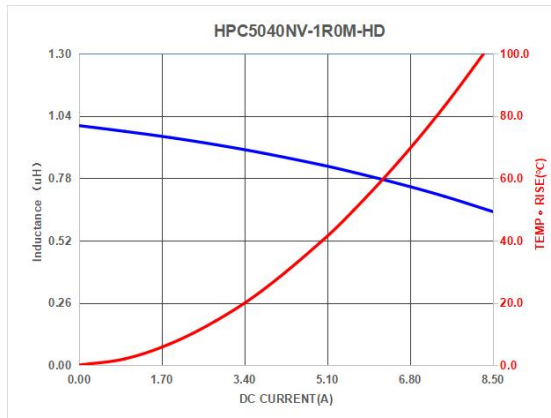
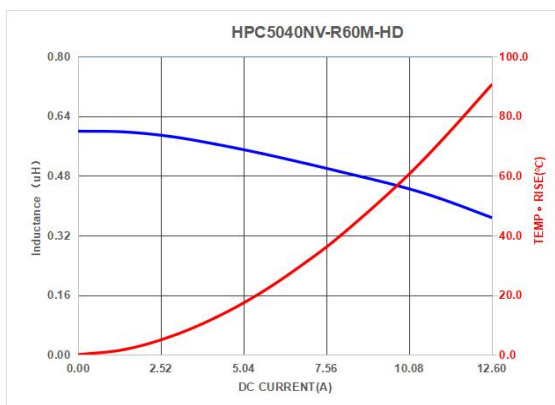
9. Notes

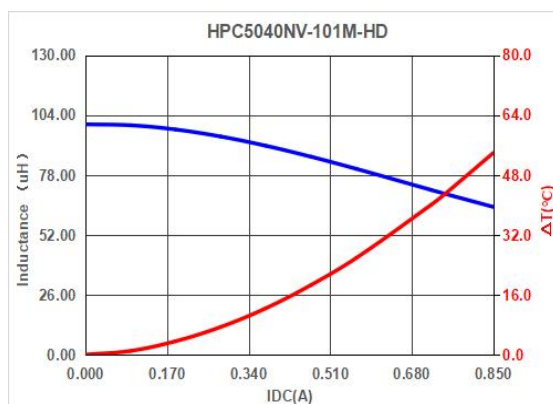
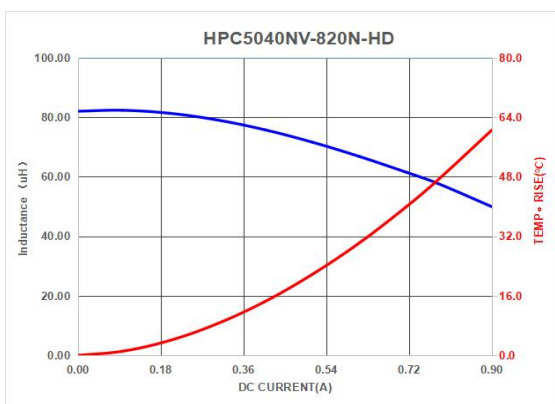
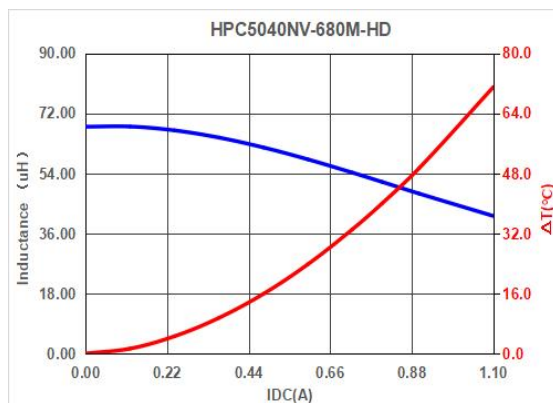
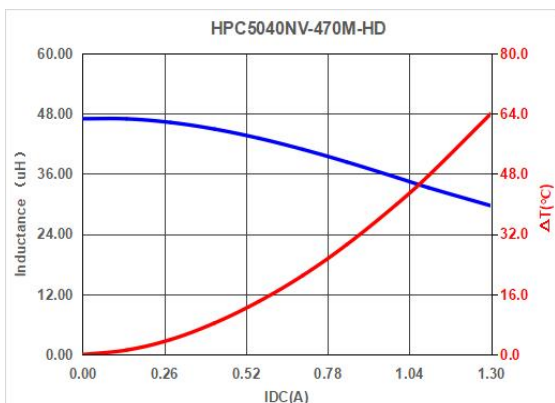
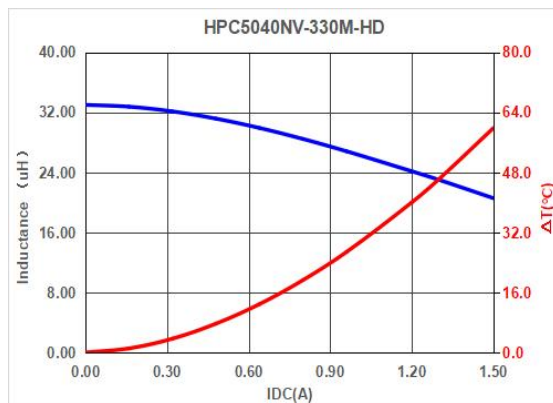
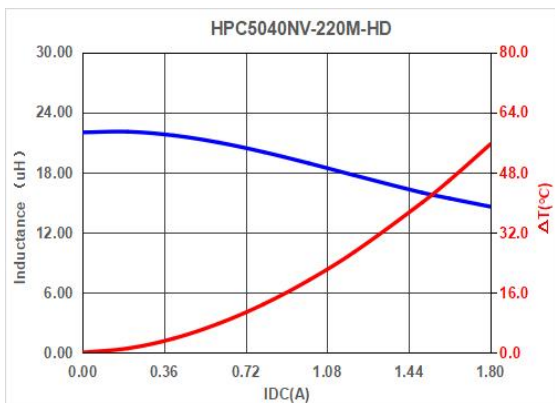
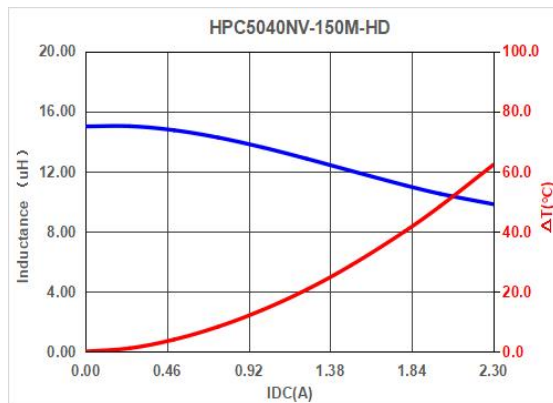
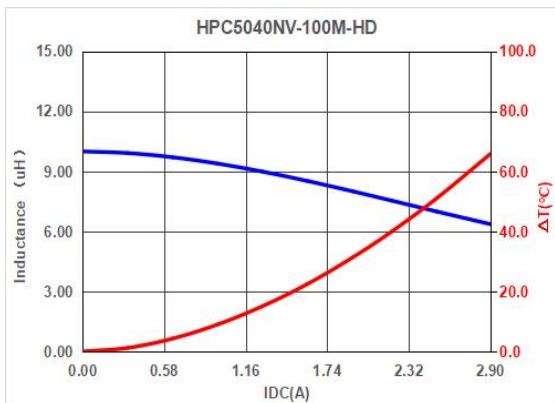
- (1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method 1 and dry it off immediately .
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappears.
- (9) The high power ultrasonic washing may damage the choke body .
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification .

Application Notice

- Storage Conditions
 - To maintain the solderability of terminal electrodes:
 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
 3. Recommended products should be used within 12 months from the time of delivery.
 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

10. Typical Performance Curves

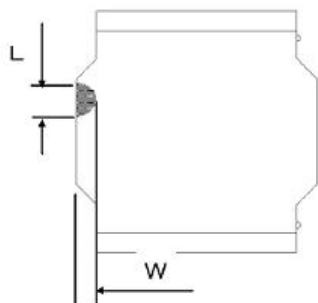




11 Appearance criterion

11-1 Core chipping

The appearance standard of the chipping size on top side, and bottom side ferrite core is listed below. Chip off is generated during molding and manufacturing process. Chip off acceptance limits subjected to the product size. Our current Defect limit is based on the IPC-A-610. Some chip off does not impact the product function, see the IPC standard 1 & 2.

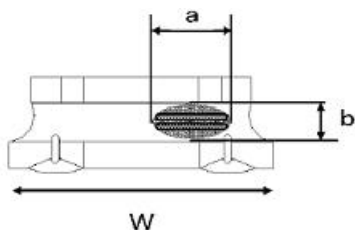


L	≤50 % of the length
W	≤25 % of the width

Defects usually occur at the corners and edges of the product, There will be a slight defect black and rough, but not exposed copper, and does not affect the product performance and reliability.

11-2 Void appearance tolerance Limit

Size of voids occurring to coating resin is specified below.



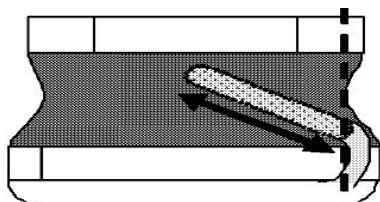
Exposed wire tolerance limit of coating resin part on product side.

Size of exposed wire occurring to coating resin is specified below.

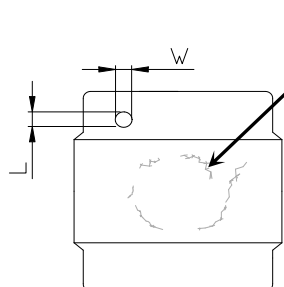
1. Width direction (dimension a) : Acceptable when $a \leq w/2$.
2. Length direction (dimension b) : Dimension b is not specified.
3. The total area of exposed wire occurring to each sides is not greater than 50% of coating resin area, and is acceptable.

11-3 External appearance criterion for exposed wire


Exposed winding wire at the secondary side is regarded as qualified product.




11-4 Electrode appearance criterion for exposed wire



Visual check on core surface with no crack means pass.


 Conforming
 Only top side of wire is exposed.
 (regardless of whole top side of wire exposed)


 Less than 1/2 of joint side length.
 (More than 1/2 is selected as defect)
 Wire is soldered insufficiently and less than half of outer diameter is covered with solder.

L&w
≤20% of the area on one single pad

Foreign materials on the product body is inevitable and accepted. Electrodes with foreign body (dirt) appearance standards Foreign materials (dirt) will not affect the coplanarity of PAD, below the example of foreign materials (dirt) quantity ≤2PCS on single PAD. Dimensions range as shown in the table.

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