



# Specification for Approval

Date: 2022/07/30

	Custor	ner :	
	TAI-TECH P/N:	TMPC0503HV-Ser	ies(G)-D-HD
	CUSTOMER P/N:		
	DESCRIPTION:		
	QUANTITY:	pcs	<u> </u>
REM	ARK:		
	Cu	stomer Approval Feedba	ack

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# **SMD Power Inductor**

TMPC0503HV-Series(G)-D-HD

	ECN HISTORY LIST							
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN			
1.0	22/07/30	New Issue	Sky Luo	Mr.Liang	Cui lingling			
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## **SMD Power Inductor**

TMPC0503HV-Series(G)-D-HD

#### 1. Features

- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.
- 5. High reliability -Reliability test complied to AEC-Q200.



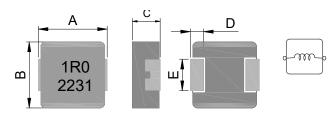


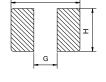


#### 2. Applications

Automotive applications.

#### 3. Dimensions

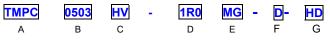




Series	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC0503HV	5.7±0.3	5.2±0.2	2.8±0.2	1.1±0.3	1.5±0.2

L(mm)	G(mm)	H(mm)
6.5	2.5	1.8

# 4. Part Numbering



BxC

A: Series

B: Dimension

C: Type Carbonyl Powder, V:vehicle.

D: Inductance 1R0=1.00uH E: Inductance Tolerance M=±20%

F: DateCode Marking: Black.1R0 and 2231 (22 YY,31 WW,follow production date).

G:Code

#### Note:

- PPCB layout is referred to standard IPC-7351B
   The above PCB layout reference only.
   Recommend solder paste thickness at 0.12mm and above.

# 5. Specification

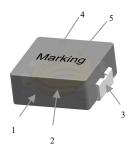
Part Number	Inductance L0 (uH)±20%	I rms ( A ) Typ	I sat (A) Typ	DCR(mΩ) Typ	DCR(mΩ) Max
TMPC0503HV-R10YG-D-HD	0.10±30%	23	27	2.5	3.0
TMPC0503HV-R20YG-D-HD	0.20±30%	16	25	2.6	3.2
TMPC0503HV-R22MG-D-HD	0.22	15.5	21	3.7	4.4
TMPC0503HV-R33MG-D-HD	0.33	14	18	4.3	5.0
TMPC0503HV-R47MG-D-HD	0.47	12	16	6.4	7.4
TMPC0503HV-R68MG-D-HD	0.68	8.5	14	10	12
TMPC0503HV-1R0MG-D-HD	1.00	7.0	11	13	14
TMPC0503HV-1R2MG-D-HD	1.20	6.5	11	14	16
TMPC0503HV-1R5MG-D-HD	1.50	6.0	10	16	25
TMPC0503HV-2R2MG-D-HD	2.20	5.5	9.0	25	35
TMPC0503HV-3R3MG-D-HD	3.30	5.0	8.0	32	38
TMPC0503HV-3R6MG-D-HD	3.60	4.9	7.0	46	54
TMPC0503HV-4R7MG-D-HD	4.70	4.6	6.0	50	53
TMPC0503HV-5R6MG-D-HD	5.60	4.25	4.50	55	63
TMPC0503HV-6R8MG-D-HD	6.80	4.0	4.3	68	76.2
TMPC0503HV-100MG-D-HD	10.0	2.75	3.50	110	128
TMPC0503HV-150MG-D-HD	15.0	2.10	2.60	165	190
TMPC0503HV-180MG-D-HD	18.0	2.0	2.30	195	230
TMPC0503HV-220MG-D-HD	22.0	1.90	1.70	220	250
TMPC0503HV-330MG-D-HD	33.0	1.60	1.60	380	440

#### Note:

- 1. Test frequency : Ls : 100KHz /1.0V.
- $3. Testing\ Instrument (or\ equ): Agilent\ 4284A, E4991A, 4339B, KEYSIGHT\ E4980A/AL, chroma 3302, 3250, 16502.$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\,\Delta T$  of 40  $^{\circ}\!C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.
- 8. Irms Testing: Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.

  Therefore temperature rise should be verified in application conditions.
- 9. Rated DC current: The lower value of Irms and Isat.

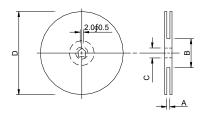
#### 6. Material List

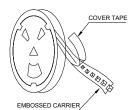


NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

# 7. Packaging Information

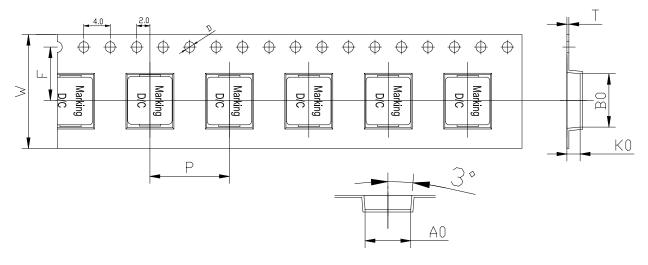
#### (1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x12mm	12.4+2/-0	100±2	13+0.5/-0.2	330

#### (2) Tape Dimension

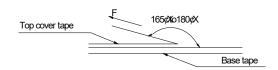


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
ТМРС	0503	6.2±0.1	5.5±0.1	3.3±0.1	8.0±0.1	12.0±0.3	5.5±0.1	0.35±0.05	1.5±0.1

#### (3) Packaging Quantity

ТМРС	0503
Chip / Reel	2000

#### (4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(°C)	(%)	(hPa)
300±10%	5~35	45~85	

# 8. Reliability and Test Condition

Item	Performance	Test Condition			
Operating temperature	-55~+125°ℂ (Including self - temperature rise)				
Storage temperature and Humidity range	110~+40℃,50~60%RH (Product with taping) 255~+125℃ (on board)				
Electrical Performance Test					
Inductance		HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.			
DCR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.			
Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0 to drop △L(%)			
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise $\triangle$ T( $\mathbb{C}$ ).  1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer			
Reliability Test					
High Temperature Exposure(Storage) AEC-Q200  Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020EClassification Reflow Profiles Temperature: 125±2°C (Inductor · ambient + temp rise) Duration: 1000hrs Min. Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55±2°C 30min Min.(Inductor) Step2: 125±2°C transition time 1min MAX. Step3: 125±2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs			
Moisture Resistance (AEC-Q200)	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	t=24 hours/cycle. Note: Steps 7a & 7b Unpowered.			
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity: 85±3 % R.H, Temperature: 85℃±2℃ Duration: 1000hrs Min Measured at room temperature after placing for24±2hrs			
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.  (IPC/JEDECJ-STD-020E Classification Reflow Profiles Temperature: 125±2°C (Inductor) Duration: 1000hrs Min. With 100% rated current. Measured at room temperature after placing for24±2hrs			
External Visual	Appearance : No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.			
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement			
Resistance to Solvents	Appearance : No damage.	Add aqueous wash chemical - OKEM clean or equivalent.			
Mechanical Shock	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition  Type Peak value (g's) Normal duration (D) (ms) Form change (Vi)ft/sec SMD 100 6 Half-sine 12.3  Lead 100 6 Half-sine 12.3  3 shocks in each direction along 3 perpendicular axes(18 shocks).			

Item	Performance	Test Condition		
Vibration		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment: Vibration checker Total Amplitude: 5g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations)		
Resistance to Soldering Heat	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Test condition:(MIL-STD-202 Condition B)  Number of heat cycles:1  Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate  260±5 (solder temp) 10±1 25mm/s±6mm/s  Depth: completely cover the termination		
Thermal shock (AEC-Q200)		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1: -55±2℃ 15±1min(Inductor) Step2: 125±2℃ within 20Sec. Step3: 125±2℃ 15±1min Number of cycles: 300 Measured at room fempraturc after placing fo24±2hrs		
ESD	Appearance : No damage.	Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target Test method: AEC-Q200-002 Test mode: Contact Discharge Discharge level: 4 KV (Level: 2)		
Solderability	More than 95% of the terminal electrode should be covered with solder $^{\circ}$	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time: 5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.		
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .		
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.		
Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board.  Support  Solder Chip Printed circuit board before testing  Printed circuit board under test  Printed circuit board under test  Displacement		
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.  radius 0,5 mm  DUT  wide  thickness shear force		

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

### 9. Soldering Specifications

#### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

#### (3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Fig. 2)

- $\bullet$  Preheat circuit and products to 150  $\!\!\!\!\!\!^{\circ}$
- · Never contact the ceramic with the iron tip
- 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

• 355°C tip temperature (max)

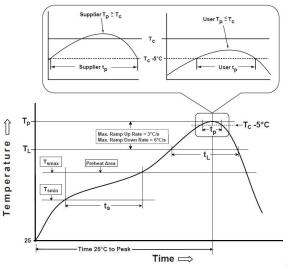
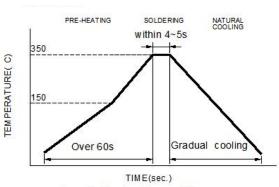


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max.

Soldering iron Method : 350± 5℃ max

Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T <sub>smin</sub> ) -Temperature Max(T <sub>smax</sub> )	150℃ 200℃
-Time(t <sub>s</sub> )from(T <sub>smin</sub> to T <sub>smax</sub> )	60-120seconds
Ramp-up rate( $T_L$ to $T_p$ )	3°C/second max.
Liquidus temperature(T <sub>L</sub> ) Time(t <sub>L</sub> )maintained above T <sub>L</sub>	217℃ 60-150 seconds
Classification temperature(T <sub>c</sub> )	See Table (1.2)
$\label{eq:tp} \mbox{Time}(t_p) \mbox{ at Tc-} \mbox{ 5^{\circ}\!$	*< 30 seconds
Ramp-down rate(Tp to TL)	6℃ /second max.
Time 25℃ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260°C
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	245℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020E •

<sup>\*</sup> Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

#### 10. Notes

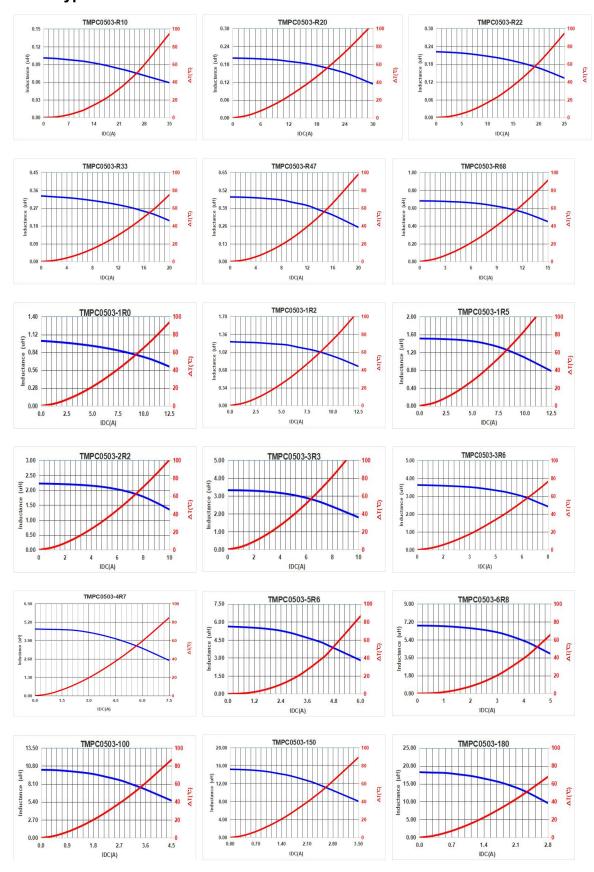
(1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition

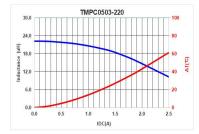
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body •
- (10) Before use, the user should determine whether this product is suitable for their own design. Our company only guarantees that the product meets the requirements of this specification.

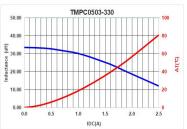
#### **Application Notice**

- Storage Conditions
  - To maintain the solderability of terminal electrodes:
  - 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
  - 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
  - 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - The use of tweezers or vacuum pick up is strongly recommended for individual components.
  - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

# 11. Typical Performance Curves







# 单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)