

# Specification for Approval

**Date:** 2022/03/15

**Customer :** \_\_\_\_\_

**TAI-TECH P/N:** TMPV0754SV-Series(N)-D

**CUSTOMER P/N:** \_\_\_\_\_

**DESCRIPTION:** \_\_\_\_\_

**QUANTITY:** \_\_\_\_\_ pcs

<b>REMARK:</b>		
Customer Approval Feedback		

西北臺慶科技股份有限公司  
 TAI-TECH Advanced Electronics Co., Ltd  
Headquarter:  
 NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI,  
 TAO-YUAN HSIEN, TAIWAN, R.O.C.  
 TEL: +886-3-4641148 FAX: +886-3-4643565  
 http://www.tai-tech.com.tw  
 E-mail: sales@tai-tech.com.tw

臺慶精密電子(昆山)有限公司  
 TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD  
 SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN,  
 JIANG-SU, CHINA  
 TEL: +86-512-57619396 FAX: +86-512-57619688  
 E-mail: sales@tai-tech.cn

慶邦電子元器件(泗洪)有限公司  
 TAIPAQ ELECTRONICS (SIHONG) CO., LTD  
 JIN SHA JIANG ROAD , CONOMIC DEVELOPMENT ZONE SIHONG ,  
 JIANGSU , CHINA.  
 TEL: +86-527-88601191 FAX: +86-527-88601190  
 E-mail: sales@taipaq.cn

Sales Dep.

APPROVED	CHECKED
Eric Kuan	Zhang mengmeng

R&D Center

APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Cui lingling

**SMD Power Inductor**

**TMPV0754SV-Series(N)-D**

**ECN HISTORY LIST**

REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	22/03/15	New Issue	Sky Luo	Mr.Liang	Cui lingling

備 注	
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**SMD Power Inductor**

**TMPV0754SV-Series(N)-D**

**1. Features**

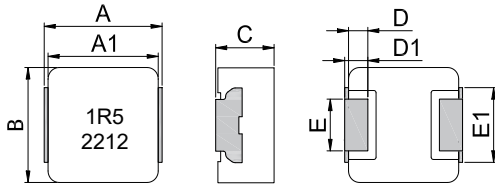
1. Low loss realized with low DCR.
2. High performance realized by metal dust core.
3. Ultra low buzz noise, due to composite construction.
4. 100% Lead(Pb)-Free and RoHS compliant.
5. High reliability -Reliability test complied to AEC-Q200.



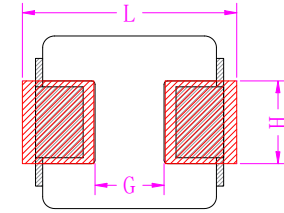
**2. Applications**

Automotive applications.

**3. Dimensions**



**Recommend PC Board Pattern**



Series	A(mm)	A1(mm)	B(mm)	C(mm)	D(mm)	D1(mm)	E(mm)	E1(mm)
TMPV0754SV	7.9±0.3	7.3±0.2	7.3±0.2	5.2±0.2	1.7±0.3	2.0±0.3	3.0±0.2	5.4±0.2

L(mm)	G(mm)	H(mm)
9.0	3.0	3.5

Note: 1.PCB layout is referred to standard IPC-7351B  
 2. The above PCB layout reference only.  
 3. Recommend solder paste thickness at 0.15mm and above.

**4. Part Numbering**



A: Series  
 B: Dimension BxC  
 C: Type Standard.  
 D: Inductance 1R5=1.50uH  
 E: Inductance Tolerance K=±10%, L=±15%, M=±20%, N=±25%, Y=±30%  
 F, G: Code  
 Marking direction cannot decide polarity.  
 Marking: Black.1R5 and 2212 (22:YY,12:WW, follow production date).

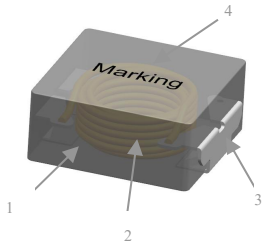
## 5. Specification

Part Number	Inductance L0 A( $\mu$ H) $\pm 20\%$	Heat Rating Current DC I <sub>rms</sub> ( A )		Saturation Current DC I <sub>sat</sub> ( A )		DCR (m $\Omega$ ) Typ	DCR (m $\Omega$ ) Max
		Typ	Max	Typ	Max		
TMPV0754SV-1R0MN-D	1.00	20.0	17.0	23.0	20.0	5.0	6.0
TMPV0754SV-1R5MN-D	1.50	17.0	15.0	19.0	17.0	6.3	7.3
TMPV0754SV-2R2MN-D	2.20	14.5	13.0	16.5	14.5	9.7	11.2
TMPV0754SV-3R3MN-D	3.30	11.5	10.5	14.0	12.3	13.0	15.0
TMPV0754SV-4R7MN-D	4.70	10.5	9.0	13.3	11.3	17.8	20.5
TMPV0754SV-5R6MN-D	5.60	9.3	8.5	11.0	9.5	19.2	22.1
TMPV0754SV-6R8MN-D	6.80	8.7	8.0	10.2	9.0	23.0	26.5
TMPV0754SV-8R2MN-D	8.20	8.0	7.5	9.2	8.1	27.3	31.4
TMPV0754SV-100MN-D	10.0	7.2	6.7	8.0	7.0	33.0	38.0
TMPV0754SV-120MN-D	12.0	6.3	5.8	7.7	6.7	43.0	52.0
TMPV0754SV-150MN-D	15.0	5.5	5.0	7.2	6.2	60.0	66.0
TMPV0754SV-220MN-D	22.0	5.0	4.5	6.3	5.4	85.0	93.5
TMPV0754SV-330MN-D	33.0	4.0	3.5	4.9	4.2	111.0	127.6
TMPV0754SV-470MN-D	47.0	3.2	2.7	4.1	3.5	156.0	171.6
TMPV0754SV-560MN-D	56.0	3.0	2.6	3.4	2.9	188.0	206.8
TMPV0754SV-680MN-D	68.0	2.7	2.4	3.0	2.6	218.0	251.0
TMPV0754SV-101MN-D	100.0	2.2	2.0	2.0	1.6	310.0	357.0

Note:

1. Test frequency : Ls : 100KHz /1.0V.
2. All test data referenced to 25°C ambient.
3. Testing Instrument(or equ) : Agilent 4284A,E4991A,4339B,KEYSIGHT E4980A/AL,chroma3302,3250,16502.
4. Heat Rated Current (I<sub>rms</sub>) will cause the coil temperature rise approximately  $\Delta T$  of 40°C
5. Saturation Current (I<sub>sat</sub>) will cause L0 to drop approximately 30%.
6. The part temperature (ambient + temp rise) should not exceed 165°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
7. I<sub>rms</sub> Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.
8. Rated DC current: The lower value of I<sub>rms</sub> and I<sub>sat</sub>.

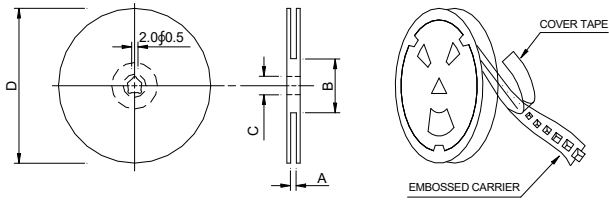
### 6. Material List



NO	Items	Materials
1	Core	Alloy Powder .
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+Sn---Plating)
4	Ink	Halogen-free ketone

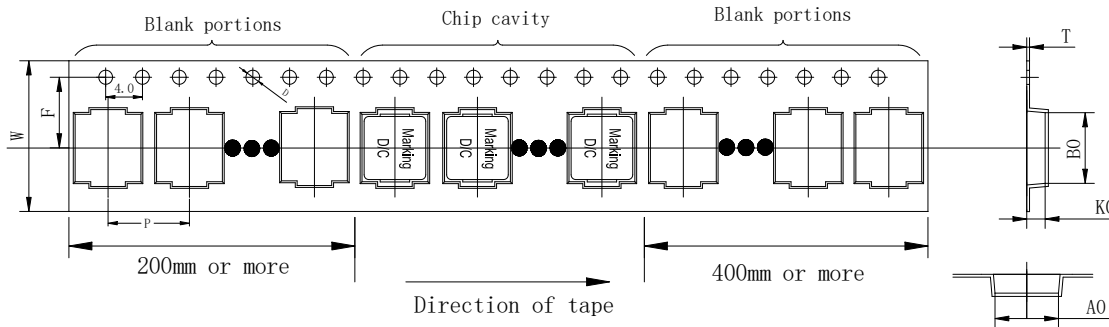
### 7. Packaging Information

#### (1) Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x16mm	16.4+2/-0	100±2	13+0.5/-0.2	330

#### (2) Tape Dimension

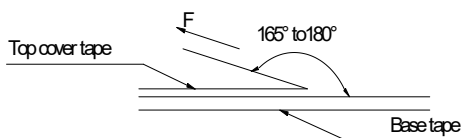


Series	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	T(mm)	D(mm)
TMPV0754	9.0±0.1	7.7±0.1	5.7±0.1	12.0±0.1	16±0.3	7.5±0.1	0.40±0.05	1.5±0.1

#### (3) Packaging Quantity

Series	Reel
TMPV0754	800

#### (4) Tearing Off Force

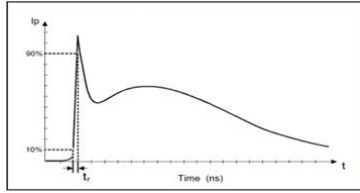
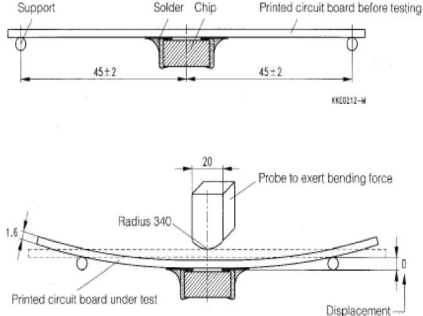
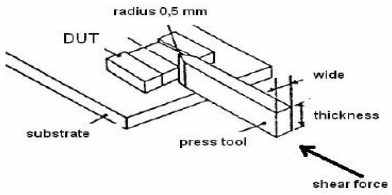


The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 standard).

Tearing Speed mm	Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)
300±10%	5~35	45~85	860~1060

### 8. Reliability and Test Condition

Item	Performance	Test Condition															
Operating temperature	-55~+165°C(Including self - temperature rise)	N/A															
Storage temperature and Humidity range	1. Less than40°C ,85%RH (Product with taping) 2. -55~+165°C(on board)	N/A															
<b>Electrical Performance Test</b>																	
Inductance	Refer to standard electrical characteristics list.	Agilent 4284A,E4991A,KEYSIGHT E4980A/AL,chroma 3302,3205															
DCR		Agilent 4339B,chrom16502															
Saturation Current (Isat)	Approximately ΔL30%	Saturation DC Current (Isat) will cause L0 to drop ΔL(%)															
Heat Rated Current (Irms)	Approximately ΔT40°C	Heat Rated Current (Irms) will cause the coil temperature rise ΔT(°C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer															
<b>Reliability Test</b>																	
High Temperature Exposure(Storage) AEC-Q200	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 165±2°C(Inductor) Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs															
Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55±2°C 30min Min.(Inductor) Step2: 165±2°C transition time 1min MAX. Step3: 165±2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs															
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity : 85±3% R.H, Temperature: 85°C±2°C Duration : 1000hrs Min Measured at room temperature after placing for24±2hrs															
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDECJ-STD-020E Classification Reflow Profiles Temperature: 165±2°C(Inductor) Duration : 1000hrs Min. With 100% rated current. Measured at room temperature after placing for24±2hrs															
External Visual	Appearance : No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.															
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement															
Resistance to Solvents	Appearance : No damage.	Add aqueous wash chemical - OKEM clean or equivalent.															
Mechanical Shock	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition <table border="1"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>100</td> <td>6</td> <td>Half-sine</td> <td>12.3</td> </tr> <tr> <td>Lead</td> <td>100</td> <td>6</td> <td>Half-sine</td> <td>12.3</td> </tr> </tbody> </table> <p>3 shocks in each direction along 3 perpendicular axes(18 shocks).</p>	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	100	6	Half-sine	12.3	Lead	100	6	Half-sine	12.3
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec													
SMD	100	6	Half-sine	12.3													
Lead	100	6	Half-sine	12.3													

Item	Performance	Test Condition						
Vibration		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment : Vibration checker Total Amplitude: 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) °						
Resistance to Soldering Heat	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Test condition:(MIL-STD-202 Condition B) Number of heat cycles:1 <table border="1" data-bbox="948 409 1318 506"> <thead> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> </tr> </thead> <tbody> <tr> <td>260±5 (solder temp)</td> <td>10±5</td> <td>25mm/s ± 6 mm/s</td> </tr> </tbody> </table>	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	260±5 (solder temp)	10±5	25mm/s ± 6 mm/s
Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate						
260±5 (solder temp)	10±5	25mm/s ± 6 mm/s						
Thermal shock (AEC-Q200)		Depth: completely cover the termination Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Condition for 1 cycle Step1: -55±2°C 15±1min(Inductor) Step2: 165±2°C within 20Sec. Step3: 165±2°C 15±1min Number of cycles : 300 Measured at room fempraturc after placing fo24±2hrs						
ESD HBM >=2KV	Appearance : No damage.	 <p>Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target                      Test method: AEC-Q200-002                      Test mode: Contact Discharge                      Discharge level: 4 KV (Level: 2 )</p>						
Solderability	More than 95% of the terminal electrode should be covered with solder .	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.						
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .						
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.						
Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. 						
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 						

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

## 9. Soldering Specifications

### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

### (2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

### (3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

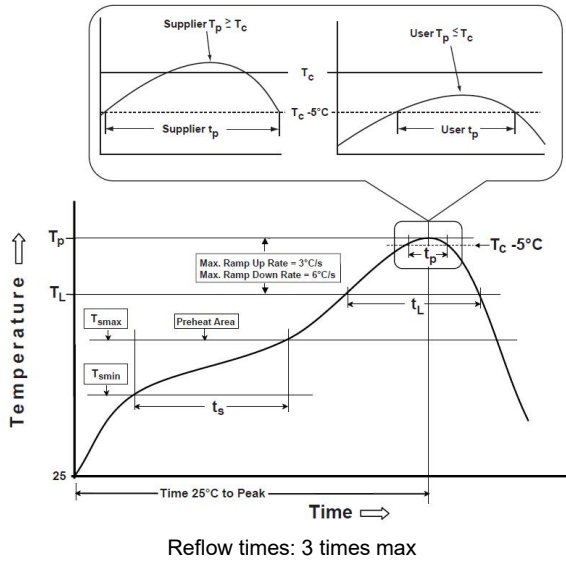


Fig.2 Iron soldering temperature profiles

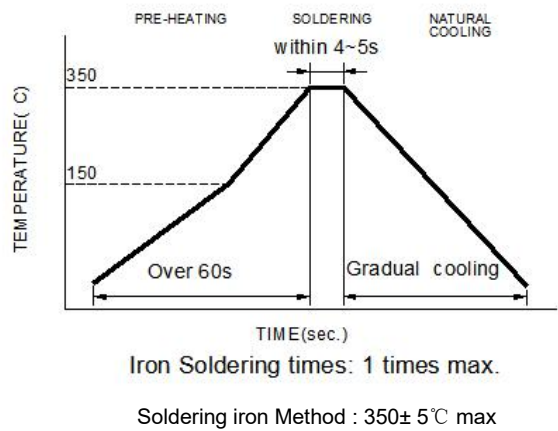


Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min( $T_{smin}$ ) -Temperature Max( $T_{smax}$ ) -Time( $t_s$ )from( $T_{smin}$ to $T_{smax}$ )	150°C 200°C 60-120seconds
Ramp-up rate( $T_L$ to $T_p$ )	3°C/second max.
Liquidus temperature( $T_L$ ) Time( $t_L$ )maintained above $T_L$	217°C 60-150 seconds
Classification temperature( $T_c$ )	See Table (1.2)
Time( $t_p$ ) at $T_c - 5^\circ C$ ( $T_p$ should be equal to or less than $T_c$ .)	* < 30 seconds
Ramp-down rate( $T_p$ to $T_L$ )	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

$T_p$ : maximum peak package body temperature,  $T_c$ : the classification temperature.

For user (customer)  $T_p$  should be equal to or less than  $T_c$ .

\* Tolerance for peak profile temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.

Table (1.2) Package Thickness/Volume and Classification Temperature ( $T_c$ )

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E ◦



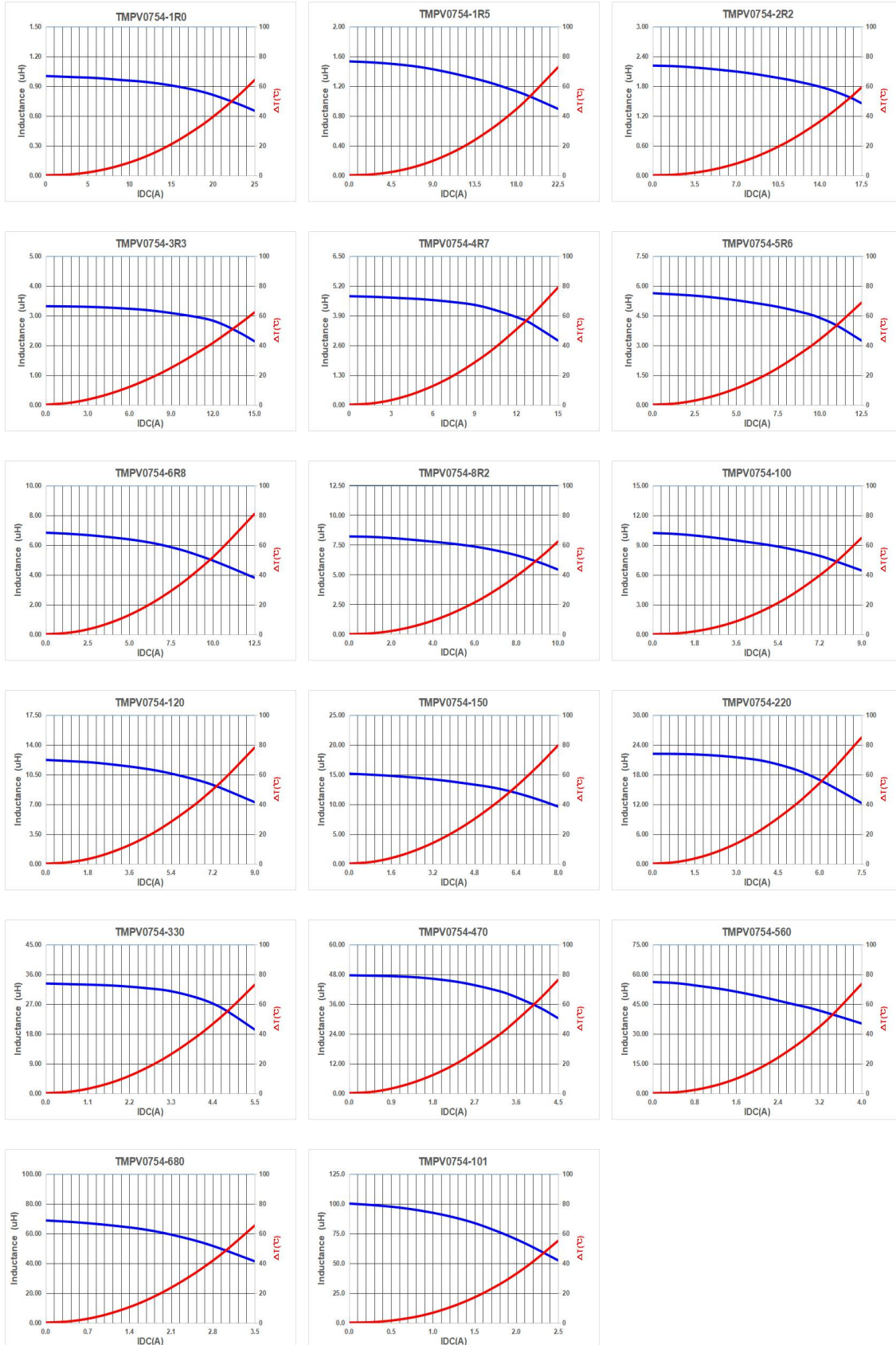
## 10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after  $48 \pm 2$  hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product.PCB washing tested to MIL-STD-202 Method 1 and dry it off immediately .
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappears.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification.

### **Application Notice**

- Storage Conditions(component level)  
To maintain the solderability of terminal electrodes:
  1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
  2. Temperature and humidity conditions:Less than40℃,85%RH.
  3. Recommended products should be used within 12 months form the time of delivery.
  4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

### 11. Typical Performance Curves



## 12、 Appearance criterion

### 一、 Introduction :

#### 1. Scope :

This document was written for the purpose of helping customers better understand the **TMPA**、**TMHC**、**TMPV** products they are purchasing. It will give the customer an idea as to the type of cosmetic irregularities that may occur from time to time during the manufacturing of the component itself, or during their use of the component.

This document also discusses the criteria that have been developed for the rejection of irregularities that are determined to be excessive.

While it is desirable to have cosmetically perfect **TMPA**、**TMHC**、**TMPV** inductors, the powdered iron manufacturing technique has cosmetic limitations.

Certified test labs have performed extensive environmental testing on **TMPA**、**TMHC**、**TMPV** inductors with and without cosmetic imperfections according to AEC-Q200 standards for thermal shock ,mechanical shock, vibration, humidity, and others. This testing has shown that the cosmetic imperfections listed in this document do not affect the performance or reliability of the **TMPA**、**TMHC**、**TMPV** inductors.

Test results are available upon request.

#### 2.Product :

The **TMPA**、**TMHC**、**TMPV** inductors are different from most inductors. The inductor body is a soft magnetic composite (SMC), not a ferrite. It is made from an iron powder mixture and cemented together using a resin binder. This powder mixture, when pressed around the inductor coil, greatly enhances the electrical properties of the inductor and gives protection from environmental forces. After pressing, the component is cured in an oven to increase the bonding strength of the resin binders with the iron powder, yielding excellent electrical and physical properties.

#### 3. The **TMPA**、**TMHC**、**TMPV** inductors provide the best combination of:

- Inductance
- Low core loss
- Saturation
- Temperature stability
- Smallest footprint
- Lowest profile

### 二、 Surface irregularities :

The following pages include descriptions of the most common irregularities seen on **TMPA**、**TMHC**、**TMPV** inductors. Common causes are described along with variations in their magnitude. Customers may sometimes see one or all of these irregularities.

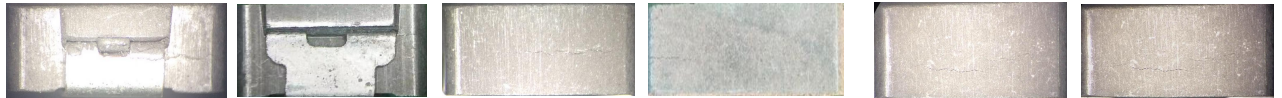
Those that are determined to adversely affect the customer's use of the component are rejected, though minor (acceptable) irregularities can occasionally be present. With the use of this guide, a customer will have a better understanding of the effect of each irregularity.



### 1. Cracks :

Cracks within the inductor body are unavoidable during the manufacturing process. Small cracks are caused by die wall friction when the parts are ejected during the pressing process, and by expansion of the coil during the process of curing the resin binder in the powdered iron body. Unlike ferrite material, cracks on the body do not affect the electrical performance of the component.

Reliability testing has shown that even cracks in excess of 0.005 inch will not cause the component to fail electrically or physically in field applications. Acceptance widths are adopted based on the ability to detect cracks both at the component and circuit level.



TMPA&TMHC

TMPV

TMPA&TMHC

TMPV

TMPA&TMHC

TMPV

Terminal area crack, acceptable

Cracks coming from the top corner of the terminal are normal and are caused by terminal expansion during curing operations.

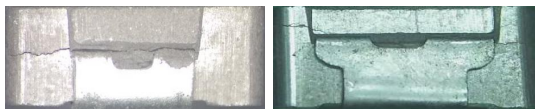
Negligible crack, acceptable

Negligible cracks are those that are nearly invisible without magnification.

Minor crack, acceptable

Minor cracks are those that are visible without magnification but are not apparent without close inspection.

#### 1-1. Crack :



TMPA&TMHC

TMPV

Moderate crack, rejectable

Moderate cracks are those that are obvious upon inspection and extend across most of the component.



TMPA&TMHC

TMPV

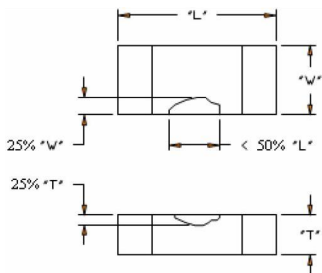
Major crack, rejectable

Major cracks are those that are obvious to a customer and would possibly result in large chip-outs that would expose the coil and lead frame.

### 2. Chip off :

Chipping of the inductor body can occur during normal processing and testing of the inductor. The acceptance criteria for chipping vary with the size of the component, our current acceptance standards are based on IPC-A-610. The effect of chipping is negligible as long as the inductor coil is not showing.

See IPC standard for class 1 and 2 components below.



T	25 % of the thickness
W	25 % of the width
L	50 % of the length

Chips typically occur on the edges and corners of the inductor body. They are slightly darker in color and rougher in appearance than the surrounding material.



TMPA&TMHC

TMPV

TMPA&TMHC

TMPV

TMPA&TMHC

TMPV

Minor chipping, acceptable

Minor chips in the inductor body are those that are typically shallow imperfections that occur on the corners and edge of components.

No coil wire or lead frame is showing and the chip does not affect the performance or reliability of the component.

Major chipping, rejectable

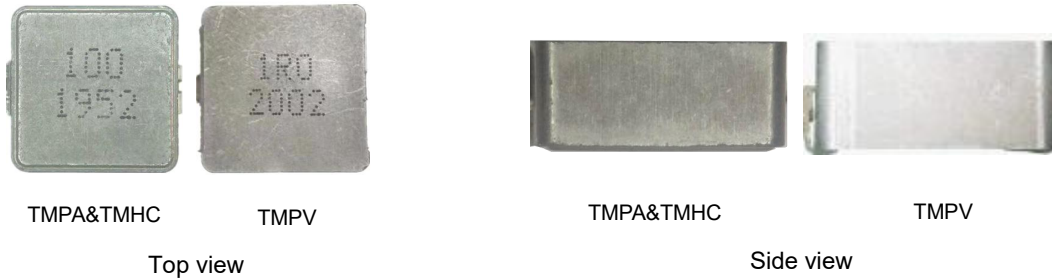
Major chips in the inductor body are those that are very obvious to the customer and may expose the wire coil or lead frame.

### 3. Oxidation :

The **TMPA**、**TMHC**、**TMPV** inductors is predominately iron, and oxidation may occur in a small percentage of inductors. Resin binders give moderate protection, but some slight oxidation may occur. All components should be stored away from heat, humidity, and ionized atmospheres as much as possible before mounting.

Basic steps should be taken in order to limit surface oxidation, including keeping the **TMPA**、**TMHC**、**TMPV** inductors sealed in their packaging until PCB mounting.

In case that oxidation does occur, the effects are contained only in the surface of the component and will not penetrate into the core material. No electrical effects have ever been documented due to oxidation of the **TMPA**、**TMHC**、**TMPV** products. Oxidation should never be considered a reliability risk.



TMPA&TMHC

TMPV

Top view

TMPA&TMHC

TMPV

Side view

### 4.Other :

A very small number of other irregularities have been reported. These occur at an exceedingly low rates and typically do not affect the components electrically. These include: Foreign material may be seen pressed into the upper terminals. This material is of the same material as the inductor body and should not be a reason for rejection unless solderability is affected.



TMPA&TMHC

TMPV

Foreign material: acceptable



TMPA&TMHC

TMPV

Scratch: acceptable

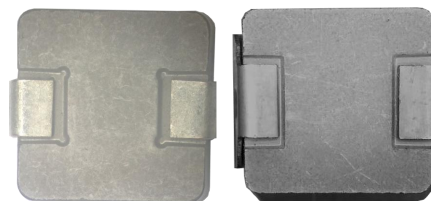
Scratches may be seen on the surface of the inductor body. Scratches are an acceptable surface irregularity.



TMPA&TMHC

TMPV

Imprinting : acceptable



TMPA&TMHC

TMPV

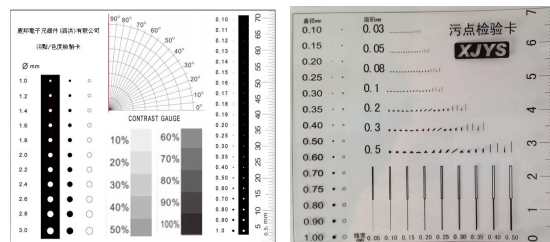
Blackening:PAD black/ brightness ratio less than 20% is OK



TMPA&TMHC

TMPV

Yellowing : PAD yellowing ratio less than 20% is OK



### ≡ Summary :

The **TMPA**、**TMHC**、**TMPV** inductors are comprised of an iron powder body compressed around a coil. Due to the fact that this iron powder body is not as solid like sintered ferrite material, irregularities such as cracks and chips do not affect the electrical properties or the reliability of the component. Criteria have been determined for the acceptability of the components that allow for a robust manufacturing process as well as an acceptable degree of cosmetic irregularity.

Reliability testing has been done on the effects of cracking of the iron powder body and on the oxidation of the iron particles that are present on the surface. Testing has shown no reliability issues from either of these cosmetic differences, Please feel free to use it!

The products described herein and this document technical questions and specific disclaimer, If you have any questions or need, please contact our corresponding business specialist or E-mail at [sales@tai-tech.com.tw](mailto:sales@tai-tech.com.tw).

Thank you for your support!

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