### KAMAYA OHM

No.: RNC-K-HTS-0001 /11

Date: 2020. 5. 15

# Data sheet

Title: FIXED THIN FILM CHIP RESISTORS;

**RECTANGULAR TYPE** 

Style: RNC06, 20, 32

# RoHS COMPLIANCE ITEM Halogen and Antimony Free

Note: •Stock conditions

Temperature:  $+5^{\circ}\text{C} \sim +35^{\circ}\text{C}$ Relative humidity:  $25\% \sim 75\%$ 

The period of guarantee: Within 2 year from shipmen t by the company.

Solderability shall be satisfied.

- Product specification contained in this data sheet are subject to change at any time without notice
- •If you have any questions or a Purchasing Specification for any quality Agreement is necessary, please contact our sales staff.



Hokkaido Research Center Approval by: T. Sannomiya Drawing by: M. Shibuya MAYA OHM Drawing No: RNC-K-HTS-0001 /11

Style

Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 1/11

#### 1. Scope

1.1 This specification covers the detail requirements for fixed thin film chip resistors; rectangular type & precision, style of RNC06, 20, 32.

#### 1.2 Applicable documents

JIS C 5201–1:2011, JIS C 5201–8: 2014, JIS C 5201–8–1: 2014 IEC60115–1: 2008, IEC60115–8: 2009, IEC60115–8–1: 2014 EIAJ RC-2133C-2010

#### 2. Classification

Type designation shall be the following form.

(Example)

RNC	32	E	1002	В	TP
1	2	3	4	5	6
Sty	le				

- 1 Fixed thin film chip resistors; rectangular type
- 2 Size
- 3 Temperature coefficient of resistance

Е	±25×10 <sup>6</sup> / °C
C	±50×10 <sup>6</sup> / °C

4 Rated resistance Example;  $1002 \rightarrow 10 \text{k}\Omega$ 

103	E24 Series, 3 digit,	Ex. 103> 10kΩ,
1002	E96 Series, 4 digit,	Ex. 1002>10kΩ
		10R2> 10.2kΩ

#### 5 Tolerance on rated resistance

В	±0.1%
С	±0.25%
D	±0.5%
F	±1%

#### 6 Packaging form

В	Bulk (loose package)	
PA	Press pocket taping	
TP	Paper taping	

/11

Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 2/11

#### 3. Rating

3.1 The ratings shall be in accordance with Table–1.

Table-1

Style	Rated dissipation (W)	Temperature coefficient of resistance (10-6/°C)	Rated resistance range( $\Omega$ )	Preferred number series for resistors	Tolerance on rated resistance
DNCOG	0.05	E: ±25 C: ±50	100~12k		B(±0.1%)
RNC06	0.05	E: ±25	27~12k		D(±0 50/.) E(±10/.)
		C: ±50	27~22.1k	E24,96	D(±0.5%),F(±1%)
RNC20	0.1		100~130k	LZ-1,50	B(±0.1%)
RINCZU	0.1	E. 105	10~130k		C(±0.25%), D(±0.5%)
DNC33	0.125	E: ±25	100~180k		B(±0.1%)
RNC32	0.125		10~180k		C(±0.25%), D(±0.5%)

Style	Limiting element	Insulation Voltage	Category temperature
Style	voltage (V)	(V)	range(°C)
RNC06	15	50	
RNC20	100	100	<i>–</i> 55∼+155
RNC32	200	100	

#### 3.2 Climatic category

55/155/56 Lower category temperature -55 °C
Upper category temperature +155 °C
Duration of the damp heat, steady state test 56days

#### 3.3 Stability class

1% Limits for change of resistance:

- for long - term tests  $\pm (1.0\% + 0.05\Omega)$ - for short - term tests  $\pm (0.25\% + 0.05\Omega)$ 

#### 3.4 Derating

The derated values of dissipation at temperature in excess of 70°C shall be as indicated by the following curve.

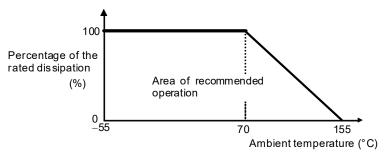


Figure-1 Derating curve

Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 3/11

#### 3.5 Rated voltage

d.c.or a.c.r.m.s.voltage calculated from the square root of the product of the rated resistance and the rated dissipation.

$$E = \sqrt{P \cdot R}$$

E: Rated voltage (V)

P: Rated dissipation (W)

R: Rated resistance ( $\Omega$ )

Limiting element voltage can only be applied to resistors when the resistance value is equal to or higher than the critical resistance value.

At high value of resistance, the rated voltage may not be applicable.

#### 4. Packaging form

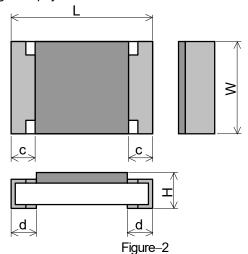
The standard packaging form shall be in accordance with Table-2.

Table-2

Symbol	Packaging form		Standard packaging quantity / units	Application
В	Bulk (loose package)		1,000 pcs.	RNC06, 20, 32
PA	Press pocket taping (paper taping)	8mm width, 2mm pitches	15,000 pcs.	RNC06
TP	Paper taping	8mm width, 4mm pitches	5,000 pcs.	RNC20, 32

#### 5. Dimensions

5.1 The resistor shall be of the design and physical dimensions in accordance with Figure-2 and Table-3.



Table–3 Unit: mm

Style	L	W	Н	С	d
RNC06	0.6±0.03	0.3±0.03	0.23±0.03	0.1±0.05	0.15±0.05
RNC20	2.0±0.15	1.25 <sup>+0.10</sup> <sub>-0.05</sub>	0.6±0.1	0.4±0.2	$0.3^{+0.2}_{-0.1}$
RNC32	3.1±0.1	1.55 <sup>+0.10</sup> <sub>-0.05</sub>	0.6±0.1	0.45±0.20	$0.3^{+0.2}_{-0.1}$

#### 5.2 Net weight (Reference)

	, ,
Style	Net weight(mg)
RNC06	0.16
RNC20	5
RNC32	9



FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 4/11

#### 6. Marking

The Rated resistance shall be marked in 3 digits (E24) or 4 digits (E96) and marked on over coat side.

• E24 series: 3 digits, E96 series: 4 digits

In case of the resistance value that E96 overlaps with E24, It is marked by either.

The Rated resistance of RNC06 should not be marked.

Marking example	Contents	Application
123	12×10 <sup>3</sup> $[\Omega] \rightarrow$ 12 $[k\Omega]$	RNC20,32
12R7	12.7 [Ω]	E96, Less than 100Ω of RNC20,32
5623	$562\times10^{3} [\Omega] \rightarrow 562[k\Omega]$	RNC20,32

#### 7. Performance

- 7.1 The standard condition for tests shall be in accordance with Sub-clause 4.2, JIS C 5201-1: 2011.
- 7.2 The performance shall be satisfied in Table-4.

Table-4(1)

No.	Test items	Condition of test (JIS C 5201–1)	Performance requirements
1	Visual examination	Sub-clause 4.4.1 Checked by visual examination.	As in 4.4.1 The marking shall be legible, as checked by visual examination.
2	Dimension	Sub-clause 4.4.2	As specified in Table–3 of this specification.
	Resistance	Sub-clause 4.5	As in 4.5.2  The resistance value shall correspond with the rated resistance taking into account the specified tolerance.
3	Voltage proof	Sub-clause 4.7 Method: 4.6.1.4(See Figure-3) Test voltage: Alternating voltage with a peak value of 1.42 times the insulation voltage. Duration: 60 s ± 5 s Insulation resistance Test voltage: Insulation voltage Duration: 1 min.	No breakdown or flash over $R \geq 1 \ G \ \Omega$
4	Solderability	Sub-clause 4.17 Without ageing Flux: The resistors shall be immersed in a non-activated soldering flux for 2s. Bath temperature: 235 °C ± 5 °C Immersion time: 2 s ± 0.5 s	As in 4.17.4.5 The terminations shall be covered with a smooth and bright solder coating.



MAYA OHM Drawing No: RNC-K-HTS-0001 /11

Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 5/11

Table-4(2)

No	Test items	Condition of test (JIS C 5201–1)	Performance requirements			
5	Mounting	Sub-clause 4.31				
		Substrate material: Epoxide woven glass				
	Overload	Sub-clause 4.13				
	(in the mounted state)	The applied voltage shall be 2.5 times the				
		rated voltage or twice the limiting element				
		voltage, whichever is the less severe.				
		Duration: 2 s	Ni a 2 2 1 a di anno anno			
		Visual examination	No visible damage			
	Columb maniphones of the	Resistance	$\Delta R \le \pm (0.25\% + 0.05\Omega)$			
	Solvent resistance of the	Sub-clause 4.30	Legible marking			
	marking	Solvent: 2-propanol				
		Solvent temperature: 23 °C ± 5 °C				
		Method 1				
		Rubbing material: cotton wool				
-	May vertice	Without recovery				
6	Mounting	Sub-clause 4.31				
		Substrate material: Epoxide woven glass				
	Bound strength of the end face	Sub-clause 4.33				
	plating	Bent value: 3 mm				
		Resistance	$\Delta R \le \pm (0.25\% + 0.05\Omega)$			
	Final measurements	Sub-clause 4.33.6	(=====,			
		Visual examination	No visible damage			
7	Resistance to soldering heat	Sub-clause 4.18	-			
		Solder temperature: 260 °C ± 5 °C				
		Immersion time: 10 s ± 0.5 s				
		Visual examination	As in 4.18.3.4			
			No sign of damage such as cracks.			
		Resistance	$\Delta R \le \pm (0.25\% + 0.05\Omega)$			
	Component solvent resistance	Sub-clause 4.29				
		Solvent: 2-propanol				
		Solvent temperature: 23 °C ± 5 °C				
		Method 2				
		Recovery: 48 h				
		Visual examination	No visible damage			
		Resistance	$\Delta R \le \pm (0.25\% + 0.05\Omega)$			

FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

Page: RNC06, 20, 32 6/11

Table-4(3)

No	Test items	Condition of test (JIS C 5201–1)	Performance requirements		
8	Mounting	Sub-clause 4.31	r enormance requirements		
8	IVIOUITIIIII	Substrate material: Epoxide woven glass			
		Substrate material. Epoxide woverriglass			
	Adhesion	Sub-clause 4.32			
	Adilesion				
		Force: 5 N (RNC06: 3N)			
		Duration: 10 s ± 1 s	No visible damage		
	Rapid change temperature	Visual examination	140 Visible darriage		
	Tapia Glarige temperature	Sub-clause 4.19			
		Lower category temperature: -55 °C			
		Upper category temperature: +155 °C			
		Duration of exposure at each temperature: 30			
		min.			
		Number of cycles: 5 cycles.	No visible damage		
		Visual examination	$\Delta R \le \pm (0.25\% + 0.05\Omega)$		
^	Oli ti	Resistance	Zi( \(\frac{1}{2}\) \(\frac{1}\) \(\frac{1}{2}\) \(\frac{1}{2}\) \(\frac{1}\) \(\frac{1}\) \(\frac{1}\) \(\fra		
9	Climatic sequence	Sub-clause 4.23			
	-Dry heat	Sub-clause 4.23.2			
		Test temperature: + 155 °C			
		Duration: 16 h			
	-Damp heat, cycle	Sub-clause 4.23.3			
	(12+12hour cycle)	Test method: 2			
	First cycle	Test temperature: 55 °C			
		[Severity(2)]			
	-Cold	Sub-clause 4.23.4			
		Test temperature –55 °C			
		Duration: 2h			
	–Damp heat, cycle	Sub-clause 4.23.6			
	(12+12hour cycle)	Test method: 2			
	Remaining cycle	Test temperature: 55 °C			
		[Severity (2)]			
		Number of cycles: 5 cycles			
	–D.C. load	Sub-clause 4.23.7			
		The applied voltage shall be the rated voltage			
		or the limiting element voltage whichever is			
		the smaller.			
		Duration: 1 min.	No visible damage		
		Visual examination	$\Delta R \le \pm (1\% + 0.05\Omega)$		
40	<b>NA</b> (1)	Resistance	2.1.12 (176.0.0022)		
10	Mounting	Sub-clause 4.31			
		Substrate material: Epoxide woven glass			
	F., d				
	Endurance at 70 °C	Sub-clause 4.25.1			
		Ambient temperature: 70 °C ± 2 °C			
		Duration: 1000 h			
		The voltage shall be applied in cycles of 1.5 h			
		on and 0.5 h.			
		The applied voltage shall be the rated voltage			
		or the limiting element voltage whichever is			
		the smaller.			
		Examination at 48 h, 500 h and			
		1000 h:	No visible domass		
		Visual examination	No visible damage		
		Resistance	$\Delta R \le \pm (1\% + 0.05\Omega)$		

Drawing No: RNC-K-HTS-0001

/11

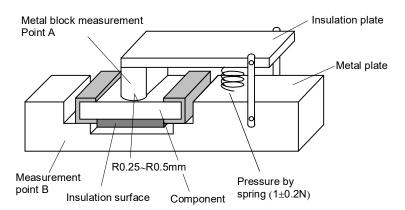
FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE
RNC06, 20, 32
Page: 7/11

Table-4(4)

No	Test items	Condition of test (JIS C 5201–1)	Performance requirements		
11	Mounting	Sub-clause 4.31 Substrate material: Epoxide woven glass			
	Variation of resistance with temperature	Sub-clause 4.8 +20 °C / +125°C	As in Table–1		
12	Mounting	Sub-clause 4.31 Substrate material: Epoxide woven glass			
	Damp heat, steady state	Sub-clause 4.24 Ambient temperature: 40 °C ± 2 °C Relative humidity: 93 +2/3 % a) 1st group: without voltage applied. b) 2nd group: The d.c.voltage shall be applied continuously. The voltage shall be accordance with Sub-clause 4.24.2.1 b). without polarizing voltage [4.24.2.1, c)] Visual examination Resistance	No visible damage Legible marking $\Delta R \le \pm (1\% + 0.05\Omega)$		
13	Dimensions (detail)  Mounting	Sub-clause 4.4.3  Sub-clause 4.31  Substrate material: Epoxide woven glass	As in Table–3		
	Endurance at upper category temperature	Sub-clause 4.25.3 Ambient temperature: 155 °C ± 2 °C Duration: 1000 h Examination at 48 h, 500 h and 1000 h: Visual examination Resistance	No visible damage $\Delta R \le \pm (1\% + 0.05\Omega)$		

<sup>\*</sup> Voltage proof method

· RNC20, 32 · RNC06



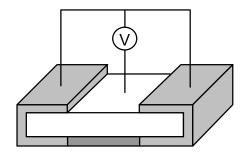


Figure-3

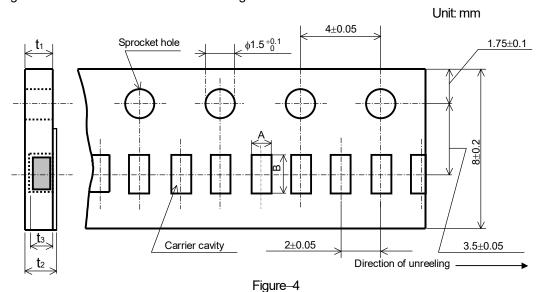
Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 8/11

#### 8. Taping

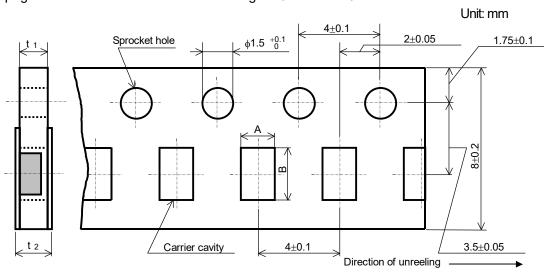
- 8.1 Applicable documents JIS C 0806-3: 2014, EIAJ ET-7200C: 2010
- 8.2 Taping dimensions
- 8.2.1 Press pocket taping (Paper taping, 8mm width, 2mm pitches)

Taping dimensions shall be in accordance with Figure-4 and Table-5.



#### 8.2.2 Paper taping (8mm width, 4mm pitches)

Taping dimensions shall be in accordance with Figure-5 and Table-6.



 Figure\_5

 Table\_6
 Unit: mm

 Style
 A
 B
 t<sub>1</sub>
 t<sub>2</sub>

 RNC20
 1.65 ± 0.15
 2.5 ± 0.2
 0.8 ± 0.1
 1.0max.

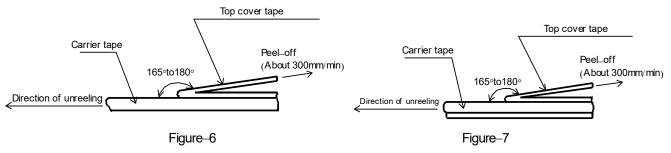
 RNC32
 2.00 ± 0.15
 3.6 ± 0.2
 0.8 ± 0.1
 1.0max.

Drawing No: RNC-K-HTS-0001 /11

FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

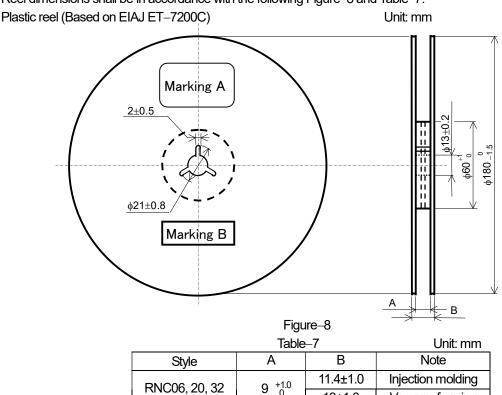
RNC06, 20, 32 Page: 9/11

- 1). The cover tapes shall not cover the sprocket holes.
- Tapes in adjacent layers shall not stick together in the packing.
- 3). Components shall not stick to the carrier tape or to the cover tape.
- 4). Pitch tolerance over any 10 pitches ±0.2mm.
- 5). The peel strength of the top cover tape shall be with in 0.1N to 0.5N on the test method as shown in the following RNC06: Figure-6, RNC20, 32: Figure-7.
- 6). When the tape is bent with the minimum radius for 25 mm, the tape shall not be damaged and the components shall maintain their position and orientation in the tape.
- 7). In no case shall there be two or more consecutive components missing. The maximum number of missing components shall be one or 0.1%, whichever is greater.
- 8). The resistors shall be faced to upward at the over coating side in the carrier cavity.



#### 8.3 Reel dimension

Reel dimensions shall be in accordance with the following Figure-8 and Table-7.



Note: Marking label shall be marked on a place of Marking A or two place of marking A and B.

RNC06, 20, 32

13±1.0

Vacuum forming

Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

RNC06, 20, 32 Page: 10/11

#### 8.4 Leader and trailer tape.

(Example)

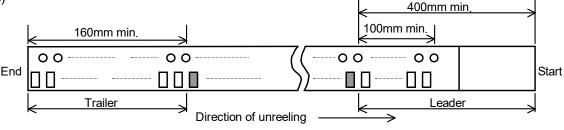


Figure-9

#### 9. Marking on package

The label of a minimum package shall be legibly marked with follows.

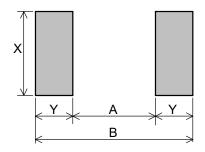
#### 9.1 Marking A

(1) Classification

(Style, Temperature coefficient of resistance, Rated resistance, Tolerance on rated resistance, Packaging form)

- (2) Quantity (3) Lot number (4) Manufacturer's name or trade mark (5) Others
- 9.2 Marking B (KAMAYA Control label)

#### 10. Recommended land pattern



Unit::mm

Size		Flow soldering		Reflow soldering					
Metric	Inch	Α	В	Х	Υ	Α	В	Х	Υ
0603	0201		Not	applied		0.3	0.9	0.3	0.3
2012	0805	1.3	3.1	1.25	0.9	1.3	2.7	1.25	0.7
3216	1206	22	4.3	16	1 05	22	3.9	1.6	0.85

Title: FIXED THIN FILM CHIP RESISTORS; RECTANGULAR TYPE

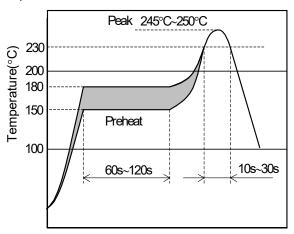
RNC06, 20, 32 Page: 11/11

#### 11. Recommended soldering condition

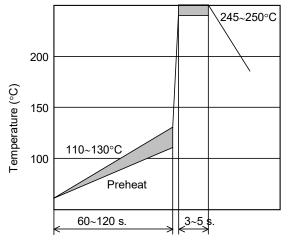
The following condition corresponds to the lead free paste and the lead content paste.

11.1 Reflow Soldering (Temperature profile)

Reflow times: 2 times



#### 11.2 Wave Soldering (Temperature profile)



#### 11.3 Soldering iron

- (1) Temperature of soldering iron tip: 300 °C, Duration: Within 10s
- (2) Temperature of soldering iron tip: 350 °C, Duration: Within 3 s

## 单击下面可查看定价,库存,交付和生命周期等信息

## >>Kamaya(釜屋电机)