то :		-	文件编号					
	承	认规格		2017 + 03 7				
种 类: <u>Common Mode Filter</u> 系列号: <u>HXA0805F2SF-900T04</u> 客户料号:								
客户承认栏								
承 i	人日期	年	月	Ħ				

(贵司承认后请签署一份返回华信安电子,谢谢!)

# 厦门华信安电子科技有限公司技术质量部

承认	确认	作成
龙梅	梁峰	王亮

TEL: 0592-6301603 FAX: 0592-5205265

Http://www.xmisnd.com



Wire Wound Type Common Mode Filter

HXA0805F2SF-900T04

	ECN HISTORY LIST										
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN						
1.0	19/03/22	新發行	龙梅	梁峰	王亮						
備											



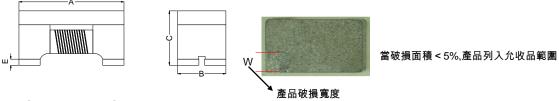
# Wire Wound Type Common Mode Filter

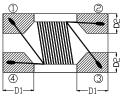
#### HXA0805F2SF-900T04

### 1.Features

- 1. High common mode impedance at high frequency cause excellent noise suppression performance.
- 2. HXA0805F2SF series realizes small size and low profile. 2.0x1.2x1.2 mm.
- 3. 100% Lead(Pb) & Halogen-Free and RoHS compliant.

## 2.Dimension





Series	A(mm)	B(mm)	C(mm)	D1(mm)	D2(mm)	E(mm)
0805F2SF	2.0±0.2	1.2±0.2	1.2±0.2	0.50±0.1	0.51±0.1	0.15±0.1

# **3.Part Numbering**

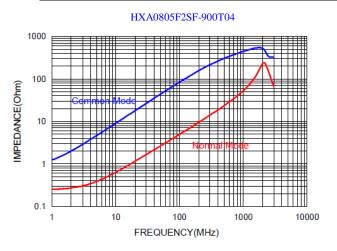
HXA	0805	F	2	S	F	-	900	-	T	<b>04</b>
А	В	С	D	Е	F		G		Н	Ι
A: Seri B: Dim										
C: Mat	erial	Fer	rite Core							
D: Nun	nber of Line	s 2=2	2 lines							
Е: Туре	e	S=	Shielded	, N=Unshi	elded					
F: Lead	d free									
G: Imp	edance	900	0=90Ω							
H: Pac	kaging	T=	Faping ar	nd Reel						
I: Rate	d Current	04:	=400mA							

# **4.Specification**

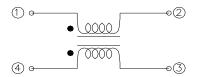
ISND Part Number			Test Frequency (MHz) DC Resistance (Ω) max.		Rated Volt. (Vdc)max.	Withstand Volt. (Vdc) max.	IR (Ω) min.
HXA0805F2SF-900T04	90±25%	100	0.30	400	50	125	10M



# Typical Impedance v.s. Frequency Curve

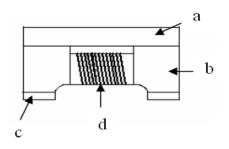


# 5.Schematic Diagram



# 6. Materials

No.	Description	Specification
a.	Upper Plate	Ferrite
b.	Core	Ferrite Core
с	Termination	Tin (Pb Free)
d	Wire	Enameled Copper Wire





# 7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	-40~+125°C (on board)	
Electrical Performance	Test	I
Z(common mode)		Agilent-4291A+ Agilent -16197A
DCR	Refer to standard electrical characteristics list.	Agilent-4338B
I.R.		Agilent4339
Temperature Rise Test	Rated Current < 1A ΔT 20°CMax Rated Current ≧ 1A ΔT 40°CMax	1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow Profiles) Temperature : 125±2°C Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs
Load Humidity		Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles Humidity : 85±2%R.H, Temperature : 85°C±2°C Duration : 1000hrs Min. with 100% rated current Measured at room temperature after placing for 24±2 hrs
Moisture Resistance	Appearance : No damage. Impedance : within±15% of initial value RDC : within ±15% of initial value and shall not exceed the specification value	<ul> <li>Measured at room temperature after placing for 24±2 hrs</li> <li>Preconditioning: Run through IR reflow for 2 times.(IPC/JEDEC J-STD-020DClassification Reflow</li> <li>Profiles</li> <li>1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs.</li> <li>2. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in 2.5hrs.</li> <li>3. Raise temperature to 65±2°C 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25°C in</li> <li>2.5hrs,keep at 25°C for 2 hrs then keep at -10°C for 3 hrs</li> <li>4. Keep at 25°C 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1–2 hrs.</li> </ul>
Thermal shock		J-STD-020DClassification Reflow Profiles Condition for 1 cycle Step1 : -40±2°C 30±5min Step2 : 25±2°C ≦0.5min Step3 : 125±2°C 30±5min
Vibration		Number of cycles : 500 Measured at room temperature after placing for 24±2 hrs Oscillation Frequency: 10 ~ 2K ~ 10Hz for 20 minutes Equipment : Vibration checker Total Amplitude:1.52mm±10% Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations)。



Performance	Test Condition				
Appearance : No damage.	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.				
Impedance : within±15% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Peak         Normal duration (D)         Wave form         Velocity change (Vi)ft/sec           SMD         50         11         Half-sine         11.3				
	Lead         50         11         Half-sine         11.3				
More than 95% of the terminal electrode should be covered with solder。	Preheat: 150°C,60sec.。 Solder: Sn96.5% Ag3% Cu0.5% Temperature: 245±5°C。 Flux for lead free: Rosin. 9.5%。 Dip time: 4±1sec。 Depth: completely cover the termination				
	Depth: completely cover the termination         Temperature(°C)       Time(s)         Tamp/immersion and emersion rate       Number of heat cycles         260 ±5 (solder temp)       10 ±1       25mm/s ±6 mm/s       1				
Appearance : No damage. Impedance : within±15% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 2 times.( IPC/JEDEC J-STD-020DClassification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a force(>0805:1kg, <=0805:0.5kg)to the side of a device being tested. This force shall be applied for 60+1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.				
	Appearance : No damage.         Impedance : within±15% of initial value         Q : Shall not exceed the specification value.         RDC : within±15% of initial value and shall not exceed the specification value         More than 95% of the terminal electrode should be covered with solder.         Appearance : No damage.         Impedance : within±15% of initial value         Q : Shall not exceed the specification value.         RDC : within±15% of initial value         Q : Shall not exceed the specification value.         RDC : within±15% of initial value and shall not				

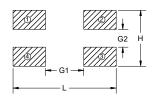




### 8.Soldering and Mounting

#### 8-1. Recommended PC Board Pattern

	HXA0805F2S/F2N
L(mm)	2.60
H(mm)	1.40
G1(mm)	1.25
G2(mm)	0.45



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

Products shall be positioned in the sideway direction to against the mechanical stress to prevent failure.

#### 8-2. Soldering

Mildly activated rosin fluxes are preferred. ISND terminations are suitable for all wave and re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### 8-2.1 Lead Free Solder re-flow:

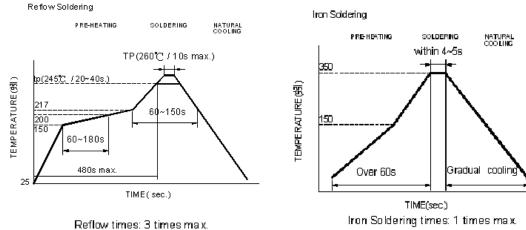
Recommended temperature profiles for re-flow soldering in Figure 1.

#### 8-2.2 Soldering Iron(Figure 2):

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. •Preheat circuit and products to 150°C ••Never contact the ceramic with the iron tip ••Use a 20 watt soldering iron with tip diameter of 1.0mm

-355°C tip temperature (max) -1.0mm tip diameter (max)

·Use a 20 watt soldering iron with tip diameter of 1.0mm ·Limit soldering time to 4~5 sec.



v limes. S limes max

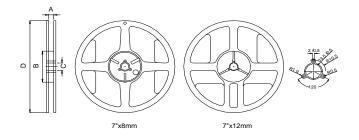
Fig.1



Fig.2

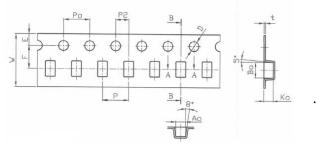
### 9. Packaging Information

#### 9-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)	
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2	

#### 9-2. Tape Dimension / 8mm

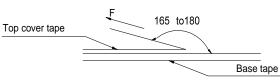


Series	W(mm)	P(mm)	E(mm)	F(mm)	P2(mm)	D(mm)	P0(mm)	A0(mm)	B0(mm)	K0(mm)	t(mm)
HXA0805F2S	8.00±0.10	4.00±0.10	1.75±0.10	3.50±0.05	2.00±0.05	1.50+0.10/-0.00	4.00±0.10	1.50±0.10	2.35±0.10	1.45±0.10	0.28±0.05

#### 9-3. Packaging Quantity

Chip size	Chip/Reel	Inner Box	Middle Box	Carton
HXA0805F2S/F2N	2000	10000	50000	100000

#### 9-4. Tearing Off Force



The force for tearing off cover tape is 15 to 80 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(°C)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

#### Application Notice

Storage Conditions

To maintain the solderability of terminal electrodes:

- 1. ISND products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C  $\,$  and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.

·Transportation

- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.



单击下面可查看定价,库存,交付和生命周期等信息

>>ISND(华信安)