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Sample data sheet

BAW filter 5.25 GHz Wi-Fi 6 UNII1-2a

Project:	AS77B
Ordering code:	[B39522B8377L210]
Date:	October 28, 2021
Version:	1.1
Note:	Sample data valid for lot P3074571 only. This sample data sheet represents devices of the lot listed above. It does not specify future devices, i.e. the final product may differ.

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1 Application

- Low-loss BAW RF coexistence filter for 5.25 GHz Wi-Fi 6 UNII1-2a
- 5.25 GHz Wi-Fi 6 UNII1-2a: pass band 5170 5330 MHz
- Usable pass band : 160 MHz

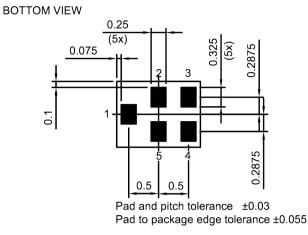
2 Features

- Package size 1.4±0.05 mm × 1.1±0.05 mm
- Package height 0.65 mm (max.)
- Approximate weight 0.1 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 3 (MSL3)



Figure 1: Picture of component with example of product marking.

3 Package



Pin configuration

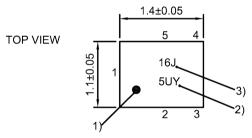
∎ 1 Input

4

- I 4 Output
- 2, 3, 5 Ground





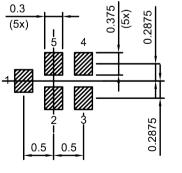


1) Marking for pad number 1

2) Example of encoded lot number

3) Example of encoded filter type number

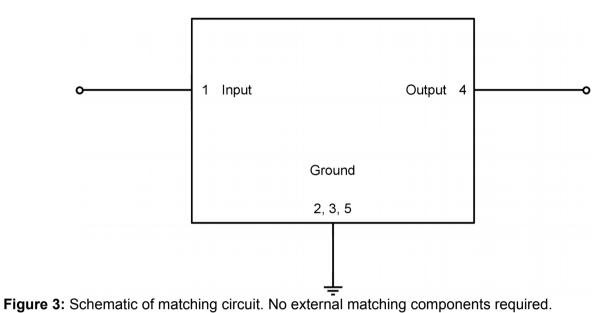
Land pattern THRU VIEW



Landing pad tolerance -0.02 **Figure 2:** Drawing of package with package height A = 0.65 mm (max.). See Sec. Package information (p. 17).



5 Matching circuit



Please read **Cautions and warnings** and **Important notes** at the end of this document.

6 **Characteristics**

Temperature range for specification	$T_{_{ m SPEC}}$	= −40 °C +85 °C
Input terminating impedance	Z _{IN}	= 50 Ω
Output terminating impedance	Z _{OUT}	= 50 Ω

				Development status ¹⁾			DGL ²⁾		
Characteristics			min. for $T_{_{ m SPEC}}$	typ. @ +25 °C	max. for T _{SPEC}	min.	max.		
Maximum insertion atten	nuation		α _{max}						
	5170 5330	MHz		_	1.5	2.1	_		dB
Insertion attenuation – W	VLAN		$\alpha_{_{WLAN}}^{~~3)}$						
	5170 5330	MHz		_	1.5	1.9			dB
Amplitude ripple (p-p)			Δα						
	5170 5330	MHz		_	0.4	1.0	_	_	dB
Maximum VSWR			VSWR _{max}						
@ input port	5170 5330	MHz		_	2.0	2.4	_	2.0	
@ output port	5170 5330	MHz		_	2.0	2.4		2.0	
Minimum attenuation			$\alpha_{_{min}}$						
	30 2250	MHz		37	39	_			dB
	2250 2400	MHz		25	30	_	_		dB
	2400 2500	MHz		25	30	_	_	_	dB
	2500 3000	MHz		35	40	_	_	_	dB
	3400 3800	MHz		35	41	—	—	—	dB
	3800 4900	MHz		35	37	—	—		dB
	5490 5895	MHz		49	53	—	—	_	dB
	5950 7125	MHz		37	41	—	—	-	dB
	7203 7500	MHz		35	39	—		-	dB
	10340 10660	MHz		—	33	—	_	—	dB

1) Values in columns min., typ., and max. indicate the development status of the current version.

2) Values in column design goal (DGL) indicate the target performance. Average over each WLAN channel with band width of 160 MHz.

3)

7 Maximum ratings

Operable temperature	<i>T</i> _{OP} = -40 °C +95 °C	
Storage temperature	$T_{\rm STG}^{(1)} = -40 ^{\circ}{\rm C} \dots +95 ^{\circ}{\rm C}$	
DC voltage	$ V_{\rm DC} ^{2} = 0 V (max.)$	
ESD voltage		
	$V_{\rm ESD}^{3)}$ = 100 V (max.)	Human body model.
	V _{ESD} ⁴⁾ = 100 V (max.)	Machine model.
Input power	P _{IN}	
@ input port: 5170 5330 MHz	28 dBm ⁶⁾	ON-state power of 160 MHz Wi-Fi downlink signal with 70% duty cycle for t.b.d. h @ 55 °C. Source and load impedance 50 Ω. ⁵⁾
@ input port: other frequency ranges	10 dBm	Continuous wave for 5000 h @ 55 °C. Source and load impedance 50 Ω .

¹⁾ Not valid for packaging material. Storage temperature for packaging material is -25 °C to +40 °C.

²⁾ In case of applied DC voltage blocking capacitors are mandatory.

³⁾ According to JESD22-A114F (HBM – Human Body Model), 1 negative & 1 positive pulse.

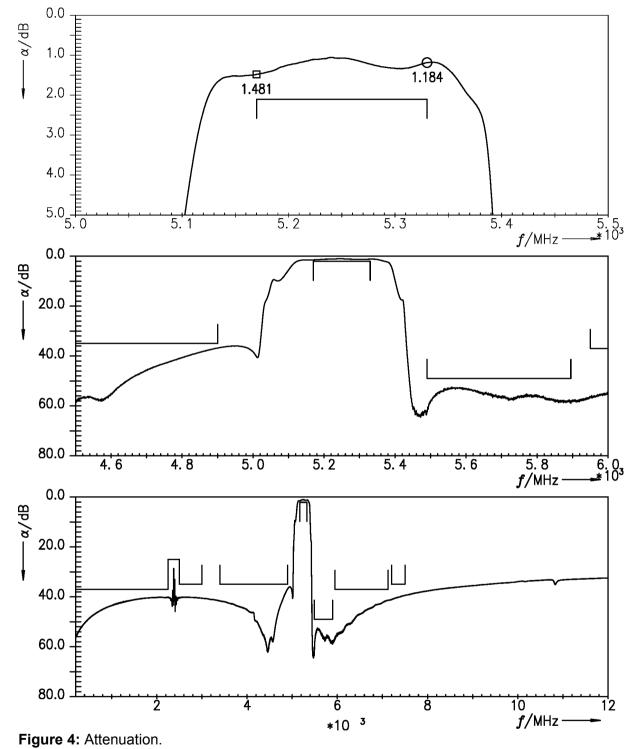
⁴⁾ According to JESD22-A115B (MM – Machine Model), 10 negative & 10 positive pulses.

⁵⁾ Expected lifetime according to accelerated power durability simulation and wear out models.

⁶⁾ Hardware test to be done.



8 Transmission coefficient



Please read **Cautions and warnings** and **Important notes** at the end of this document.

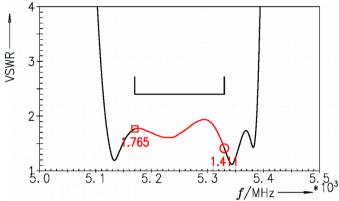


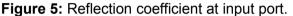
□ = 5170.0 O = 5330.0

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Z_{IN}=50 Ω

9 Reflection coefficients





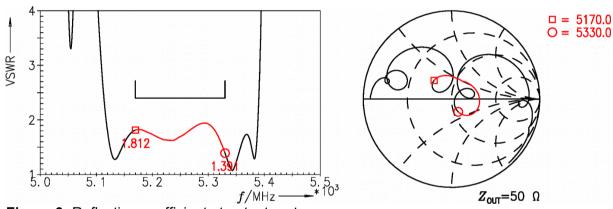
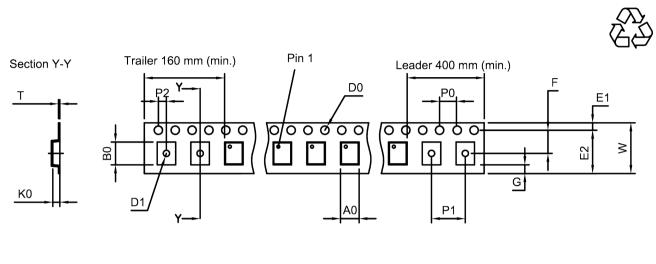


Figure 6: Reflection coefficient at output port.



10 Packing material

10.1 Tape



User direction of unreeling

Figure 7: Drawing of tape (first-angle projection) for illustration only and not to scale. The valid tape dimensions are listed in Table 1.

A ₀	-
B ₀	_
D ₀	-
D_1	-
E1	-

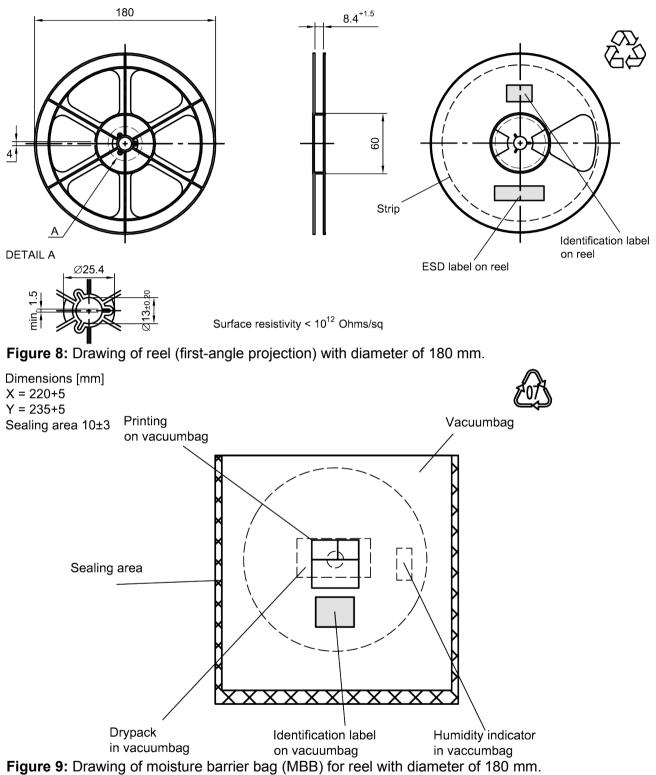
E ₂ –	-
F –	-
G –	-
K ₀ –	-
P ₀ –	-

P ₁	_
P_2	-
Т	-
W	-

Table 1: Tape dimensions.



10.2 Reel with diameter of 180 mm



Please read Cautions and warnings and Important notes at the end of this document.

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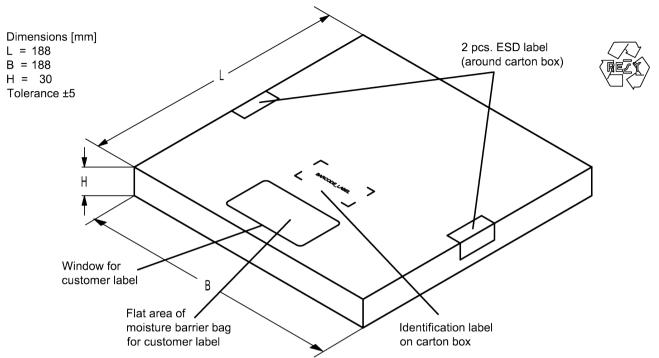


Figure 10: Drawing of folding box for reel with diameter of 180 mm.

10.3 Reel with diameter of 330 mm

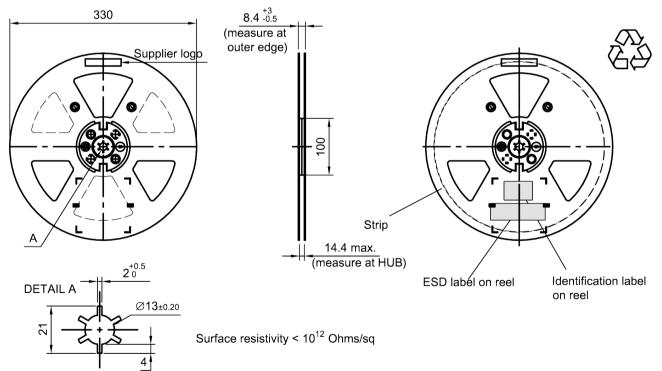


Figure 11: Drawing of reel (first-angle projection) with diameter of 330 mm.



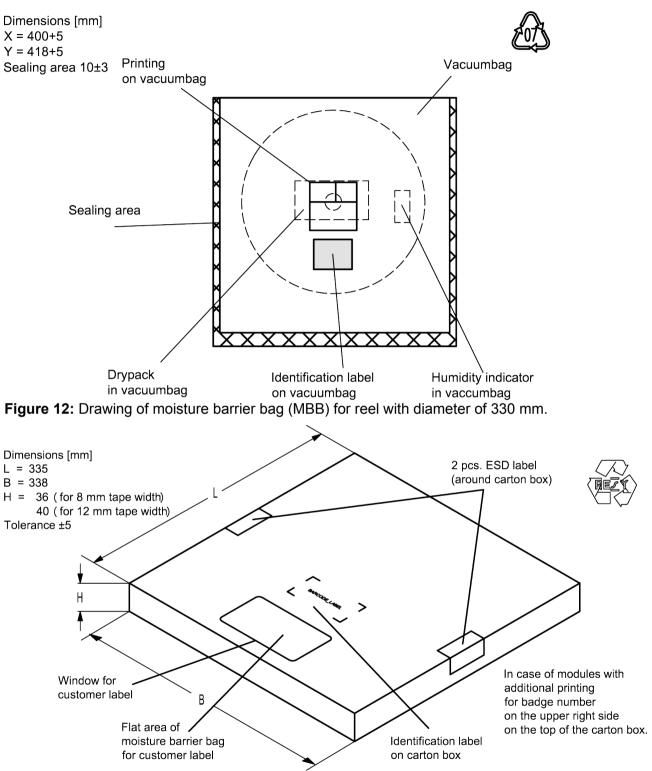


Figure 13: Drawing of folding box for reel with diameter of 330 mm.

11 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
<i>T</i> > 220 °C	30 s to 70 s
<i>T</i> > 230 °C	min. 10 s
<i>T</i> > 245 °C	max. 20 s
<i>T</i> ≥ 255 °C	-
peak temperature <i>T</i> _{peak}	250 °C +0/-5 °C
wetting temperature T _{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads
soldering temperature T	measured at solder pads

Table 2: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

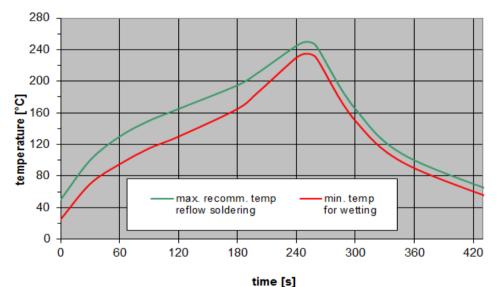


Figure 14: Recommended reflow profile for convection and infrared soldering – lead-free solder.

12 Annotations

12.1 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

12.2 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

13 Cautions and warnings

13.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under https://rffe.qualcomm.com/.

13.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

13.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

13.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Projection method

Unless otherwise specified first-angle projection is applied.

14 ESD protection of acoustic devices

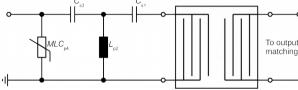
Acoustic devices are Electro Static Discharge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies must be applied.

In general, "ESD matching" must be ensured at that electrical port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the acoustic device must be designed to short circuit or to block the ESD pulse.

Below three figures show recommended "ESD matching" topologies.

For wide band acoustic devices the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and input port. The required component values must be determined from case to case.



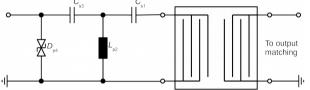


Figure 15: MLC varistor plus ESD matching.

Figure 16: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified "ESD matching" topologies can be used alternatively.

40

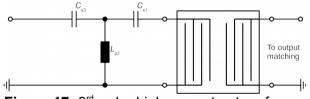


Figure 17: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: **"ESD protection for SAW filters".** This report can be found under <u>https://rffe.qualcomm.com</u>.



15 Revision history

Changes compared to previously issued iteration.

Version	Detailed specification changes	Date
1.1	Initial Release.	Oct 28, 2021



16 Important notes

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- 2. We also point out that in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
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