Qualcomm

RF360
Europe GmbH

Data sheet

SAW Rx filter

Beidou B1-2, B1-C, & B1-I; GPS L1; Galileo E1; GLONASS G1; GNSS L1

Part number:	B2624
Ordering code:	B39162B2624P810
Date:	March 11, 2021
Version:	2.1

DCN: 80-PA243-523 Rev. B

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Table of contents

1 Application

- GNSS:
  GNSS L1: pass band 1525 – 1605.89 MHz
  Beidou B1-I: pass band 1559.052 – 1563.144 MHz
  Beidou B1-C: pass band 1559.059 – 1591.788 MHz
  GPS L1: pass band 1565.42 – 1585.42 MHz
  Galileo E1: pass band 1573.374 – 1577.466 MHz
  Beidou B1-2: pass band 1587.694 – 1591.788 MHz
  GLONASS G1: pass band 1597.55 – 1605.89 MHz
- Minimized group delay variation in pass band

2 Features

- Package size $1.4_{\pm 0.1}$ mm × $1.1_{\pm 0.1}$ mm
- Package height 0.45 mm (max.)
- Approximate weight 3 mg
- RoHS compatible
- Package for Surface Mount Technology (SMT)
- Ni/Au-plated terminals
- Filter surface passivated
- Electrostatic Sensitive Device (ESD)
- Moisture Sensitivity Level 2a (MSL2a)
- AEC-Q200 qualified component family
  (Grade 2: −40 °C to +105 °C)

3 Package

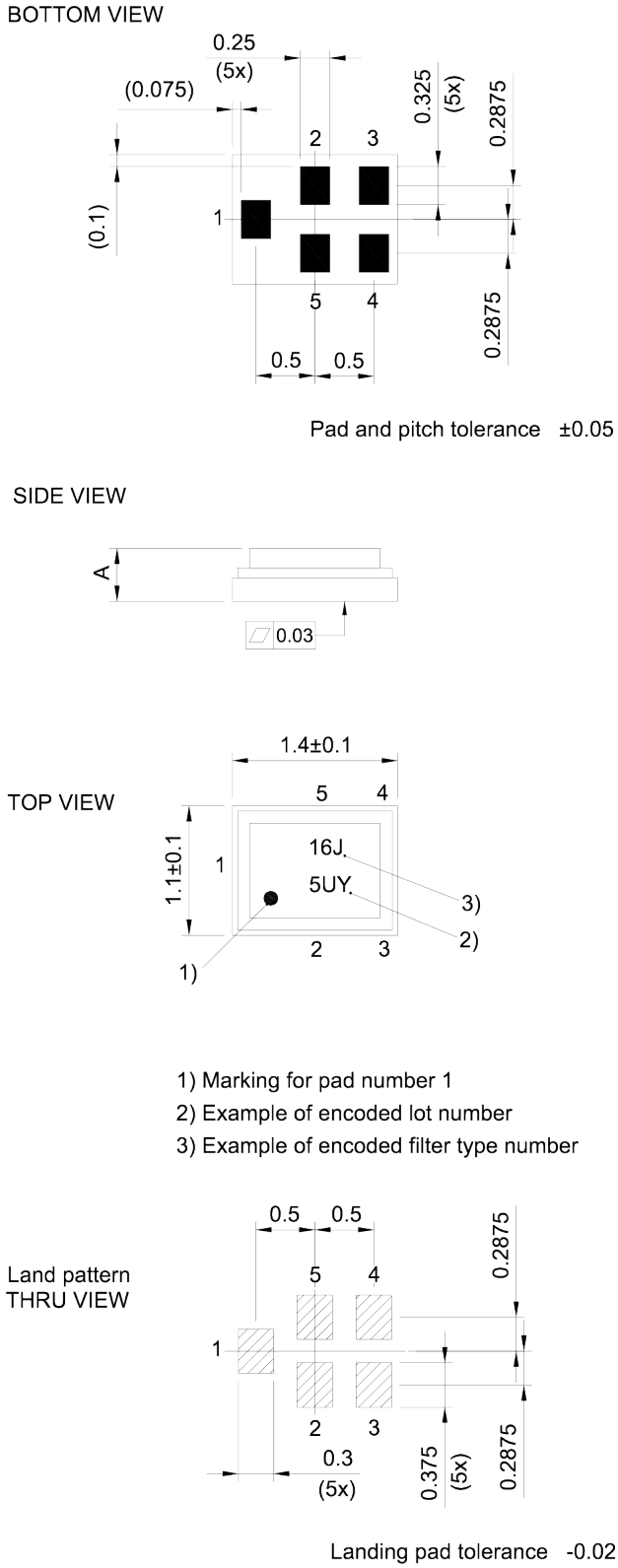

Figure 1: Drawing of package with package height A = 0.45 mm (max.). See Sec. Package information (p. 20).

4 Pin configuration

- 1 Input
- 4 Output
- 2, 3, 5 Ground

5 Matching circuit

■ L_{p1} = 5.3 nH

■ L_{p4} = 16 nH

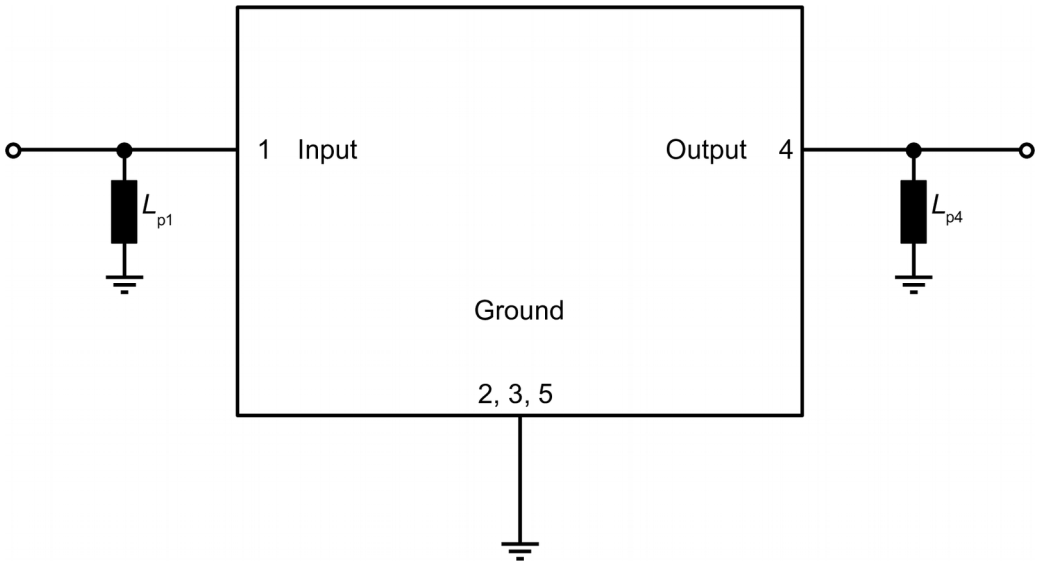

Figure 2: Schematic of matching circuit.

6 Characteristics

Temperature range for specification T_{SPEC} = −40 °C ... +105 °C
Input terminating impedance Z_{IN} = 50 Ω // 5.3 nH[1)]
Output terminating impedance Z_{OUT} = 50 Ω // 16 nH[1)]

Characteristics			min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
Center frequency		f_C	—	1565.5	—	MHz
Maximum insertion attenuation		α_{max}				
	1525 ... 1559 MHz		—	0.9	1.5	dB
B1-I	1559.052 ... 1563.144 MHz		—	0.9	1.2	dB
B1-C	1559.059 ... 1591.788 MHz		—	1.0	1.5	dB
L1	1565.42 ... 1585.42 MHz		—	1.0	1.3	dB
E1	1573.374 ... 1577.466 MHz		—	0.9	1.3	dB
B1-2	1587.694 ... 1591.788 MHz		—	0.9	1.5	dB
G1	1597.55 ... 1605.89 MHz		—	0.9	1.3	dB
Amplitude ripple (p-p)		$\Delta\alpha$				
	1525 ... 1559 MHz		—	0.2	0.7	dB
	1559.052 ... 1563.144 MHz		—	0.1	0.5	dB
	1559.059 ... 1591.788 MHz		—	0.2	0.5	dB
	1565.42 ... 1585.42 MHz		—	0.2	0.5	dB
	1573.374 ... 1577.466 MHz		—	0.1	0.5	dB
	1587.694 ... 1591.788 MHz		—	0.1	0.5	dB
	1597.55 ... 1605.89 MHz		—	0.1	0.5	dB
Group delay ripple		$\Delta\tau_{var}$ [2)]				
	1525 ... 1559 MHz		—	4	7	ns
	1559.052 ... 1563.144 MHz		—	1	5	ns
	1559.059 ... 1591.788 MHz		—	3	7	ns
	1565.42 ... 1585.42 MHz		—	2	5	ns
	1573.374 ... 1577.466 MHz		—	1	5	ns
	1587.694 ... 1591.788 MHz		—	3	7	ns
	1597.55 ... 1605.89 MHz		—	2	6	ns
Maximum VSWR		$VSWR_{max}$				
@ input port	1525 ... 1559 MHz		—	1.6	1.9	
	1559.052 ... 1563.144 MHz		—	1.6	1.8	
	1559.059 ... 1591.788 MHz		—	1.7	1.8	
	1565.42 ... 1585.42 MHz		—	1.7	1.7	
	1573.374 ... 1577.466 MHz		—	1.7	1.8	
	1587.694 ... 1591.788 MHz		—	1.5	1.7	
	1597.55 ... 1605.89 MHz		—	1.5	1.7	
@ output port	1525 ... 1559 MHz		—	1.5	1.8	
	1559.052 ... 1563.144 MHz		—	1.5	1.6	
	1559.059 ... 1591.788 MHz		—	1.6	1.7	

Characteristics				min. for T_{SPEC}	typ. @ +25 °C	max. for T_{SPEC}	
	1565.42 ... 1585.42	MHz		—	1.6	1.7	
	1573.374 ... 1577.466	MHz		—	1.6	1.7	
	1587.694 ... 1591.788	MHz		—	1.5	1.7	
	1597.55 ... 1605.89	MHz		—	1.4	1.7	
Minimum attenuation			α_{min}				
	100 ... 730	MHz		39	42	—	dB
	730 ... 1380	MHz		33	36	—	dB
	1380 ... 1463	MHz		35	43	—	dB
	1656 ... 1710	MHz		28	36	—	dB
	1710 ... 1920	MHz		37	40	—	dB
	1920 ... 2110	MHz		30	36	—	dB
	2110 ... 2600	MHz		35	38	—	dB
	2600 ... 3000	MHz		32	41	—	dB
	3000 ... 3400	MHz		32	39	—	dB
	3400 ... 4700	MHz		31	34	—	dB
	4700 ... 6000	MHz		27	30	—	dB

[1] See Sec. Matching circuit (p. 6).
[2] Aperture 1000 kHz.

7 Maximum ratings

Operable temperature	T_{OP} = −40 °C ... +105 °C	
Storage temperature	T_{STG} [1)] = −40 °C ... +105 °C	
DC voltage	$\lvert V_{DC} \rvert$ [2)] = 0 V (max.)	
Input power @ input port: 1525 ... 1606 MHz	P_{IN} = 18 dBm	Continuous wave for 5000 h @ 85 °C.

1) Not valid for packaging material. Storage temperature for packaging material is −25 °C to +40 °C.
2) In case of applied DC voltage blocking capacitors are mandatory.

8 Transmission coefficient

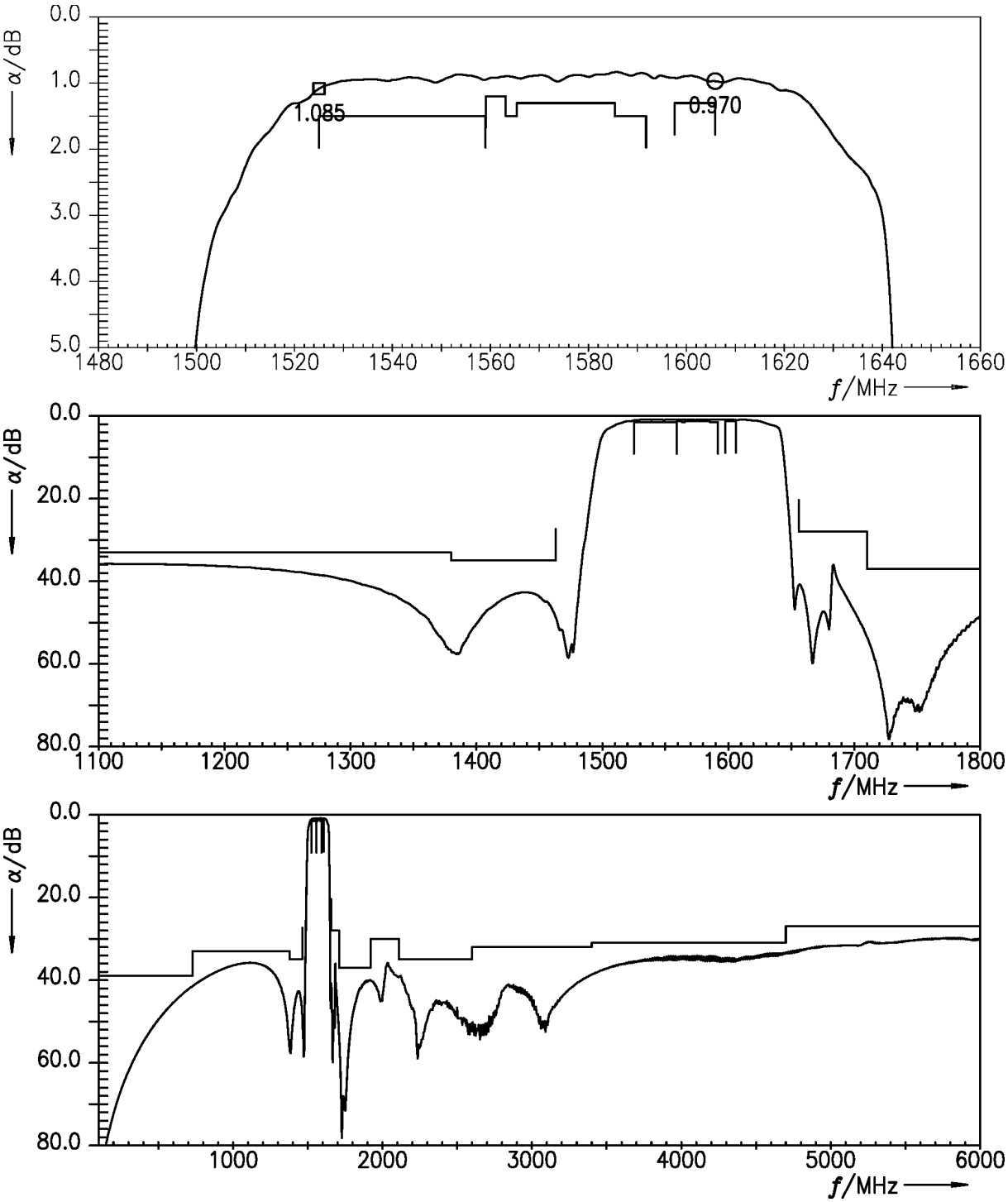

Figure 3: Attenuation.

9 Reflection coefficients

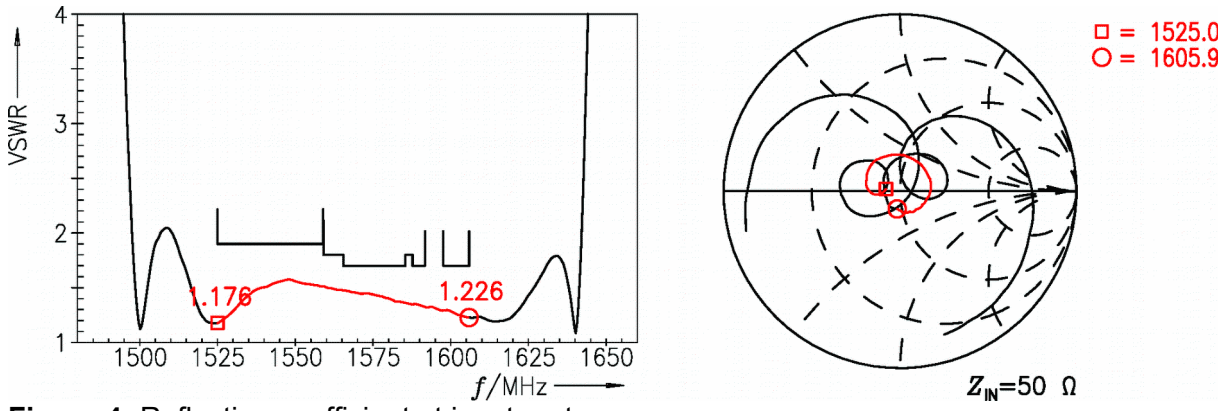

Figure 4: Reflection coefficient at input port.

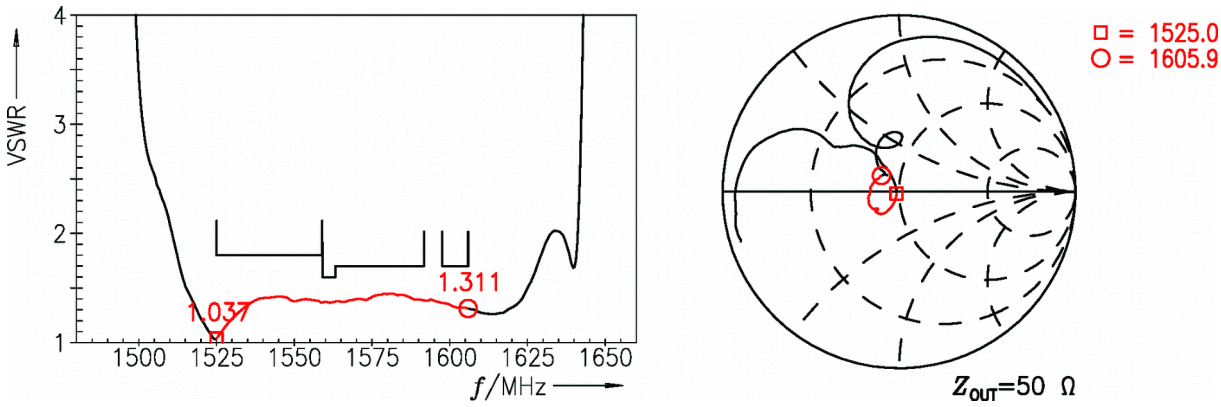

Figure 5: Reflection coefficient at output port.

10 Group delay

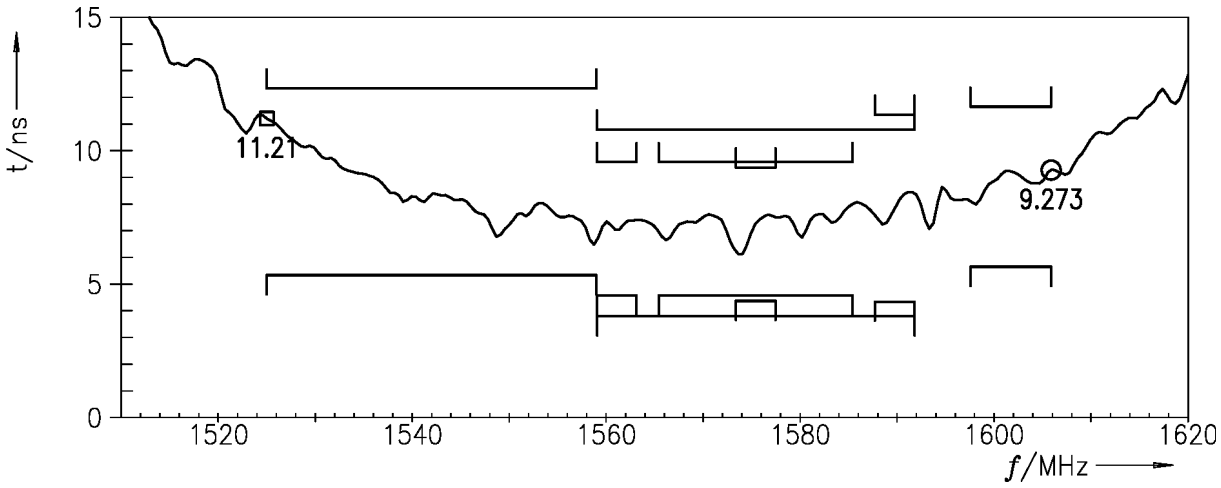

Figure 6: Group delay ripple.

11 Packing material

11.1 Tape

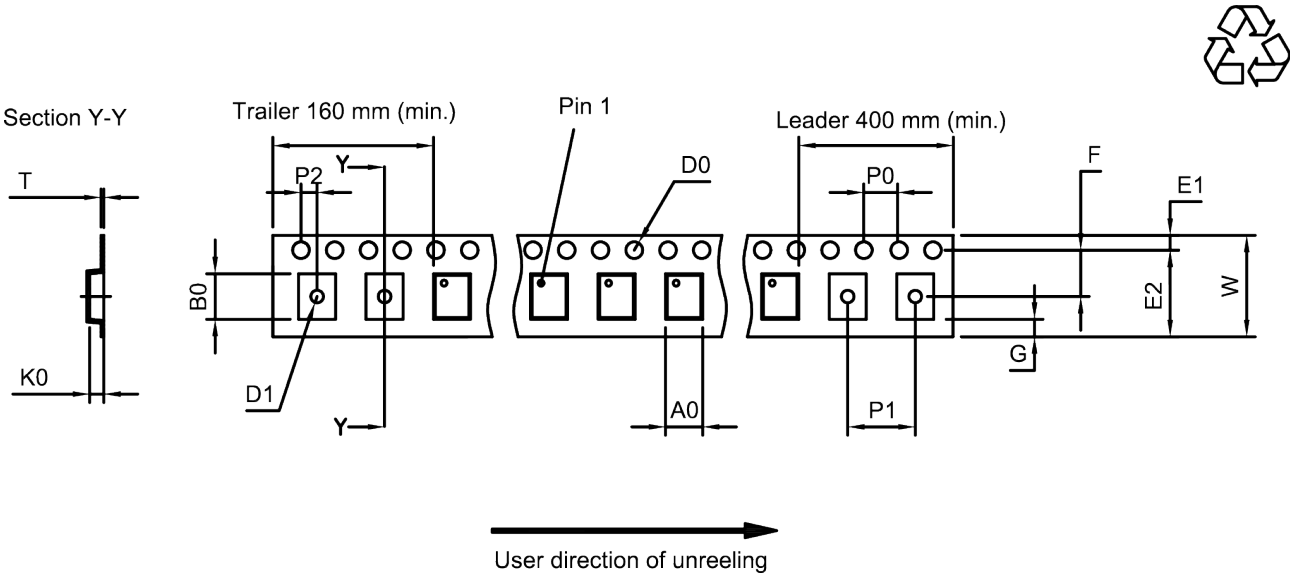

Figure 7: Drawing of tape (first-angle projection) for illustration only and not to scale. The valid tape dimensions are listed in Table 1.

A_0	1.27±0.05 mm
B_0	1.57±0.05 mm
D_0	1.5+0.1/−0 mm
D_1	0.5±0.1 mm
E_1	1.75±0.1 mm

E_2	6.25 mm (min.)
F	3.5±0.05 mm
G	0.75 mm (min.)
K_0	0.62±0.05 mm
P_0	4.0±0.1 mm

P_1	4.0±0.1 mm
P_2	2.0±0.05 mm
T	0.25±0.03 mm
W	8.0+0.3/−0.1 mm

Table 1: Tape dimensions.

11.2 Reel with diameter of 180 mm

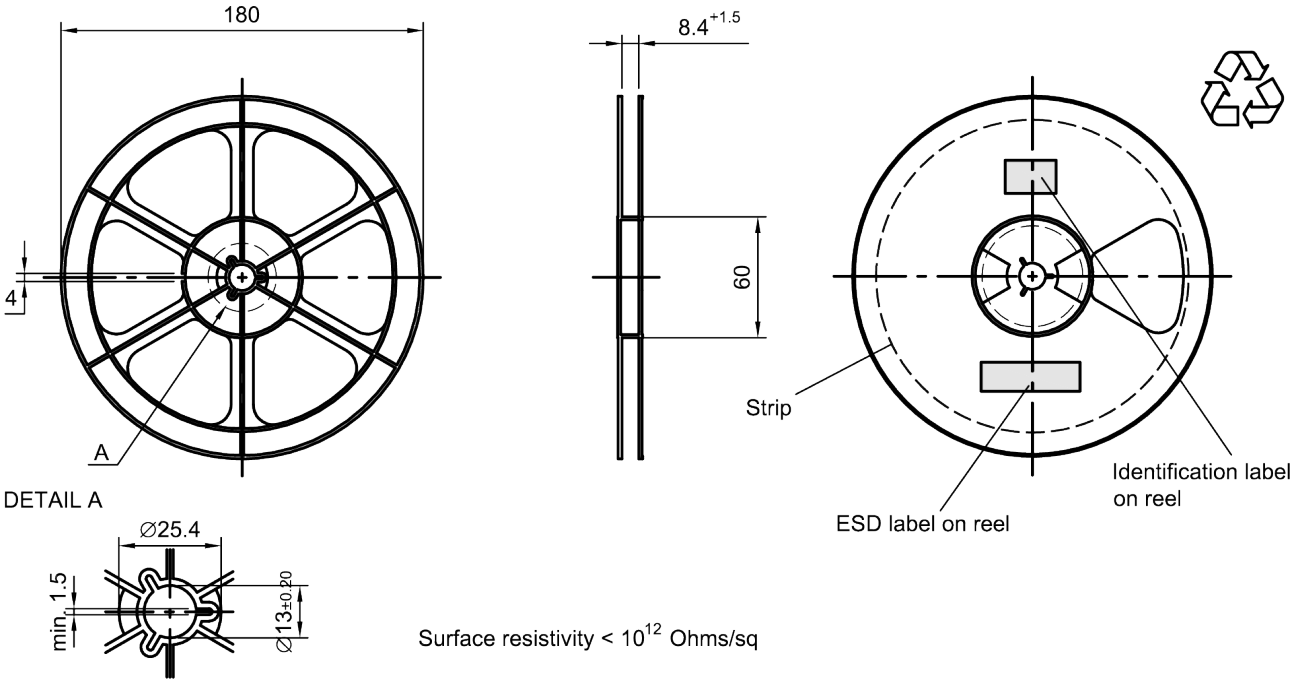

Figure 8: Drawing of reel (first-angle projection) with diameter of 180 mm.

Dimensions [mm]
X = 220+5
Y = 235+5
Sealing area 10±3

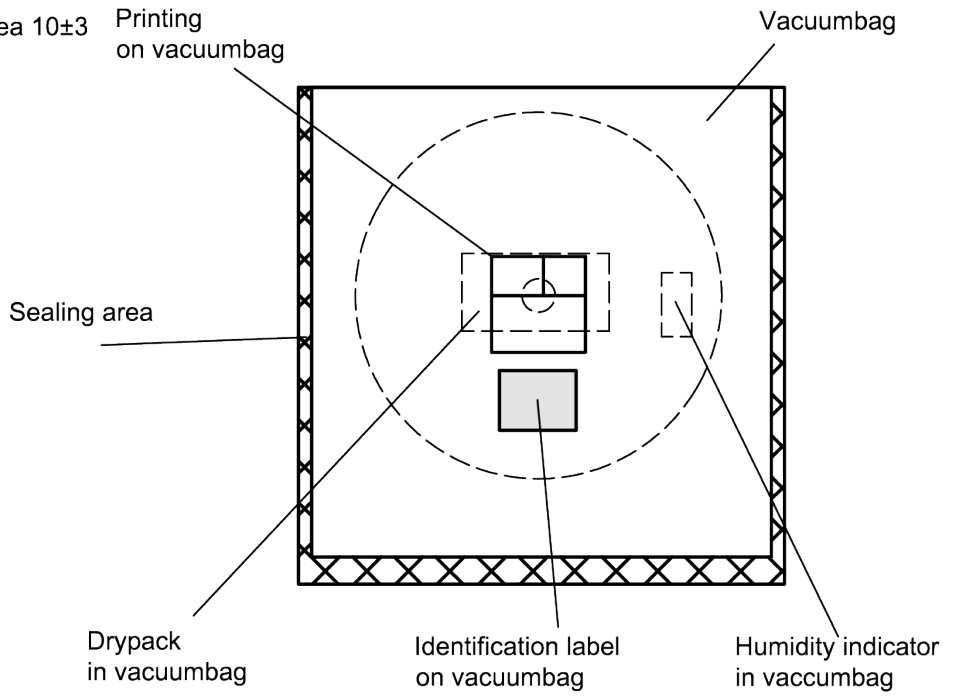

Figure 9: Drawing of moisture barrier bag (MBB) for reel with diameter of 180 mm.

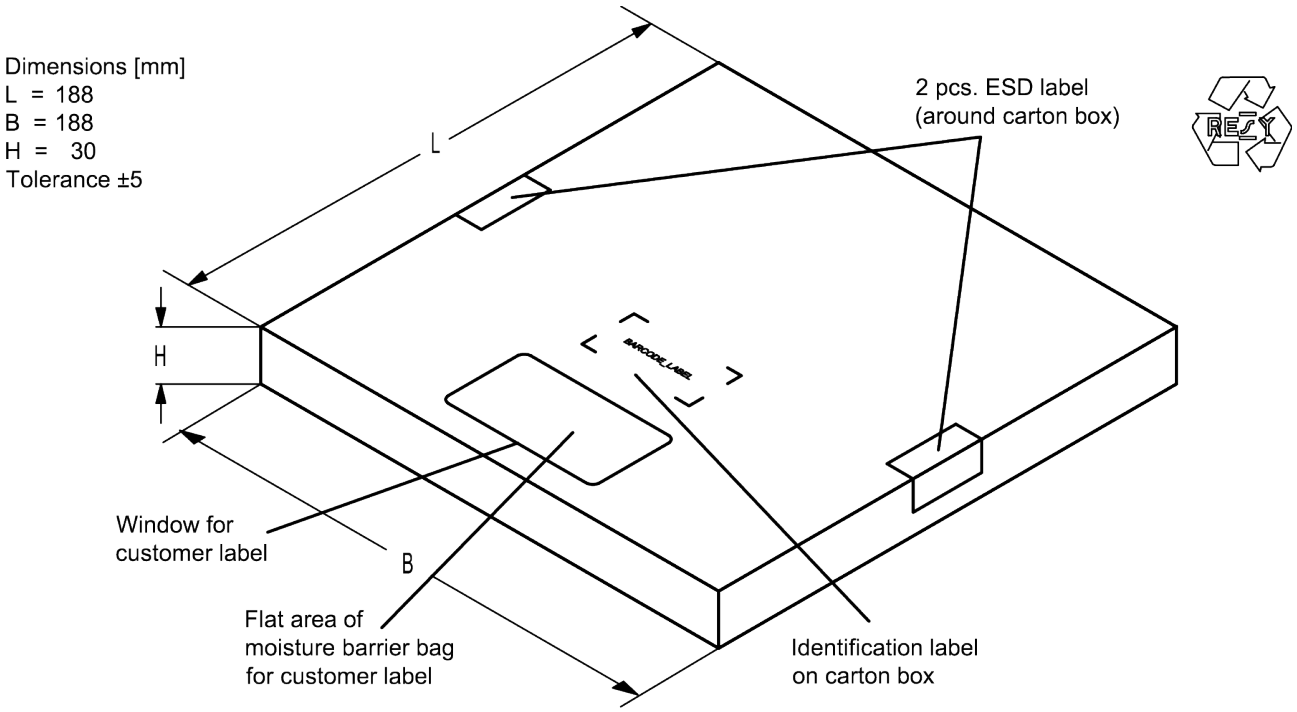

Figure 10: Drawing of folding box for reel with diameter of 180 mm.

12 Marking

Products are marked with product type number and lot number encoded according to Table 2:

- Type number:

  The 4 digit type number of the ordering code, e.g., B3xxxxB**1234**xxxx,
  is encoded by a special BASE32 code into a 3 digit marking.

  Example of decoding type number marking on device in decimal code.
  16J **=>** **1234**
  1 x 32^2 + **6** x 32^1 + **18 (=J)** x 32^0 **=** **1234**
  The BASE32 code for product type B2624 is 2J0.

- Lot number:

  The last 5 digits of the lot number, e.g., **12345**,
  are encoded based on a special BASE47 code into a 3 digit marking.

  Example of decoding lot number marking on device in decimal code.
  5UY **=>** **12345**
  5 x 47^2 + **27 (=U)** x 47^1 + **31 (=Y)** x 47^0 **=** **12345**

Adopted BASE32 code for type number			
Decimal value	Base32 code	Decimal value	Base32 code
0	0	16	G
1	1	17	H
2	2	18	J
3	3	19	K
4	4	20	M
5	5	21	N
6	6	22	P
7	7	23	Q
8	8	24	R
9	9	25	S
10	A	26	T
11	B	27	V
12	C	28	W
13	D	29	X
14	E	30	Y
15	F	31	Z

Adopted BASE47 code for lot number			
Decimal value	Base47 code	Decimal value	Base47 code
0	0	24	R
1	1	25	S
2	2	26	T
3	3	27	U
4	4	28	V
5	5	29	W
6	6	30	X
7	7	31	Y
8	8	32	Z
9	9	33	b
10	A	34	d
11	B	35	f
12	C	36	h
13	D	37	n
14	E	38	r
15	F	39	t
16	G	40	v
17	H	41	\
18	J	42	?
19	K	43	{
20	L	44	}
21	M	45	<
22	N	46	>
23	P		

Table 2: Lists for encoding and decoding of marking.

13 Soldering profile

The recommended soldering process is in accordance with IEC 60068-2-58 – 3rd edit and IPC/JEDEC J-STD-020B.

ramp rate	≤ 3 K/s
preheat	125 °C to 220 °C, 150 s to 210 s, 0.4 K/s to 1.0 K/s
T > 220 °C	30 s to 70 s
T > 230 °C	min. 10 s
T > 245 °C	max. 20 s
T ≥ 255 °C	–
peak temperature T_{peak}	250 °C +0/-5 °C
wetting temperature T_{min}	230 °C +5/-0 °C for 10 s ± 1 s
cooling rate	≤ 3 K/s
soldering temperature T	measured at solder pads

Table 3: Characteristics of recommended soldering profile for lead-free solder (Sn95.5Ag3.8Cu0.7).

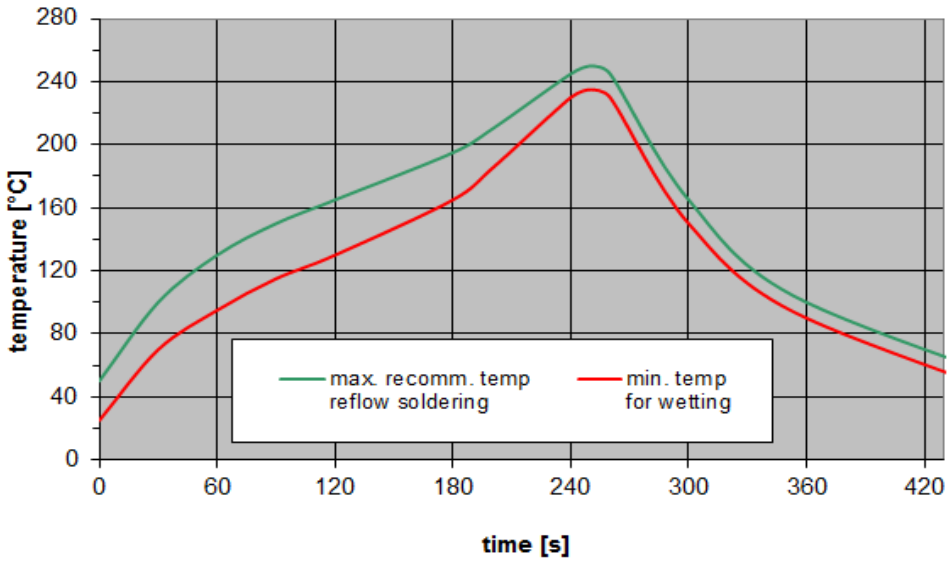

Figure 11: Recommended reflow profile for convection and infrared soldering – lead-free solder.

14 ESD protection of SAW filters

SAW filters are **E**lectro **S**tatic **D**ischarge sensitive devices. To reduce the probability of damages caused by ESD, special matching topologies have to be applied.

In general, “ESD matching” has to be ensured at that filter port, where electrostatic discharge is expected.

Electrostatic discharges predominantly appear at the antenna input of RF receivers. Therefore, only the input matching of the SAW filter has to be designed to short circuit or to block the ESD pulse.

Below three figures show recommended “ESD matching” topologies.

For wide band filters the high-pass ESD matching structure needs to be at least of 3rd order to ensure a proper matching for any impedance value of antenna and SAW filter input. The required component values have to be determined from case to case.

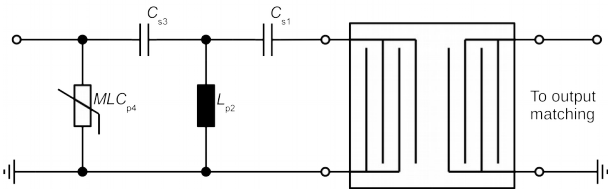

Figure 12: MLC varistor plus ESD matching.

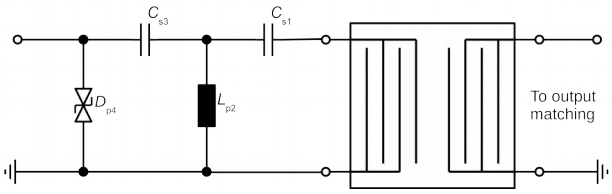

Figure 13: Suppressor diode plus ESD matching.

In cases where minor ESD occur, following simplified “ESD matching” topologies can be used alternatively.

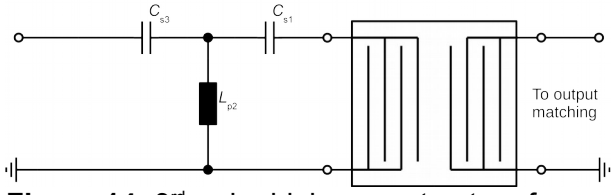

Figure 14: 3rd order high-pass structure for basic ESD protection.

In all three figures the shunt inductor L_{p2} could be replaced by a shorted microstrip with proper length and width. If this configuration is possible depends on the operating frequency and available PCB space.

Effectiveness of the applied ESD protection has to be checked according to relevant industry standards or customer specific requirements.

For further information, please refer to RF360 Application report: **“ESD protection for SAW filters”.** This report can be found under https://rffe.qualcomm.com.

15 Annotations

15.1 RoHS compatibility

ROHS-compatible means that products are compatible with the requirements according to Art. 4 (substance restrictions) of Directive 2011/65/EU of the European Parliament and of the Council of June 8th, 2011, on the restriction of the use of certain hazardous substances in electrical and electronic equipment ("Directive") with due regard to the application of exemptions as per Annex III of the Directive in certain cases.

15.2 Scattering parameters (S-parameters)

The pin/port assignment is available in the headers of the S-parameter files. Please contact your local RF360 sales office.

16 Cautions and warnings

16.1 Display of ordering codes for RF360 products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications and the website of RF360, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products. Detailed information can be found on the Internet under https://rffe.qualcomm.com/.

16.2 Material information

Due to technical requirements components may contain dangerous substances. For information on the type in question please also contact one of our sales offices.

For information on recycling of tapes and reels please contact one of our sales offices.

16.3 Moldability

Before using in overmolding environment, please contact your local RF360 sales office.

16.4 Package information

Landing area

The printed circuit board (PCB) land pattern (landing area) shown is based on RF360 internal development and empirical data and illustrated for example purposes, only. As customers' SMD assembly processes may have a plenty of variants and influence factors which are not under control or knowledge of RF360, additional careful process development on customer side is necessary and strongly recommended in order to achieve best soldering results tailored to the particular customer needs.

Dimensions

Unless otherwise specified all dimensions are understood using unit millimeter (mm).

Dimensions do not include burrs.

Projection method

Unless otherwise specified first-angle projection is applied.

17 Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application.** As a rule, RF360 Europe GmbH and its affiliates are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an RF360 product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified.** In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous)**. Useful information on this will be found in our Material Data Sheets on the Internet (https://rffe.qualcomm.com). Should you have any more detailed questions, please contact our sales offices.
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