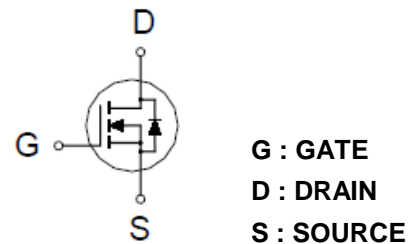
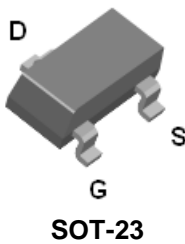


# P3203CMG

## N-Channel Enhancement Mode MOSFET

### PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	$I_D$
30V	32m $\Omega$ @ $V_{GS} = 4.5V$	6A



### ABSOLUTE MAXIMUM RATINGS<sup>3</sup>

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 12$	
Continuous Drain Current <sup>2</sup>	$I_D$	$T_A = 25\text{ }^\circ\text{C}$	6
		$T_A = 70\text{ }^\circ\text{C}$	5
Pulsed Drain Current <sup>1,2</sup>	$I_{DM}$	30	A
Power Dissipation	$P_D$	$T_A = 25\text{ }^\circ\text{C}$	1.25
		$T_A = 70\text{ }^\circ\text{C}$	0.8
Operating Junction & Storage Temperature Range	$T_J, T_{STG}$	-55 to 150	$^\circ\text{C}$

### THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient <sup>3</sup>	$R_{\theta JA}$		100	$^\circ\text{C} / \text{W}$

<sup>1</sup>Pulse width limited by maximum junction temperature.

<sup>2</sup>Limited only by maximum temperature allowed.

<sup>3</sup> $T_A = 25\text{ }^\circ\text{C}$  Unless Otherwise Noted.

# P3203CMG

## N-Channel Enhancement Mode MOSFET

### ELECTRICAL CHARACTERISTICS<sup>3</sup>

PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
<b>STATIC</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	30			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	0.45	0.7	1.2	V
Gate-Body Leakage	$I_{GSS}$	$V_{DS} = 0V, V_{GS} = \pm 12V$			$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 24V, V_{GS} = 0V$			1	$\mu A$
		$V_{DS} = 20V, V_{GS} = 0V, T_J = 55^\circ C$			10	
On-State Drain Current <sup>1</sup>	$I_{D(ON)}$	$V_{DS} = 5V, V_{GS} = 4.5V$	30			A
Drain-Source On-State Resistance <sup>1</sup>	$R_{DS(ON)}$	$V_{GS} = 2.5V, I_D = 4A$		32	52	m $\Omega$
		$V_{GS} = 4.5V, I_D = 5A$		24	32	
		$V_{GS} = 10V, I_D = 6A$		22	28	
Forward Transconductance <sup>1</sup>	$g_{fs}$	$V_{DS} = 5V, I_D = 5A$		33		S
<b>DYNAMIC</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 15V, f = 1MHz$		620		$\mu F$
Output Capacitance	$C_{oss}$			69		
Reverse Transfer Capacitance	$C_{rss}$			62		
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{DS} = 15V, V_{GS} = 4.5V, I_D = 5A$		8		nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$			1.5		
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$			3		
Turn-On Delay Time <sup>2</sup>	$t_{d(on)}$	$V_{DS} = 15V, I_D \cong 5A, V_{GS} = 4.5V, R_{GS} = 6\Omega$		4.5		nS
Rise Time <sup>2</sup>	$t_r$			4		
Turn-Off Delay Time <sup>2</sup>	$t_{d(off)}$			37		
Fall Time <sup>2</sup>	$t_f$			6		
<b>SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTIC</b>						
Continuous Current	$I_S$				6	A
Forward Voltage <sup>1</sup>	$V_{SD}$	$I_F = 1.3A, V_{GS} = 0V$			1.3	V
Reverse Recovery Time	$t_{rr}$	$I_F = 6A, di_F/dt = 100 A/\mu s$		10.5		nS
Reverse Recovery Charge	$Q_{rr}$			2.1		$\mu C$

<sup>1</sup>Pulse test : Pulse Width  $\leq 300 \mu sec$ , Duty Cycle  $\leq 2\%$ .

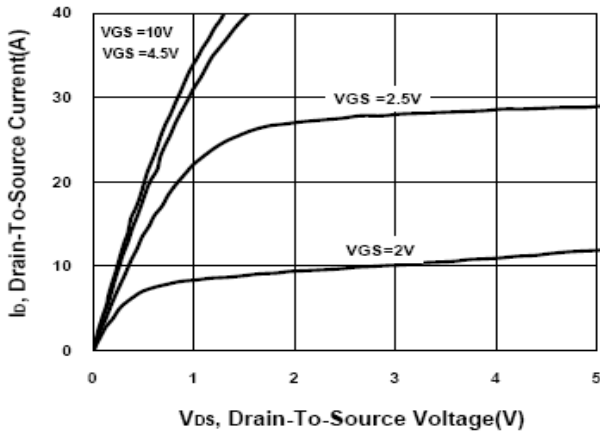
<sup>2</sup>Independent of operating temperature.

<sup>3</sup> $T_J = 25^\circ C$ , Unless Otherwise Noted.

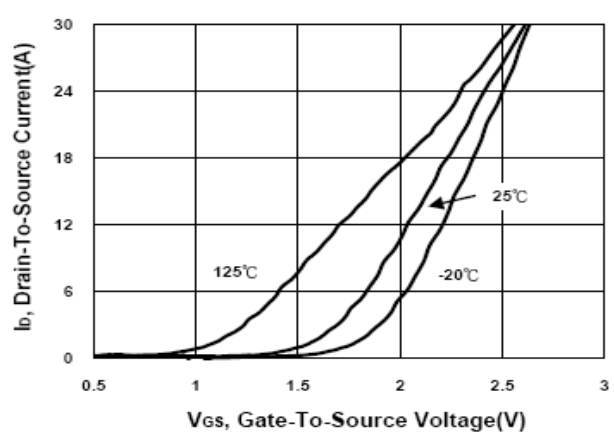
# P3203CMG

## N-Channel Enhancement Mode MOSFET

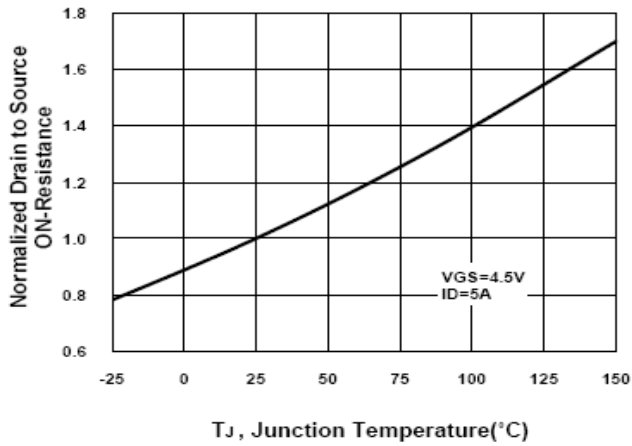
**Output Characteristics**



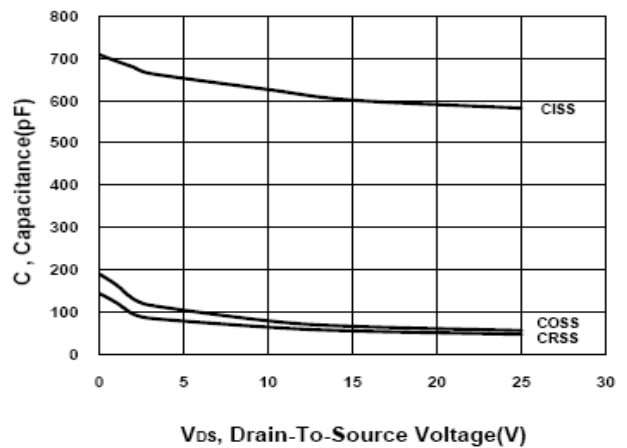
**Transfer Characteristics**



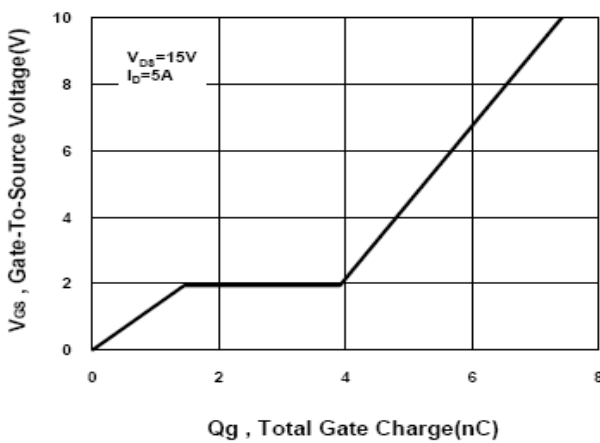
**On-Resistance VS Temperature**



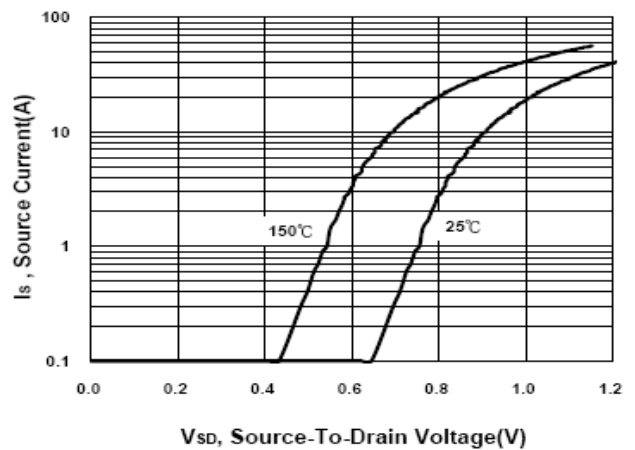
**Capacitance Characteristic**



**Gate charge Characteristics**



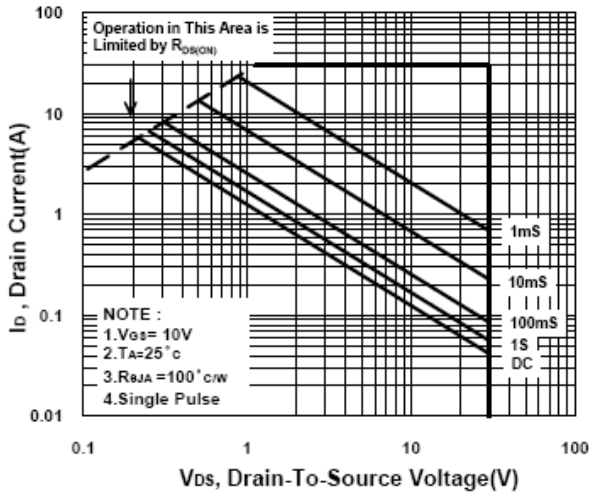
**Source-Drain Diode Forward Voltage**



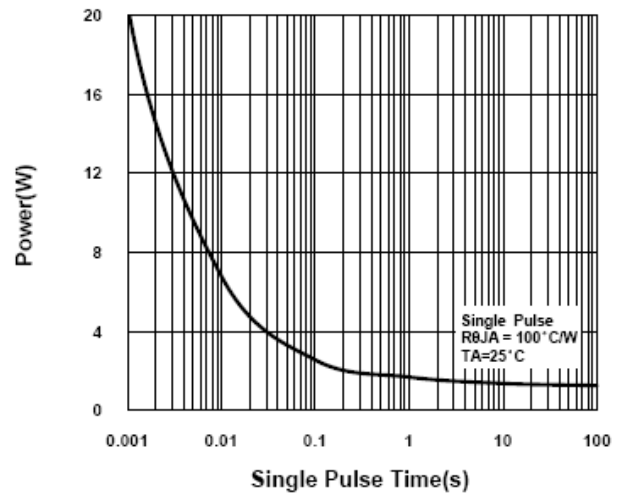
# P3203CMG

## N-Channel Enhancement Mode MOSFET

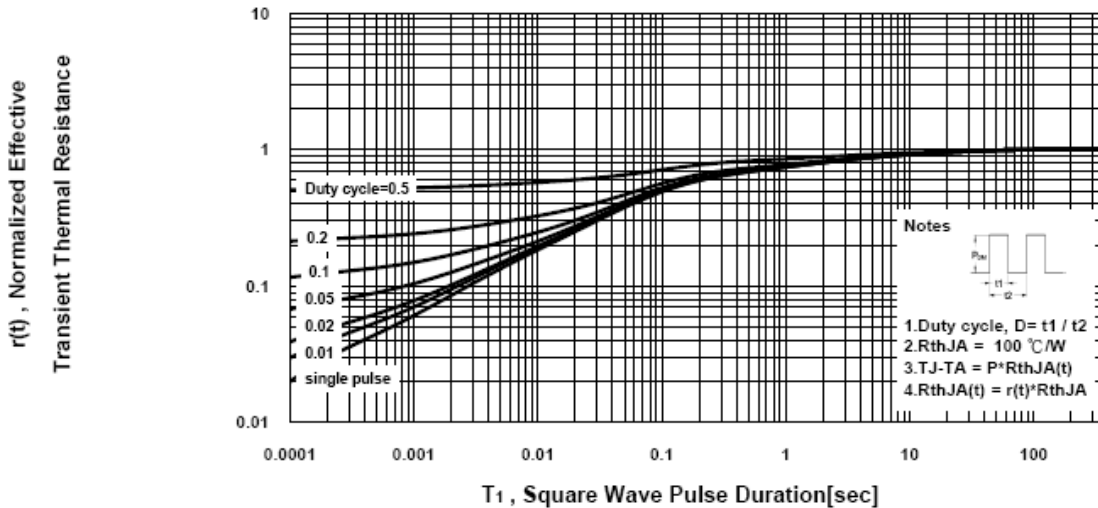
**Safe Operating Area**



**Single Pulse Maximum Power Dissipation**



**Transient Thermal Response Curve**



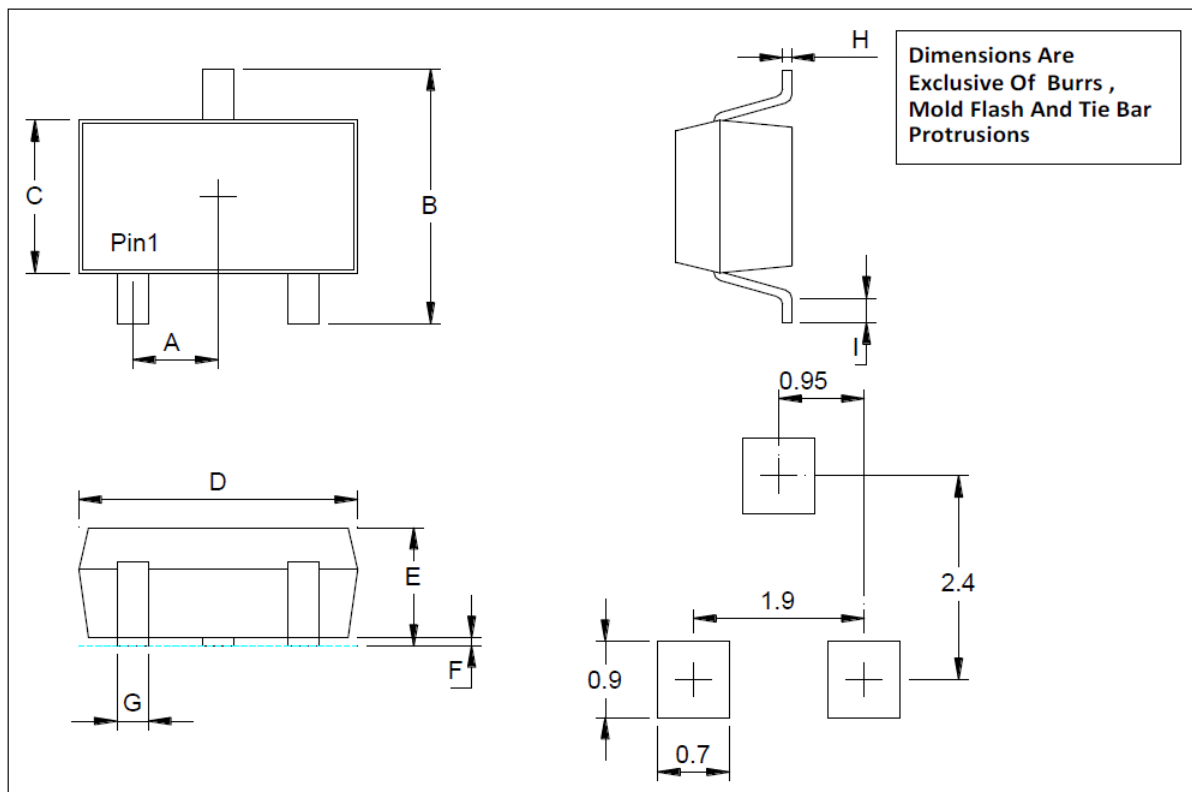
# P3203CMG

## N-Channel Enhancement Mode MOSFET

### Package Dimension

### SOT-23-3 MECHANICAL DATA

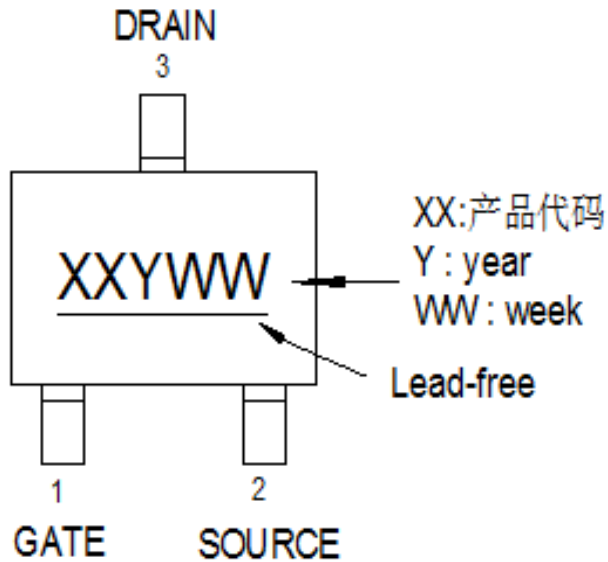
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A		0.95		H	0.1		0.2
B	2.4	2.8	3	I	0.3	0.45	0.6
C	1.4	1.5	1.73				
D	2.7	2.9	3.1				
E	1	1.1	1.31				
F	0		0.15				
G	0.3	0.4	0.5				



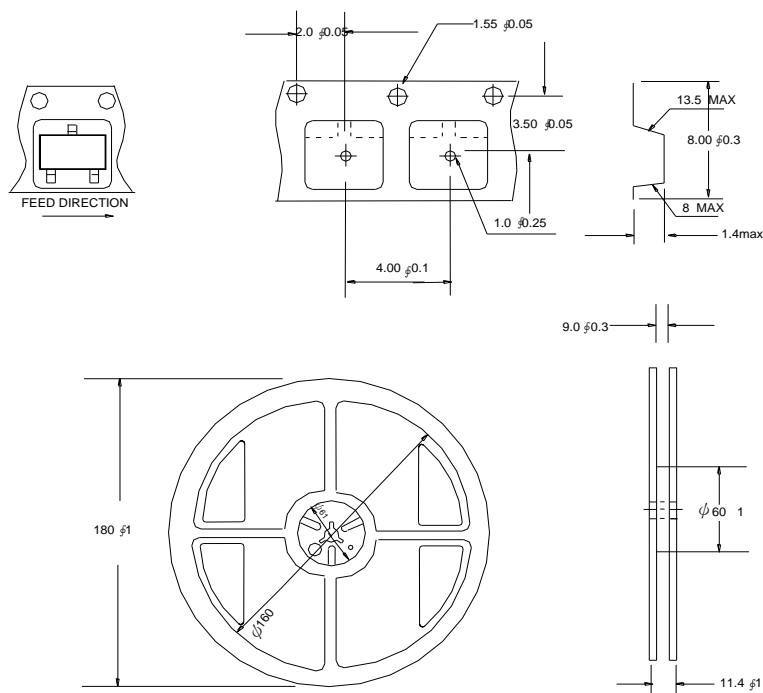
# P3203CMG

## N-Channel Enhancement Mode MOSFET

### A. Marking Information (此产品代码为：1B)



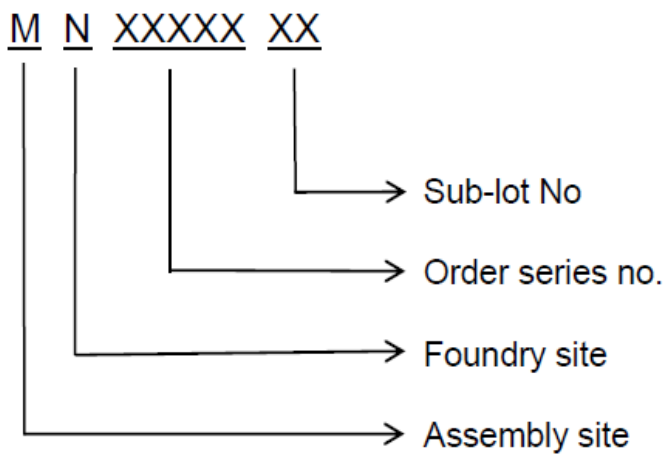
### B. Tape&Reel Information:3000pcs/Reel



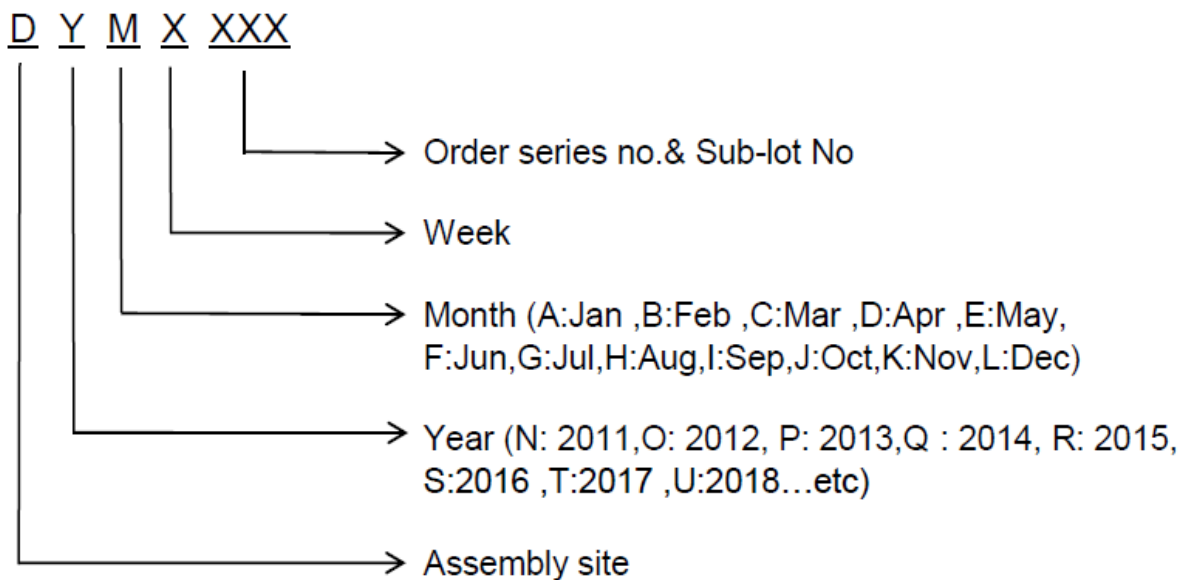
**P3203CMG**  
**N-Channel Enhancement Mode MOSFET**

**C. Lot No.&Date Code rule**

1.Lot No.



2.Date Code





## P3203CMG

### N-Channel Enhancement Mode MOSFET

#### D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文"0"和数字"0", "G"和"Q"的字型即可)
3	U-NIKC	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	RoHS label	 long axis: 12 mm minor axis: 6 mm bottom color: White Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 10 mm bottom color: Green Font color: Black Font style: Arial
12	Scan information	Device / Lot / D/C / QTY , Insert " / " between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least



单击下面可查看定价，库存，交付和生命周期等信息

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