

CRYSTAL OSCILLATOR (SPXO)

OUTPUT: LV-PECL, LVDS



SG3225EEN

SG3225VFN

(3.2 × 2.5 × 1.05 mm)



Product Number

Product Number
\$G3225EEN: X1G005221xxxx00 (fo ≤ 200 MHz)
X1G005511xxxx00 (fo > 200 MHz)
\$G5032EEN: X1G005531xxxx00
\$G7050EEN: X1G005531xxxx00 (fo ≤ 200 MHz)
X1G005551xxxx00 (fo ≤ 200 MHz)
X1G005551xxxx00 (fo > 200 MHz)
X1G005521xxxx00 (fo > 200 MHz)
\$G5032VEN: X1G005541xxxx00
\$G50532VEN: X1G005541xxxx00

SG7050VEN: X1G005331xxxx00 (fo ≤ 200 MHz) X1G005561xxxx00 (fo > 200 MHz)

SG5032EEN

SG5032VEN

(5.0 × 3.2 × 1.3 mm)



SG3225 / 5032 / 7050EEN SG3225 / 5032 / 7050VEN

25 MHz to 500 MHz Frequency range Supply voltage 2.5 V Typ. / 3.3 V Typ. LV-PECL or LVDS Output Function Output enable (OE)

50 fs Typ. (fo = 156.25 MHz, LV-PECL) Phase jitter

-40 C to +105 C Operating temperature :

Specifications (characteristics)

Opecineations							
		Specific	cations				
Item	Symbol	LV-PECL LVDS		Conditions / Remarks			
		SG3225EEN / SG5032EEN	SG3225VEN / SG5032VEN	Conditions / Remarks			
		/ SG7050EEN	/ SG7050VEN				
Output fraguancy range	fo	25 MHz to	500 MHz	Except for SG5032EEN / SG5032VEN	Please contact us for	r available	
Output frequency range	10	200.1 MHz	to 500 MHz	SG5032EEN / SG5032VEN	frequencies.	cies.	
Supply voltage	Vcc	D: 2.5 V ± 0.125 V,	C: 3.3 V ± 0.165 V				
Storage temperature	T stg	-55 C to	+125 C				
Operating temperature	T_use	G: -40 C to +85 C,	H: -40 C to +105 C				
		D: ±25 x	10-6 May	Includes initial frequency tolerance, ter	mperature variation,	Refer to	
Frequency tolerance	f tol	D. ±25 x	TU - Max.	supply voltage change and 5 years aging (+25 °C)	ing (+25 °C)		
Frequency tolerance	1_101	J: ±50 × 10 ⁻⁸ Max.		Includes initial frequency tolerance, ter	cy tolerance, temperature variation, figure *		
		L: ±100 ×	10 ⁻⁶ Max.	supply voltage change and 10 years a	10 years aging (+25 °C)		
Current consumption	Icc	60 mA Max.	25 mA Max.	OE = V_{CC} , L_ECL = 50Ω or L_LVDS =	: 100 Ω		
Disable current	I dis	25 mA Max.	15 mA Max.	OE = GND			
Symmetry	SYM	45 % to	o 55 %	At output crossing point			
Output voltage (LV-PECL)	VoH	Vcc - 1.1 V Min.	_	DC characteristics			
Output Voltage (EV-F-ECE)	VoL	Vcc - 1.5 V Max.	_				
	Vod	_	250 mV to 450 mV	Differential output voltage, V _{OD1} , V _{OD2}			
Output voltage (LVDS)	dV₀□	dV_{OD} – 50 mV Max. $dV_{OD} = V_{OD1} - V_{OD2} $	DC characterist	DC characteristics			
Output Voltage (EVDS)	Vos	_	1.15 V to 1.35 V	Offset voltage, Vos1, Vos2	DC CHaracterist	50 characteristics	
	dVos	_	50 mV Max.	dVos = Vos1 - Vos2			
Output load condition	L ECL	50 Ω	_	Terminated to V _{CC} - 2.0 V			
Output load condition	L_LVDS	_	100 Ω	Connected between OUT to OUT			
Input voltage	VIH		cc Min.	OE terminal			
input voltage	VIL	30 % V	cc Max.				
		0.35 ns Max.			ween 20 % and 80 % of (V		
Rise/Fall times	tr / tf		0.3 ns Max.	20 1111 12 2 10 2 200 1111 12	ween 20 % and 80 % of Dit	ferential	
		0.3 ns Max.		All Outer	put peak to peak voltage		
Startup time	t str	10 ms	Max.	Time at minimum supply voltage to be	0 s		

Phase Jitter

Product Name	100 MHz	125 MHz	156.25 MHz	200 MHz	312.5 MHz	491.52 MHz	Conditions
SG3225EEN / SG5032EEN / SG7050EEN	75 fs Typ.	60 fs Typ.	50 fs Typ.	40 fs Typ.	30 fs Typ.	20 fs Typ.	Offset frequency:
SG3225VFN / SG5032VFN / SG7050VFN	90 fs Tvp	70 fs Tvp.	60 fs Tvp	50 fs Tvp.	40 fs Tvp.	30 fs Tvp	12 kHz to 20 MHz

Product Name (Standard form) SG3225 EEN 156.250000MHz C D G A 4567

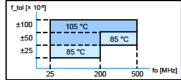
(56: Unavailable code DH, DG and JH at fo > 200 MHz, Refer to figure *1) *1 : Maximum T_use of operating range

O

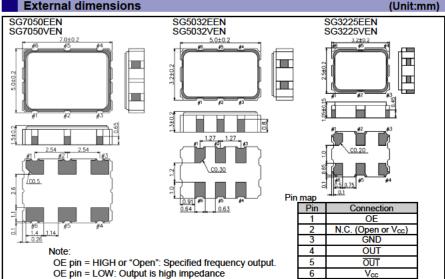
①Model ②Output (E: LV-PECL, V: LVDS) ③Frequency ④Supply voltage ⑤Frequency tolerance ⑥Opera ing temperature (7) Internal identification code("A" is default)

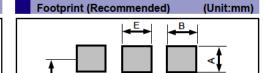
48			⑤Frequency tolerance		
С	3.3 V Typ.		D	±25 × 10 ⁻⁶	
D	2.5 V Typ.		7	±50 × 10 ⁻⁶	
			_	±100 × 10-8	

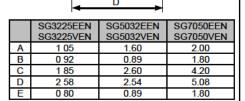
6Operating temperature		
G	-40 to +85 C	
Н	-40 to +105 C	



External dimensions







In order to achieve optimum jitter performance, it is recommended that 0.1 µF and 10 µF bypass capacitors should be connected between Vcc and GND and placed as close to the Vcc pin as possible.

PROMOTION OF ENVIRONMENTAL MANAGEMENT SYSTEM CONFORMING TO INTERNATIONAL STANDARDS

At Seiko Epson, all environmental initiatives operate under the Plan-Do-Check-Action (PDCA) cycle designed to achieve continuous improvements. The environmental management system (EMS) operates under the ISO 14001 environmental management standard.

All of our major manufacturing and non-manufacturing sites, in Japan and overseas, completed the acquisition of ISO 14001 certification.

ISO 14000 is an international standard for environmental management that was established by the International Standards Organization in 1996 against the background of growing concern regarding global warming, destruction of the ozone layer, and global deforestation.

WORKING FOR HIGH QUALITY

In order provide high quality and reliable products and services than meet customer needs, Seiko Epson made early efforts towards obtaining ISO9000 series certification and has acquired ISO9001 for all business establishments in Japan and abroad. We have also acquired IATF 16949 certification that is requested strongly by major automotive manufacturers as standard.

IATF 16949 is the international standard that added the sector-specific supplemental requirements for automotive industry based on ISO9001.

Explanation of the mark that are using it for the catalog



►Pb free.



► Complies with EU RoHS directive.

*About the products without the Pb-free mark.

Contains Pb in products exempted by EU RoHS directive.

(Contains Pb in sealing glass, high melting temperature type solder or other.)



▶ Designed for automotive applications such as Car Multimedia, Body Electronics, Remote Keyless Entry etc.



▶ Designed for automotive applications related to driving safety (Engine Control Unit, Air Bag, ESC etc.).

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