

TO :

文件编号

HXA-L56-22(01)

发行日期

2022年01月15日

承认规格书

种类: Wire Wound Molded SMD Power Inductors

系列号: HXTC-SERIES

客户料号: _____

客户承认栏

客户承认栏	
承认日期	年 月 日

(贵司承认后请签署一份返回艾申迪电子, 谢谢!)

厦门艾申迪电子有限公司技术质量部

承认	确认	作成
龙梅	梁峰	王亮

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SMD Power Choke Coil

HXTC-SERIES

ECN HISTORY LIST

REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	22/01/15	新發行	龙梅	梁峰	王亮
備 注					

1. Features

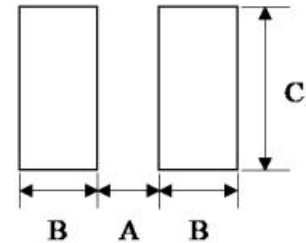
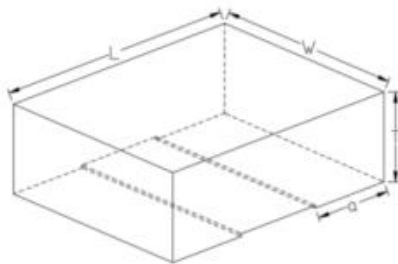
1. Soft saturation.
2. High current, low DCR, high efficiency.
3. Very low acoustic noise and very low leakage flux noise.
4. High reliability.
5. 100% Lead (Pb)-Free and RoHS compliant.
6. Operating temperature -55~+125°C(Including self-temperature rise)



2. Applications

Note PC power system · incl. IMVP-6
DC/DC converter.

3. Dimensions

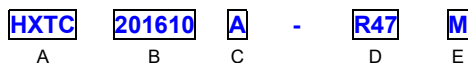


Series	L(mm)	W(mm)	T(mm)	a(mm)	A	B	C
HXTC160810A	1.6±0.2 [0.063±0.008]	0.8±0.2 [0.031±0.008]	1.0Max [0.039Max]	0.4±0.2 [0.016±0.008]	0.6~0.8	0.6~0.8	0.6~0.8
HXTC201208A	2.0±0.2 [0.079±0.008]	1.2±0.2 [0.047±0.008]	0.8Max [0.031Max]	0.6±0.2 [0.024±0.008]	0.8~1.2	0.8~1.2	1.2~2.0
HXTC201210A	2.0±0.2 [0.079±0.008]	1.2±0.2 [0.047±0.008]	1.0Max [0.039Max]	0.6±0.2 [0.024±0.008]	0.8~1.2	0.8~1.2	1.2~2.0
HXTC201610A	2.0±0.2 [0.079±0.008]	1.6±0.2 [0.063±0.008]	1.0Max [0.039Max]	0.6±0.2 [0.024±0.008]	0.8~1.2	0.8~1.2	1.2~2.0
HXTC252010A	2.5±0.2 [0.098±0.008]	2.0±0.2 [0.079±0.008]	1.0Max [0.039Max]	0.8±0.2 [0.031±0.008]	1.2~1.6	0.8~1.2	1.8~2.4

Note:

1. Inductance tolerance code (M=±20%).
2. Rated current: Isat or Irms, whichever is smaller.
3. Isat: Max.Value, DC current at which the inductance drops less than 30% from its value without current;
Typ. Value, DC current at which the inductance drops 30% from its value without current.
4. Irms: DC current that will cause the temperature rise (ΔT) from 22°C ambient.
5. For Max. Value, ΔT < 40°C ; for Typ. Value, ΔT is approximate 40°C.

4. Part Numbering

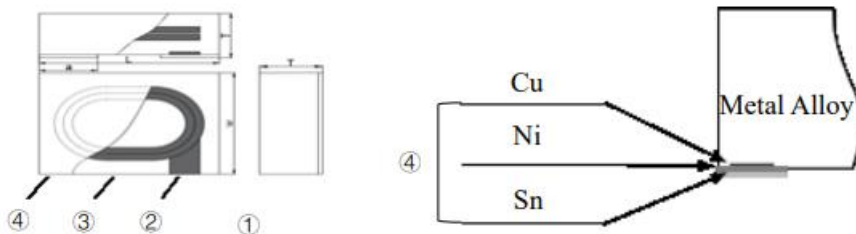


- A: Series
 B: Dimension
 C: Type
 D: Inductance
 E: Inductance Tolerance
- A x C
 R47=0.47uH
 M = ±20%

5. Specification

ASDI Part Number	Inductance L0 (uH)±20% @ 0 A	I rms (A) Typ.	I rms (A) Max.	I sat (A) Max.	I sat (A) Typ.	DCR(mΩ) Max.	DCR(mΩ) Typ.	Thickness (mm)
HXTC160810A-1R0M	1.0	2.0	1.8	2.1	2.3	110	100	1.0Max
HXTC160810A-2R2M	2.2	1.1	1.0	1.2	1.3	290	272	1.0Max
HXTC201208A-1R0M	1.0	3.0	2.7	3.2	3.6	70	63	1.0Max
HXTC201208A-2R2M	2.2	1.6	1.3	1.8	2.2	155	144	1.0Max
HXTC201210A-R47M	0.47	4.5	4.1	4.6	5.1	27	24	1.0Max
HXTC201610A-R47M	0.47	4.8	4.4	4.8	5.3	22	19	1.0Max
HXTC201610A-1R0M	1.00	3.5	3.2	3.5	4.0	42	38	1.0Max
HXTC201610A-2R2M	2.20	2.3	2.0	2.4	2.7	95	85	1.0Max
HXTC252010A-R47M	0.47	5.5	5.0	6	6.7	20	17	1.0Max
HXTC252010A-1R0M	1.0	4.5	4.0	4.5	5	40	36	1.0Max

6. Material List



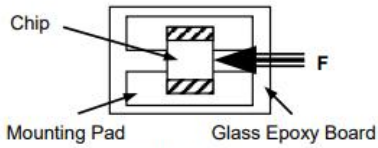
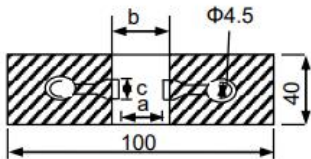
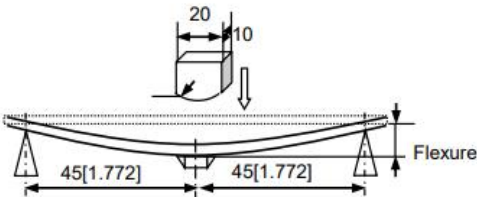
N0.	Description	Specification
①	Metal Alloy Body	Metal Alloy Powder
②	Inner Wire	Enameled Copper Wire
③	Pull-out Electrode	Cu
④	Terminal	Electro-Plating: Cu/Ni/Sn


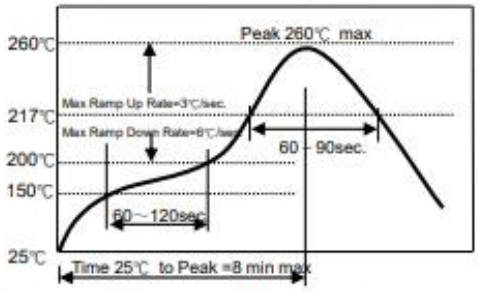
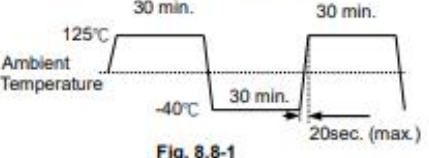
7. Electrical Test

Test Item	Performance	Test Condition
DC Resistance (DCR)	Refer to Electrical Characteristics.	Test equipment: High Accuracy Milliohmmeter-AX-1152D
Inductance (L)		a. Test equipment: High Accuracy RF Impedance Analyzer-WK 6500B. b. Test signal:1V. c. Test frequency refers to Electrical Characteristics.
		a. Set test current to be 0 mA. b. Measure initial temperature of chip surface.

Temperature Rise Current (I _{rms})	Approximately $\Delta T \leq 40^{\circ}\text{C}$.	<ul style="list-style-type: none"> c. Gradually increase voltage and measure chip temperature for corresponding current. d. Definition of Temperature Rise Current (I_{rms}) : I_{rms} is direct electric current as chip surface temperature rose just 40°C against chip initial surface temperature.
Saturation Current (I _{sat})	$\Delta L \leq 30\%$ typical.	<ul style="list-style-type: none"> a. Test equipment: High Accuracy RF Impedance Analyzer- WK 6500B. b. Measuring Frequency: 1MHz. c. Test Current: 1mA. d. Definition of Saturation Current (I_{sat}) : I_{sat} is the value of DC current as inductance L (μH) decreased just 30% against initial value
Self-Resonant Frequency (SRF)	Refer to Electrical Characteristics.	<ul style="list-style-type: none"> a. Test equipment: High Accuracy RF Impedance Analyzer--WK 6500B. b. Test signal: 1V.

8. Reliability Test

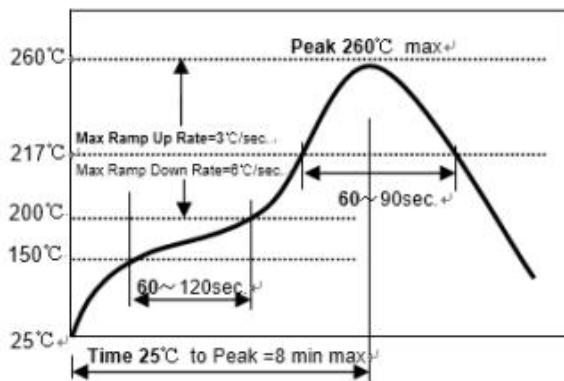
Items	Requirements	Test Methods and Remarks																								
8.1 Terminal Strength	<p>No removal or split of the termination or other defects shall occur.</p>  <p style="text-align: center;">Fig. 8.1-1</p>	<ul style="list-style-type: none"> ① Solder the inductor to the testing jig (glass epoxy board shown in Fig.8.1-1) using eutectic solder. Then apply a 10N force in the direction of the arrow. ② Keep time: 10±1s. ③ Speed: 1.0mm/s. 																								
8.2 Resistance to Flexure	<p>No visible mechanical damage.</p> <p style="text-align: center;">Unit: mm [inch]</p> <table border="1" style="margin-left: auto; margin-right: auto;"> <thead> <tr> <th>Type</th> <th>a</th> <th>b</th> <th>c</th> </tr> </thead> <tbody> <tr> <td>160808</td> <td>0.6</td> <td>2.2</td> <td>1.2</td> </tr> <tr> <td>201208</td> <td>0.8</td> <td>2.4</td> <td>1.4</td> </tr> <tr> <td>201210</td> <td>0.8</td> <td>2.4</td> <td>1.4</td> </tr> <tr> <td>201610</td> <td>0.8</td> <td>2.4</td> <td>1.4</td> </tr> <tr> <td>252010</td> <td>1.3</td> <td>3.0</td> <td>2.3</td> </tr> </tbody> </table>  <p style="text-align: center;">Fig.8.2-1</p>	Type	a	b	c	160808	0.6	2.2	1.2	201208	0.8	2.4	1.4	201210	0.8	2.4	1.4	201610	0.8	2.4	1.4	252010	1.3	3.0	2.3	<ul style="list-style-type: none"> ① Solder the inductor to the test jig (glass epoxy board shown in Fig.8.2-1) Using a eutectic solder. Then apply a force in the direction shown Fig. 8.2-2. ② Flexure: 2mm. ③ Pressurizing Speed: 0.5mm/sec. ④ Keep time: 30 sec. ⑤ Test board size: 100×40×1.0.  <p style="text-align: center;">Fig.8.2-2</p>
Type	a	b	c																							
160808	0.6	2.2	1.2																							
201208	0.8	2.4	1.4																							
201210	0.8	2.4	1.4																							
201610	0.8	2.4	1.4																							
252010	1.3	3.0	2.3																							
8.3 Vibration	<ul style="list-style-type: none"> ① No visible mechanical damage. ② Inductance change: Within ±10%. 	<ul style="list-style-type: none"> ① Solder the inductor to the testing jig (glass epoxy board shown in Fig.8.3-1) using eutectic solder. 																								

	 <p style="text-align: center;">Glass Epoxy Board Fig. 8.3-1</p>	<p>② The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz.</p> <p>③ The frequency range from 10 to 55 Hz and return to 10 Hz shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in each 3 mutually perpendicular directions (total of 6 hours).</p>
8.4 Dropping	<p>① No visible mechanical damage.</p> <p>② Inductance change: Within $\pm 10\%$.</p>	Drop chip inductor 10 times on a concrete floor from a height of 100 cm.
8.5 Temperature	Inductance change should be within $\pm 20\%$ of initial value measuring at 25°C.	<p>Temperature range: $-40^{\circ}\text{C} \sim +125^{\circ}\text{C}$</p> <p>Reference temperature: $+25^{\circ}\text{C}$</p>
8.6 Solderability	<p>① No visible mechanical damage.</p> <p>② Wetting shall exceed 90% coverage.</p>	<p>① Solder temperature: $245 \pm 2^{\circ}\text{C}$</p> <p>② Duration: 3 sec.</p> <p>③ Solder: Sn/3.0Ag/0.5Cu.</p> <p>④ Flux: 25% Resin and 75% ethanol in weight.</p>
8.7 Resistance to Soldering Heat	<p>① No visible mechanical damage.</p> <p>② Inductance change: Within $\pm 10\%$.</p>	<p>① Re-flowing Profile: Please refer to Fig. 8.7-1.</p> <p>② Test board thickness: 1.0mm</p> <p>③ Test board material: glass epoxy resin</p> <p>④ The chip shall be stabilized at normal condition for 1~2 hours before measuring</p> 
8.8 Thermal Shock	<p>① No mechanical damage.</p> <p>② Inductance change: Within $\pm 10\%$.</p>  <p style="text-align: center;">Fig. 8.8-1</p>	<p>① Temperature, Time: (See Fig.8.8-1) -40°C for 30 ± 3 min \rightarrow 125°C for 30 ± 3 min.</p> <p>② Transforming interval: 20 sec.(Max.).</p> <p>③ Tested cycle: 100 cycles.</p> <p>④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
8.9 Resistance to Low Temperature	<p>① No mechanical damage.</p> <p>② Inductance change: Within $\pm 10\%$.</p>	<p>① Temperature: $-40 \pm 2^{\circ}\text{C}$</p> <p>② Duration: 1000^{+24} hours.</p> <p>③ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>
8.10 Resistance to High Temperature	<p>① No mechanical damage.</p> <p>② Inductance change: Within $\pm 10\%$.</p>	<p>① Temperature: $125 \pm 2^{\circ}\text{C}$</p> <p>② Duration: 1000^{+24} hours.</p> <p>③ The chip shall be stabilized at normal condition for 1~2 hours before measuring.</p>

8.11 Damp Heat (Steady States)	① No visible mechanical damage. ② Inductance change: Within $\pm 10\%$.	① Temperature: $60\pm 2^{\circ}\text{C}$ ② Humidity: 90% to 95% RH. ③ Duration: 1000^{+24} hours. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
8.12 Loading Under Damp Heat	① No visible mechanical damage. ② Inductance change: Within $\pm 10\%$.	① Temperature: $60\pm 2^{\circ}\text{C}$ ② Humidity: 90% to 95% RH. ③ Duration: 1000^{+24} hours. ④ Applied current: Rated current. ⑤ The chip shall be stabilized at normal condition for 1~2 hours before measuring.
8.13 Loading at High Temperature (Life Test)	① No visible mechanical damage. ② Inductance change: Within $\pm 10\%$.	① Temperature: $85\pm 2^{\circ}\text{C}$ ② Duration: 1000^{+24} hours. ③ Applied current: Rated current. ④ The chip shall be stabilized at normal condition for 1~2 hours before measuring.

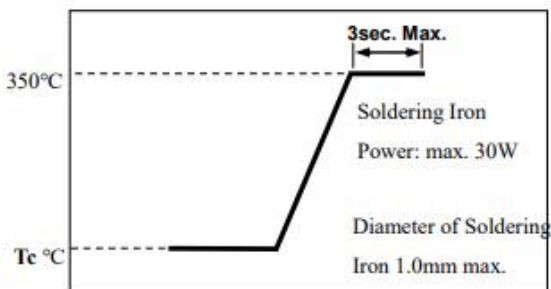
9. Soldering and Mounting

9.1 Reflow Profile:



Preheat condition	150 ~200°C/60~120sec
Allowed time above	217°C: 60~90sec
Max temp	260°C
Max time at Max temp	10sec
Solder paste	Sn/3.0Ag/0.5Cu
Allowed Reflow time	2x Max

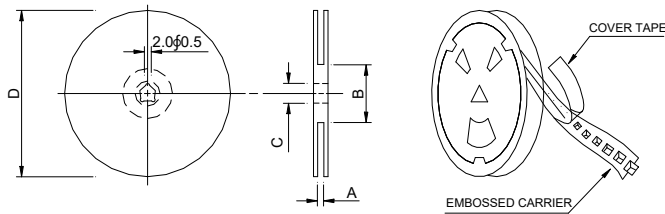
9.2 Reflow Profile:



Iron soldering power	Max.30W
Pre-heating	150 °C / 60sec
Soldering Tip temperature	350°C Max
Soldering time	3sec Max
Solder paste	Sn/3.0Ag/0.5Cu
Max	1 times for iron soldering

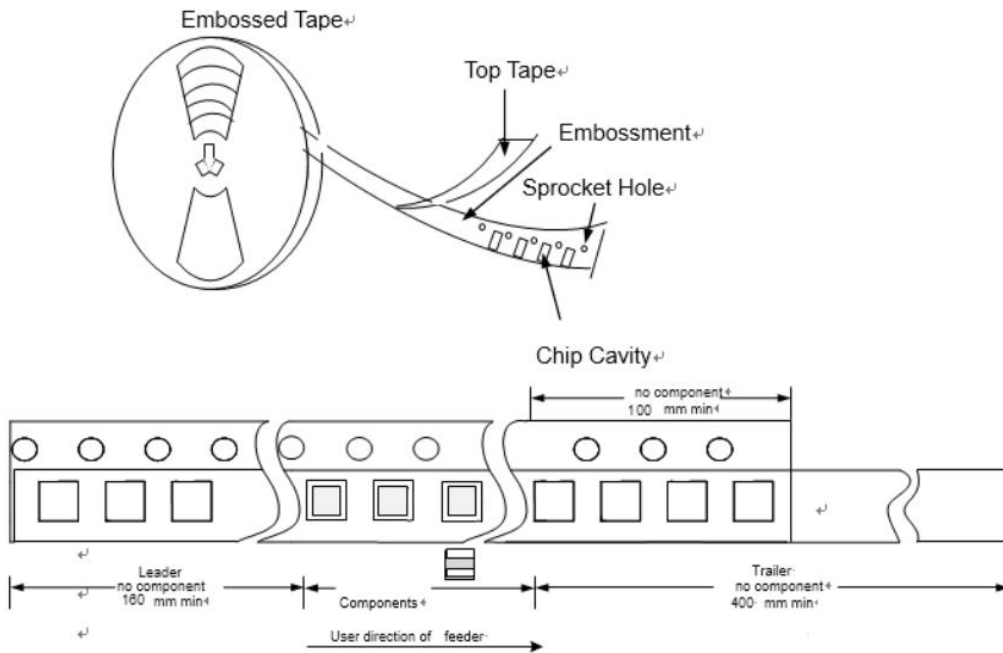
10. Packaging Information

(1) Reel Dimension



A(mm)	B(mm)	C(mm)	D(mm)
8.4	58	13.5	178

(2) Tape Dimension

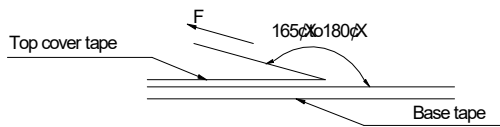


Type	Ao	Bo	P	Po	P1	Ko max	t max	W
160810	1.10±0.1	1.90±0.1	4.0±0.1	4.0±0.1	2.0±0.05	1.3	0.3	8.0±0.1
201208	1.50±0.1	2.30±0.1	4.0±0.1	4.0±0.1	2.0±0.05	1.1	0.3	8.0±0.1
201210	1.50±0.1	2.30±0.1	4.0±0.1	4.0±0.1	2.0±0.05	1.3	0.3	8.0±0.1
201610	1.90±0.1	2.30±0.1	4.0±0.1	4.0±0.1	2.0±0.05	1.3	0.3	8.0±0.1
252010	2.30±0.1	2.80±0.1	4.0±0.1	4.0±0.1	2.0±0.05	1.3	0.3	8.0±0.1

(3) Packaging Quantity

HXTC	160810	201208	201210	201610	252010
Chip / Reel	4000	4000	4000	4000	3000

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions (referenced ANSI/EIA-481-C-2003 of 4.11 standard).

Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)	Tearing Speed mm/min
5~35	45~85	860~1060	300

Application Notice

Storage Conditions

To maintain the solderability of terminal electrodes:

1. ASDI products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
2. Temperature and humidity conditions: Less than 30°C and 70% RH.
3. Recommended products should be used within 6 months from the time of delivery.
4. The packaging material should be kept where no chlorine or sulfur exists in the air.

Transportation

1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价，库存，交付和生命周期等信息

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