

Specification for Approval

Date: 2023/06/06

Customer:

TAI-TECH P/N: TMPF0606LRV-220MN-D-HD

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

pcs

REMARK:					
Cu	stomer Approval Feedba	ack			

□ 西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd <u>Headquarter:</u> NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C. TEL: +886-3-4641148 FAX: +886-3-4643565 http://www.tai-tech.com.tw E-mail: sales@tai-tech.com.tw

Sales Dep.

APPROVED	CHECKED
Eric Kuan	Zhang mengmeng

R&D Center

APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Cui lingling

□ 臺慶精密電子(昆山)有限公司 TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA TEL: +86-512-57619396 FAX: +86-512-57619688 E-mail: sales@tai-tech.cn

■ 慶邦電子元器件(泗洪)有限公司 TAIPAQ ELECTRONICS (SIHONG) CO., LTD JIN SHA JIANG ROAD, CONOMIC DEVELOPMENT ZONE SIHONG, JIANGSU, CHINA. TEL: +86-527-88601191 FAX: +86-527-88601190 E-mail: sales@taipaq.cn

SMD Power Inductor

TMPF0606LRV-220MN-D-HD

		ECN HIST	ORY LIST	-	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	23/06/06	New Issue	Sky Luo	Mr.Liang	Cui lingling
備					
注					

P1

SMD Power Inductor

1. Features

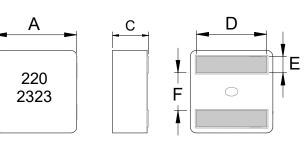
- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.

2. Applications

Commercial applications

3. Dimensions

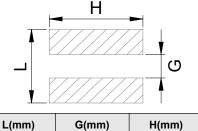
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TMPF0606LRV-220MN-D-HD

Recommend PC Board Pattern



-()	•()		
5.6 ref	2.5 ref	5.6 ref	
star 4 DOD Law	with the second se		- 4 -

Note: 1.PCB layout is referred to standard IPC-7351B 2. The above PCB layout reference only.

3. Recommend solder paste thickness at

0.15mm and above.

Series	A	B	C	D	E	F
	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)
TMPF0606LRV	6.6±0.2	6.4±0.2	5.8±0.2	5.3±0.3	1.4±0.2	2.6±0.25

4. Part Numbering

TMPF	0606	LRV	-	220	MN -	D -	HD
А	В	С		D	E	F	G
A: Series B: Dimen			BxC				
C: Type D: Induct	ance		Mate 220=	erial. =22.0uH			
E: Inducta	ance Tolera	nce	M=±	20%			
F: Date C G:Code	ode		Mark	ing: Black	.220 and 232	3 (23 YY	,23 WW,follow production date).



DCR Test

5. Specification

	Inductance	I rms (А) Тур	l sat	(A)	DCR	DCR
Part Number	(uH) ±20% @ 0 A	20℃ rise	40℃ rise	Тур	Мах	(mΩ) Typ	(mΩ) Max
TMPF0606LRV-220MN-D-HD	22.0	3.8	5.0	5.6	5.0	55.0	60.5

Note:

- 2. All test data referenced to $25\,^\circ\!\!\mathbb{C}$ ambient.
- 3. Testing Instrument(or equ) : Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.

4. Current that causes the specified temperature rise from 25 $^\circ\,$ C ambient.

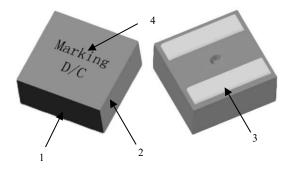
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 155°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Irms Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.

8. Ratedoperatingvoltage(acrossinductor)15V ref.

9. Rated DC current: The lower value of Irms and Isat.

^{1.} Test frequency : L : 100KHz /0.1V.

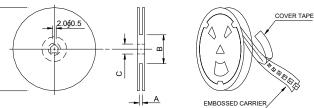
6. Material List



NO	Items	Materials		
1	Core	Alloy powder.		
2	Wire	Polyester Wire or equivalent.		
3	Solder	100% Pb free solder		
4	Ink	Halogen-free ketone		

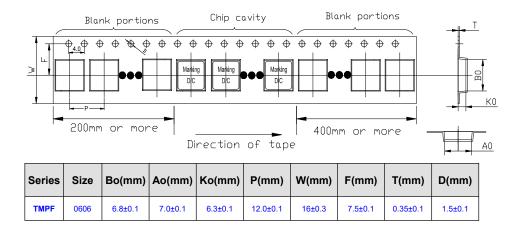
7. Packaging Information

(1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x16mm	16.4+2/-0	100±2	13+0.5/-0.2	330

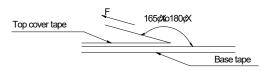
(2) Tape Dimension



(3) Packaging Quantity

TMPF	0606	
Chip / Reel	750	

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(℃)	(%)	(hPa)
300±10%	5~35	45~85	860~1060

8. Reliability and Test Condition

Item	Performance	Test Condition			
Operating temperature	-55~+155 ^{°C} (Including self - temperature rise)				
Storage temperature and Humidity range	110~+40°C,50~60%RH (Product with taping) 255~+155°C(on board)				
Electrical Performance Test	2. 00 (100 c(01 board)				
Inductance		HP4284A,CH11025,CH3302,CH1320,CH1320S			
DCR	Refer to standard electrical characteristics list.	LCR Meter. CH16502,Agilent33420A Micro-Ohm Meter.			
Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0			
		to drop △L(%) Heat Rated Current (Irms) will cause the coil temperature rise ∠			
Heat Rated Current (Irms)	Approximately △T20℃&△T40℃	T(°C). 1.Applied the allowed DC current			
Reliability Test		2. Temperature measured by digital surface thermometer			
		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC			
High Temperature Exposure(Storage)		J-STD-020E Classification Reflow Profiles Temperature ÷ 155±2℃(Inductor)			
AEC-Q200		Duration :1000hrs Min. Measured at room temperature after placing for 24±2 hrs			
		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles			
		Condition for 1 cycle			
Temperature Cycling AEC-Q200		Step1 : -55±2℃ 30min Min.(Inductor) Step2 : 155±2℃ transition time 1min MAX.			
		Step3 : 155±2°C 30min Min.			
		Step4 ÷ Low temp. transition time 1min MAX. Number of cycles ÷ 1000			
	_	Measured at room temperature after placing for 24±2 hrs			
		t=24 hours/cycle. Note:Steps 7a & 7b Unpowered.			
		70 101 104, C00+ 100 - 1041 100 - 1041 63 101 104, C00+ 100 - 1041 100 - 1041 64 4, 600 7074 100 - 1041 100 - 1041 65 102 - 1041 100 - 1041 100 - 1041 66 4, 600 7074 100 - 1041 100 - 1041 75 100 - 1041 100 - 1041 100 - 1041			
	Appearance: No damage.	45 HUNDITY L 40 UKCOKTROLLO 35 L			
Moisture Resistance	Inductance: within \pm 10% of initial value	70 70 70 70 70 70 70 70 70 70			
(AEC-Q200)	RDC: within \pm 15% of initial value and shall not exceed the specification value	10 INITIAL MEADMEMENTS -2*C 0 0 15 AS SPECIFICD IN 3.2 0 0 - VIDBATC 15 10 0 0 0 0 0 0 0			
		> - VOLTAGE APPLIED AS SPECIFIED IN 3.5 0 0 3.4.2 - - - - 0 0 0 0 - - - - 0			
		-30 SHALL BE PERFORMED A MIXIPUS OF			
		PRIOR TO FIRST CYCLE STEP 1 STEP 2 STEP 3 STEP 4 STEP 5 STEP 6 STEP 7 UNKESS OTHERMISE SPECIFIED - ONE CYCLE 24 HOURS. REPEAT AS SPECIFIED IN 3.3			
		Preconditioning: Run through IR reflow for 3 times.			
Biased Humidity		(IPC/JEDEC J-STD-020E Classification Reflow Profiles)			
(AEC-Q200)		Humidity :85±3% R.H, Temperature:85℃±2℃			
		Duration : 1000hrs Min Measured at room temperature after placing for24±2hrs			
	-	Preconditioning: Run through IR reflow for 3 times.			
High Temperature Operational Life		(IPC/JEDECJ-STD-020E Classification Reflow Profiles			
(AEC-Q200)		Temperature : 155±2℃ (Inductor) Duration : 1000hrs Min. With 100% rated current.			
		Measured at room temperature after placing for24±2hrs			
External Visual	Appearance: No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.			
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement			
Resistance to Solvents	Appearance: No damage.	Add aqueous wash chemical - OKEM clean or equivalent.			
		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC			
Mechanical Shock	Appearance: No damage. Inductance: within \pm 10% of initial value RDC: within \pm 15% of initial value and shall not exceed the specification value	J-STD-020E Classification Reflow Profiles) Test condition			
		Type Peak value Normal Wave Velocity			
		Type (g's) duration (D) (ms) form change (Vi)ft/sec SMD 100 6 Half-sine 12.3			
		Lead 100 6 Half-sine 12.3			
		3 shocks in each direction along 3 perpendicular axes(18 shocks).			

Item	Performance	Test Condition		
Vibration		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment: Vibration checker Total Amplitude: 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).		
Resistance to Soldering Heat	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value	Test condition::(MIL-STD-202 Condition B) Temperature (° C) Time (s) Temperature ramp/immersion and emersion rate Number of heat cycles		
		$\begin{tabular}{ c c c c c c c c c c c c c c c c c c c$		
		Temperature time 25°C to peak temperature component T _{sman} T _{sman} T _{sman} t _s ramp up reflow process		
		Component Ramp up to 160°C Texms Tit t, Tit Texms Ramp down		
		Tockess 1 dm -2 dm and Votame < 30 mad 3 00 1°C s compart compart specified 210° cm 200° cm 210° cm 200° cm 20° cm 20° cm		
Thermal shock (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1 : -55±2℃ 15±1min(Inductor) Step2 : 155±2℃ 15±1min Number of cycles: 300 Measured at room fempraturc after placing fo24±2hrs		
ESD	Appearance: No damage.	10% to the test of the test of		
Solderability	More than 95% of the terminal electrode should be covered with solder.	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C		
	Refer Specification for Approval	Test time: 30 +0/-0.5 seconds. Summary to show Min, Max, Mean and Standard deviation .		
Electrical Characterization Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.		
Board Flex	Appearance: No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) $x = 2$ mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed circuit board before testing 45 ± 2 45 ± 2 45 ± 2 45 ± 2 45 ± 2		
		Probe to exert bending force Radius 340 Printed circuit board under test		

Terminal Strength(SMD)	Appearance: No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.
		substrate press tool shear force

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

9. Soldering Specifications

(1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Soldering Reflow:

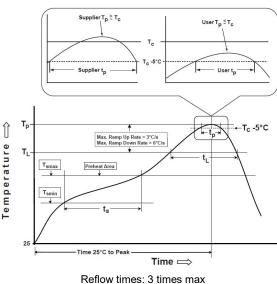
Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

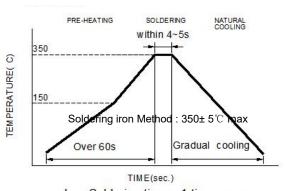
(3) Iron Reflow:

- Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)
- $\,\cdot\,\,$ Preheat circuit and products to 150 $^\circ\!\!\!\!^\circ$ $\,\cdot\,\,$ Never contact the ceramic with the iron tip
- · 355°C tip temperature (max) 1.0mm tip diameter (max)
- the iron tip · Use a 20 watt soldering iron with tip diameter of 1.0mm · Limit soldering time to 4~5sec.

Fig.2 Iron Reflow

Fig.1 Soldering Reflow





Iron Soldering times: 1 times max.

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(t _s)from(T _{smin} to T _{smax})	150℃ 200℃ 60-120seconds
Ramp-up rate(T∟to T _p)	3℃/second max.
Liquidus temperature(T _L) Time(t _L)maintained above T _L	217℃ 60-150 seconds
Classification temperature(T _c)	See Table (1.2)
Time(t_p) at Tc- 5 $^\circ\!\!\!\!^\circ C$ (Tp should be equal to or less than Tc.)	< 30 seconds
Ramp-down rate(T_p to T_L)	6℃ /second max.
Time 25 $^\circ C$ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E.

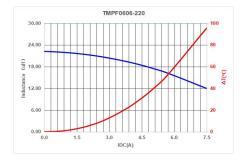
10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 \pm 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification.

Application Notice

- Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40 $^\circ\!\mathrm{C}$ and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

11. Typical Performance Curves



单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)