

# Specification for Approval Date: 2023/07/26 Customer :

TAI-TECH P/N: TMPC0503HPV-Series(G)-HD

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

pcs

REMARK:		
Cu	stomer Approval Feedba	ack

□西北臺慶科技股份有限公司 TAI-TECH Advanced Electronics Co., Ltd <u>Headquarter:</u> NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C. TEL: +886-3-4641148 FAX: +886-3-4643565 http://www.tai-tech.com.tw E-mail: sales@tai-tech.com.tw

Sales Dep.

APPROVED	CHECKED
Eric Kuan	Zhang mengmeng

#### R&D Center

APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Cui lingling

□臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA TEL: +86-512-57619396 FAX: +86-512-57619688 E-mail: sales@tai-tech.cn

■慶邦電子元器件(泗洪)有限公司 TAIPAQ ELECTRONICS (SIHONG) CO., LTD JIN SHA JIANG ROAD, CONOMIC DEVELOPMENT ZONE SIHONG, JIANGSU, CHINA. TEL: +86-527-88601191 FAX: +86-527-88601190 E-mail: sales@taipaq.cn

# **SMD** Power Inductor

TMPC0503HPV-Series(G)-HD

		ECN HIST	ORY LIS	<b>ST</b>	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	23/04/20	New Issue	Sky Luo	Mr.Liang	Cui lingling
備					
注					

# **SMD** Power Inductor

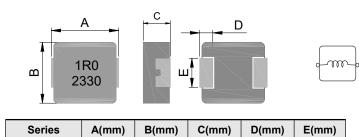
#### 1. Features

- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.
- 5. High reliability -Reliability test complied to AEC-Q200.

# 2. Applications

Automotive applications.

#### 3. Dimensions



 $2.8\pm0.2$ 

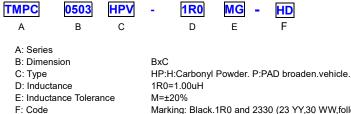
1.1±0.3

 $5.2 \pm 0.2$ 

# 4. Part Numbering

 $5.7\!\pm\!0.3$ 

TMPC0503HP



Marking: Black.1R0 and 2330 (23 YY,30 WW,follow production date).

 $2.5\!\pm\!0.3$ 

# **Recommend PC Board Pattern**

Halogen-free

AEC-Q200

L(mm)	G(mm)	H(mm)	
6.5	2.5	2.8	
Note: 1.PCB layout is referred to standard IPC-7351E			

2. The above PCB layout reference only. 3. Recommend solder paste thickness at 0.12mm and above.

TMPC0503HPV-Series(G)-HD

RoH

# 5. Specification

Part Number	Inductance (uH) ±20%	Irms(A)	Isat ( A )	DCR	(mΩ)
	@0A	Тур	Тур	Тур	Max
TMPC0503HPV-R10YG-HD	0.10±30%	24.0	40.0	2.20	2.60
TMPC0503HPV-R15YG-HD	0.15±30%	22.0	37.0	2.50	3.00
TMPC0503HPV-R22MG-HD	0.22	19.0	32.0	3.20	3.80
TMPC0503HPV-R33MG-HD	0.33	15.0	20.0	4.30	5.00
TMPC0503HPV-R47MG-HD	0.47	13.0	18.0	6.30	7.10
TMPC0503HPV-R56MG-HD	0.56	12.0	17.0	7.80	8.60
TMPC0503HPV-R68MG-HD	0.68	11.0	15.5	8.00	9.00
TMPC0503HPV-R82MG-HD	0.82	10.0	14.0	8.80	10.0
TMPC0503HPV-1R0MG-HD	1.00	9.0	13.0	11.0	12.7
TMPC0503HPV-1R5MG-HD	1.50	8.0	11.5	14.4	16.6
TMPC0503HPV-2R2MG-HD	2.20	7.0	11.0	26.0	29.2
TMPC0503HPV-3R3MG-HD	3.30	6.0	9.0	33.0	38.0
TMPC0503HPV-4R7MG-HD	4.70	5.5	8.0	48.0	53.0
TMPC0503HPV-5R6MG-HD	5.60	4.7	7.3	54.0	62.0
TMPC0503HPV-6R8MG-HD	6.80	4.2	6.0	68.0	76.2
TMPC0503HPV-100MG-HD	10.0	3.5	4.0	104	120
TMPC0503HPV-150MG-HD	15.0	2.7	3.2	165	190
TMPC0503HPV-220MG-HD	22.0	2.2	2.7	217	250

Note:

1. Test frequency : Ls : 100KHz /1.0V.

2. All test data referenced to 25  $^\circ\!\!\mathbb{C}$  ambient.

3. Testing Instrument(or equ) : Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.

4. Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\, {\it \Delta}T$  of 40  $^\circ\!{\rm C}$ 

5. Saturation Current (Isat) will cause L0 to drop approximately 30%.

6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.

7. Irms Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.

Therefore temperature rise should be verified in application conditions.

8.Rated DC current: The lower value of Irms and Isat.

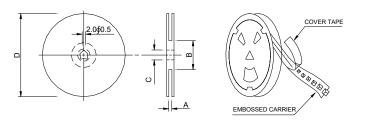
#### 6. Material List



NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	paint	Epoxy resin
5	Ink	Halogen-free ketone

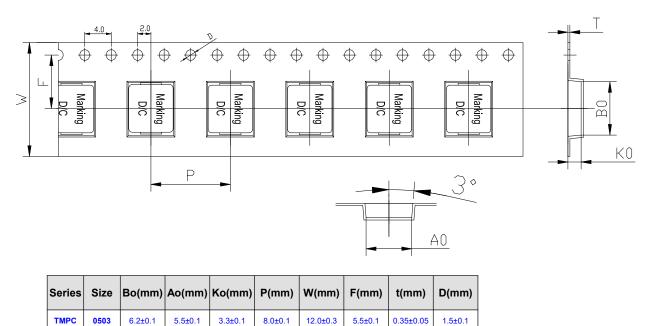
# 7. Packaging Information

#### (1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x12mm	12.4+2/-0	100±2	13+0.5/-0.2	330

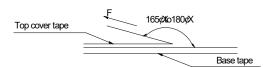
#### (2) Tape Dimension



#### (3) Packaging Quantity

ТМРС	0503
Chip / Reel	2000

#### (4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(°C)	(%)	(hPa)
300±10%	5~35	45~85	860~1060

# 8. Reliability and Test Condition

ltem	Performance	Test Condition		
Operating temperature	-55~+125°C (Including self - temperature rise)			
Storage temperature and lumidity range	110~+40°C,50~60%RH (Product with taping) 255~+125°C (on board)			
lectrical Performance Test				
nductance		HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.		
CR	Refer to standard electrical characteristics list.	CH16502,Agilent33420A Micro-Ohm Meter.		
aturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0 to drop		
leat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise T(°C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer		
Reliability Test	1			
High Temperature Exposure(Storage) AEC-Q200		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020EClassification Reflow Profiles Temperature: 125±2°C (Inductor · ambient + temp rise) Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs		
Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55±2°C 30min Min.(Inductor) Step2: 125±2°C transition time 1min MAX. Step3: 125±2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs		
Moisture Resistance (AEC-Q200)		13 		
Biased Humidity (AEC-Q200)		The locate → 0.1.2.3.4.5.4.7.4.9.10.11.2.1.0.11.0.10.11.0.10.2.0.2.0.2.0		
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles Temperature: 125±2°C (Inductor) Duration : 1000hrs Min. With 100% rated current. Measured at room temperature after placing for24±2hrs		
External Visual	Appearance : No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.		
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement		
Resistance to Solvents	Appearance : No damage.	Add aqueous wash chemical - OKEM clean or equivalent.		
Mechanical Shock	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition Type         Peak value         Normal duration (D) (ms)         Wave form         Velocity change (Vi)ft/sec           SMD         100         6         Half-sine         12.3		
	specification value			

Vibration       Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDE: J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment : Vibration checker Total Amplitude: 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) •         Test condition::(MIL-STD-202 Condition B)         Temperature (C)       Temperature ramp/immersion and emersion rate         260 ±5       10 ±1       25mm/s ±6 mm/s       1         Depth: completely cover the termination       Continental       1	EC
Test condition::(MIL-STD-202 Condition B)         Temperature (C)       Time (s)       Temperature ramp/immersion and emersion rate       Number of cyclet         260 ±5 (solder temp)       10 ±1       25mm/s ±6 mm/s       1         Depth: completely cover the termination Continental       Continental	
Image: Second	
Resistance to Soldering Heat	
Q: Shall not exceed the specification value. RDC: within ± 15% of initial value and shall not exceed the specification value	
Trackets > 3 mmad Trackets > 3 mmad Towness > 3 mmad Towness > 3 mmad Towness > 3 mmad Trackets > 3 mma	6.0±0.1°C/s (The component shall be specified for usage in senal production with up to 6.0°C/s)
Thermal shock       Preconditioning: Run through IR reflow for 3 times.( IPC/, J-STD-020DClassification Reflow Profiles Condition for 1 cycle         Thermal shock       Step1: -55±2°C 15±1min(Inductor)         (AEC-Q200)       Step2: 125±2°C within 20Sec.         Step3: 125±2°C 15±1min       Number of cycles : 300         Measured at room fempraturc after placing fo24±2hrs	JEDEC
ESD Appearance : No damage. Direct Contact and Air Discharge PASSIVE COMPONENT HBM ES Discharge Waveform to a Coaxial Target Test method: AEC-Q200-002	SD
Test mode : Contact Discharge Discharge level : 4 KV (Level: 2) a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds.	
Solderability More than 95% of the terminal electrode should be covered with solder • b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5° Test time: 30 +0/-0.5 seconds.	5°C
Electrical Characterization Refer Specification for Approval Summary to show Min, Max, Mean and Standard deviation .	
Flammability Electrical Test not required. V-0 or V-1 are acceptable.	

Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board.
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (18 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

### 9. Soldering Specifications

#### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

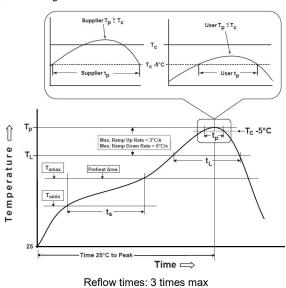
#### (3) Iron Reflow:

- Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)
- Preheat circuit and products to  $150^\circ\!\mathrm{C}$ • 355℃ tip temperature (max)

Table (1.1): Reflow Profiles

· 1.0mm tip diameter (max)

- · Never contact the ceramic with the iron tip Use a 20 watt soldering iron with tip diameter of 1.0mm Limit soldering time to 4~5sec.
- Fig.1 Soldering Reflow



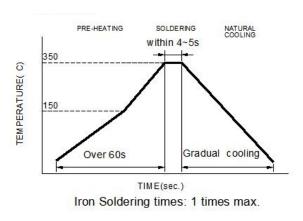


Fig.2 Iron soldering temperature profiles

Soldering iron Method : 350± 5℃ max

Profile Type:	Pb-Free Assembly			
Preheat				
-Temperature Min(T <sub>smin</sub> )	<b>150</b> ℃			
-Temperature Max(T <sub>smax</sub> )	200℃			
-Time( $t_s$ )from( $T_{smin}$ to $T_{smax}$ )	60-120seconds			
Ramp-up rate(T <sub>L</sub> to T <sub>p</sub> )	3℃/second max.			
Liquidus temperature(T <sub>L</sub> )	217℃			
Time(t <sub>L</sub> )maintained above $T_L$	60-150 seconds			
Classification temperature(T <sub>c</sub> )	See Table (1.2)			
$Time(t_p)$ at Tc- $5^\circ\!\mathbb{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds			
Ramp-down rate( $T_P$ to $T_L$ )	6℃ /second max.			
Time 25 $^\circ\!\!\!\!^\circ \mathrm{C}$ to peak temperature	8 minutes max.			

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

\* Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

#### Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E °

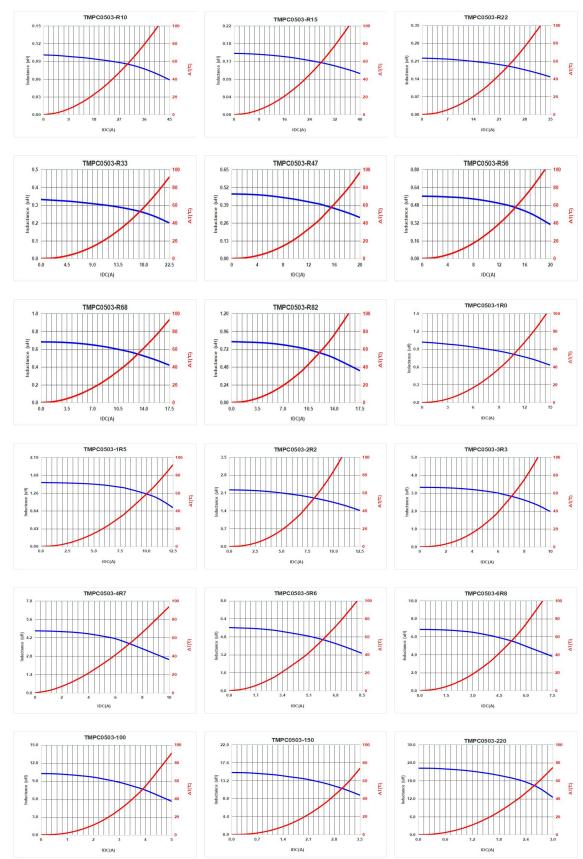
#### 10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method - and dry it off immediately -
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly , and marking disappearnc .
- (9) The high power ultrasonic washing may damage the choke body .
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification.

#### Application Notice

- Storage Conditions
  - To maintain the solderability of terminal electrodes:
  - 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
  - 2. Temperature and humidity conditions: Less than  $40^{\circ}$ C and 60% RH.
  - 3. Recommended products should be used within 12 months form the time of delivery.
  - 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

# **11. Typical Performance Curves**



单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)