

Specification for Approval

Date: 2023/07/13

Customer:

TAI-TECH P/N: TMPF0505LRV-220MN-ABD-HD

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

pcs

REMARK:		
Cu	stomer Approval Feedba	ack

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SMD Power Inductor

TMPF0505LRV-220MN-ABD-HD

		ECN HIST	ORY LIST	•	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	23/07/13	New Issue	Sky Luo	Mr.Liang	Cui lingling
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注					

SMD Power Inductor

1. Features

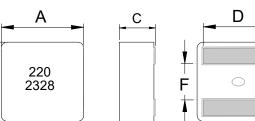
- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.
- 5. High reliability -Reliability test complied to AEC-Q200.

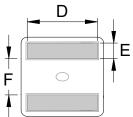
2. Applications

Automotive applications.

3. Dimensions

В

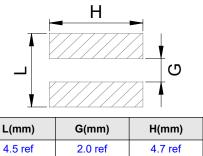






Recommend PC Board Pattern

TMPF0505LRV-220MN-ABD-HD



- Note: 1.PCB layout is referred to standard IPC-7351B 2. The above PCB layout reference only. 3. Recommend solder paste thickness at
 - 0.12mm and above.

Series	A (mm)	B (mm)	C (mm)	D (mm)	E (mm)	F (mm)
TMPF0505LRV-ABD	6.0±0.2	5.7±0.2	4.8±0.2	4.3±0.3	1.1±0.2	2.3±0.25

4. Part Numbering

TMPF	0505	LRV	-	220	MN -	ABD -	HD	
А	В	С		D	E	F	G	- Provide State
A: Series								1111
B: Dimen	sion		BxC					and the second se
C: Type			Mater	al.				
D: Inducta	ance		220=2	2.0uH				
E: Inducta	ance Tolera	nce	M=±2	0%				
F: DateC	ode		Marki	ng: Black	c.220 and 23	28 (23 YY, 28	3WW,follow production date).
			AB:ov	ersize				
G: Code								



DCR Test

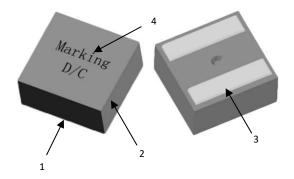
5. Specification

Daví Numban	Inductance	I rms (А) Тур	l sat	(A)	DCR(mΩ)	DCR(mΩ)
Part Number	(uH) ±20% @ 0 A	20℃ rise	40℃ rise	Тур	Мах	Тур	Max
TMPF0505LRV-220MN-ABD-HD	22.0	2.50	3.40	5.00	4.10	90.6	99.65

Note:

- 1. Test frequency : L : 100KHz /0.1V.
- 2. All test data referenced to 25° C ambient.
- 3. Testing Instrument(or equ) : Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.
- 4. Current that causes the specified temperature rise from 25° C ambient.
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%
- 6. The part temperature (ambient + temp rise) should not exceed 155[°]C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Irms Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components. Therefore temperature rise should be verified in application conditions.
- 8. Rated operating voltage(across inductor)15V Ref.
- 9. Rated DC current: The lower value of Irms and Isat.

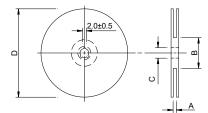
6. Material List

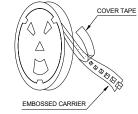


NO	Items	Materials
1	Core	Alloy powder.
2	Wire	Polyester Wire or equivalent.
3	Solder	100% Pb free solder
4	Ink	Halogen-free ketone

7. Packaging Information

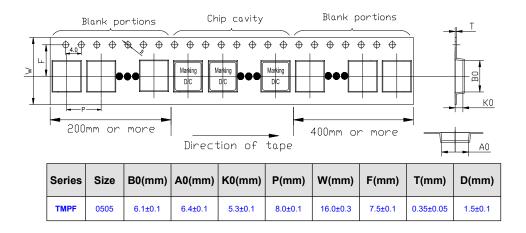
(1) Reel Dimension



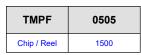


Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x16mm	16.4+2/-0	100±2	13+0.5/-0.2	330

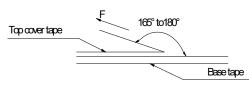
(2) Tape Dimension



(3) Packaging Quantity



(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Т	earing Speed	Room Temp.	Room Humidity	Room atm
	mm	(℃)	(%)	(hPa)
	300±10%	5~35	45~85	860~1060

8. Reliability and Test Condition

Item	Performance	Test Condition			
Operating temperature	-55~+155°C(Including self - temperature rise)				
Storage temperature and Humidity range	110~+40℃,50~60%RH (Product with taping) 255~+155℃(on board)				
Electrical Performance Test					
Inductance		HP4284A,CH11025,CH3302,CH1320,CH1320S			
DCR	Refer to standard electrical characteristics list.	LCR Meter. CH16502,Agilent33420A Micro-Ohm Meter.			
Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0			
		to drop △L(%) Heat Rated Current (Irms) will cause the coil temperature rise ∠			
Heat Rated Current (Irms)	Approximately △T40℃	T(℃). 1.Applied the allowed DC current			
Reliability Test		2. Temperature measured by digital surface thermometer			
High Temperature Exposure(Storage) AEC-Q200		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 155±2°C (Inductor) Duration : 1000hrs Min. Measured at room temperature after placing for 24±2 hrs Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle			
Temperature Cycling AEC-Q200		Step1: -55±2°C 30min Min.(Inductor) Step2: 155±2°C transition time 1min MAX. Step3: 155±2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs			
Moisture Resistance (AEC-Q200)	Appearance: No damage. Inductance: within \pm 10% of initial value Q: Shall not exceed the specification value. RDC: within \pm 15% of initial value and shall not exceed the specification value	t=24 hours/cycle. Note:Steps 7a & 7b Unpowered.			
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity : 85±3 % R.H, Temperature: 85℃±2℃ Duration : 1000hrs Min Measured at room temperature after placing for24±2hrs			
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles Temperature: 155±2°C (Inductor) Duration : 1000hrs Min. With 100% rated current. Measured at room temperature after placing for24±2hrs			
External Visual	Appearance: No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.			
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement			
Resistance to Solvents	Appearance: No damage.	Add aqueous wash chemical - OKEM clean or equivalent.			
	Appearance: No damage.	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition			
Mechanical Shock	Inductance: within \pm 10% of initial value Q: Shall not exceed the specification value.	(g s) duration (D) (ms) form change (V) fit/sec			
	RDC: within \pm 15% of initial value and shall not exceed the specification value	SMD TOU 6 Half-sine 12.3 Lead 100 6 Half-sine 12.3			

Item	Performance	Test Condition			
Vibration		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment: Vibration checker Total Amplitude: 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).			
		Test condition::(MIL-STD-202 Condition B)			
	Appearance: No damage. Impedance: within±15% of initial value	, , , , , , , , , , , , , , , , , , ,			
		Temperature (° C) Time (s) Temperature ramp/immersion and emersion rate Number of heat cycles 260 ±5 10 ±4 Comm(n + 0) 0 <td< td=""></td<>			
		(solder temp) 10 ±1 25mm/s ±6 mm/s 1			
		Depth: completely cover the termination			
		Continental			
		1 Temperature			
		time 25°C to peak temperature			
		$T_{\text{Smax}} = \frac{T_{L}}{L}$			
Resistance to Soldering Heat		Tsmin ts			
	Inductance: within \pm 10% of initial value	ramp down			
	Q: Shall not exceed the specification value.	ramp up reflow process			
	RDC: within \pm 15% of initial value and shall not exceed the specification value	Time			
		·			
		Component Ramp up to Tsmin ts Tsmax TL tc Tpeat* tp** time Ramp down Size 150°C 25°C to 25°			
		Thickness < 1.5mm or Thickness 1.6mm_2.5mm and peak 250°C 240s			
		Volume < 350 mm3 6 0.04.0.1°C/s			
		1.6mm-2.5mm and component Volume 350-2000 mm ³ shall be s1001 shall be s250°C shall be			
		01 motores > 2.5mm and toget in seral 21107 21105 2207°C 2908 29006 29006 usage in seral Ventore < 80 mm0			
		1.6mm-2.5mm and 3.0°C(s) with up to Volume > 2000 mm ³ 3.0°C(s) >245°C 6.0°C(s)			
		or Thickness > 2.5mm and Volume > 350 mm3			
		Table 1: Minimum requirements for lead-free soldering *peak temperature is measured on the centre top of the component package ** tp measured @ Tpeak-5*C			
	-				
		Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles			
		Condition for 1 cycle			
Thermal shock		Step1: -55±2℃ 15±1min(Inductor) Step2: 155±2℃ within 20Sec.			
(AEC-Q200)		Step3: 155±2°C 15±1min			
		Number of cycles: 300			
		Measured at room fempraturc after placing fo24±2hrs			
		ip 🏚			
		5074			
ESD		10%			
		t, Time (ns)			
		Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD			
		Discharge Waveform to a Coaxial Target			
		Test method: AEC-Q200-002 Test mode: Contact Discharge			
		Discharge level: 4 KV (Level: 2)			
Solderability	More than 95% of the terminal electrode should be covered with	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C			
		Test time:5 +0/-0.5 seconds.			
		b. Method D category 3. (steam aging 8hours ± 15 min)@			
		260°C±5°C			
		Test time: 30 +0/-0.5 seconds.			
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .			
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.			
L	1	1			

Board Flex	Appearance: No damage	J-STD-020E Classification Reflow Profiles Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. Support Solder Chip Printed circuit board before testing 45±2 45±2 45±2 Kussus-w Radius 340 Probe to exert bending force 1.6 Radius 340 Displacement
Terminal Strength(SMD)	Appearance: No damage	Preconditioning: Run through IR reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 + 1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

9. Soldering Specifications

(1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

(3) Iron Reflow:

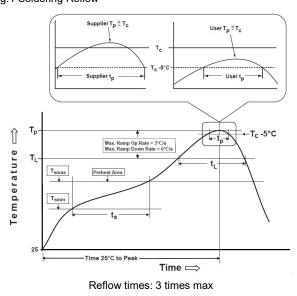
- Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that
- a soldering iron must be employed the following precautions are recommended.(Fig. 2)

· 1.0mm tip diameter (max)

- $\cdot\,$ Preheat circuit and products to 150 $^\circ\!\!\!\!^\circ$ $\,\cdot\,$ Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
 Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

· 355°C tip temperature (max)



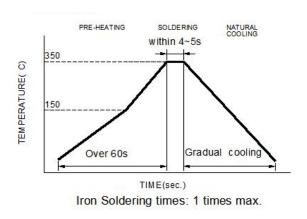


Fig.2 Iron soldering temperature profiles

Soldering iron Method : 350± 5°C max

Profile Type:	Pb-Free Assembly	
Preheat		
-Temperature Min(T _{smin})	150 ℃	
-Temperature Max(T _{smax})	200 ℃	
-Time(t_s)from(T_{smin} to T_{smax})	60-120seconds	
Ramp-up rate(T _L to T _p)	3℃/second max.	
Liquidus temperature(T _L)	217℃	
Time(t_L)maintained above T_L	60-150 seconds	
Classification temperature(T _c)	See Table (1.2)	
Time(t_p) at Tc- 5 $^\circ\!\!\!\!^\circ C$ (Tp should be equal to or less than Tc.)	*< 30 seconds	
Ramp-down rate(T_p to T_L)	6℃ /second max.	
Time 25 $^\circ\!\mathrm{C}$ to peak temperature	8 minutes max.	

Tp: maximum peak package body temperature, **Tc**: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

* Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260 ℃	260 ℃	260° ℃
	1.6-2.5mm	260 ℃	250 ℃	245℃
	≥2.5mm	250℃	245℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020E.

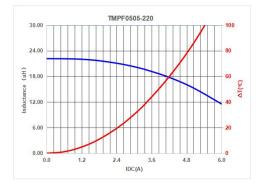
10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 \pm 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification.

Application Notice

- Storage Conditions
 - To maintain the solderability of terminal electrodes:
 - 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
 - 2. Temperature and humidity conditions: Less than 40 °C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

11. Typical Performance Curves



单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)