

Date: 2022/05/24

Customer :

TAI-TECH P/N: TMPF0503A-R33MN-D-HD

CUSTOMER P/N:

DESCRIPTION:

QUANTITY:

pcs

REMARK:					
Customer Approval Feedback					

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APPROVED	CHECKED
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APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Cui lingling

# **SMD** Power Inductor

### TMPF0503A-R33MN-D-HD

	ECN HISTORY LIST							
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN			
1.0	22/05/24	New Issue	Sky Luo	Mr.Liang	Cui lingling			
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) 注								
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# **SMD** Power Inductor

## 1. Features

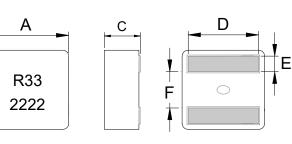
- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.

# 2. Applications

Commercial applications

## 3. Dimensions

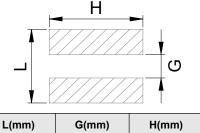
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TMPF0503A-R33MN-D-HD

# **Recommend PC Board Pattern**



-()	•()		
4.5 ref	2.0 ref	4.7 ref	
Note: 1.PCB layo	out is referred to	standard IPC-73	51B

2. The above PCB layout reference only.

 Recommend solder paste thickness at 0.12mm and above.

Series	A	B	C	D	E	F
	(mm)	(mm)	(mm)	(mm)	(mm)	(mm)
TMPF0503A	5.5±0.2	5.3±0.2	2.9±0.2	4.3±0.3	1.1±0.2	2.3±0.25

# 4. Part Numbering

TMPF	0503	A	-	<b>R33</b>	MN -	D -	HD		
A	В	С		D	E	F	G		
A: Series B: Dimen C: Type D: Inducta E: Inducta F: DateCo G:Code	sion ance ance Toleran	ice	R33 M=±	erial. 5=0.33uH ⊧20%	.R33 and 22	222 (22Y	Y, 22 WW,fa	ollow production da	ate).



DCR Test

# 5. Specification

Part Number	Inductance	l rms(A) Typ		l sat(A)		DCR	DCR
	(uH) ±20% @ 0 A	20℃ rise	40℃ rise	Тур	Max	(mΩ) Typ.	(mΩ) Max.
TMPF0503A-R33MN-D-HD	0.33	13.8	19.2	28.0	26.0	3.20	3.52

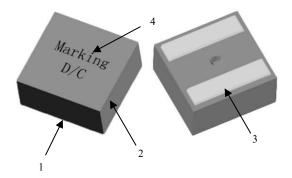
#### Note:

1. Test frequency : L : 100KHz /0.1V.

- 2. All test data referenced to  $25^\circ\!\!\mathbb{C}$  ambient.
- 3. Testing Instrument(or equ) : Agilent 4284A, E4991A, 4339B, KEYSIGHT E4980A/AL, chroma3302, 3250, 16502.
- 4. Current that causes the specified temperature rise from 25  $^\circ\text{C}$  ambient.
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.
- 8. Rated operating voltage(across inductor) 40V ref.
- 9. Rated DC current: The lower value of Irms and Isat.

P2

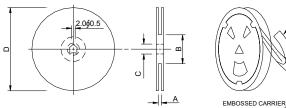
# 6. Material List



NO	Items	Materials		
1	Core	Alloy powder.		
2	Wire	Polyester Wire or equivalent.		
3	Solder	100% Pb free solder		
4	Ink	Halogen-free ketone		

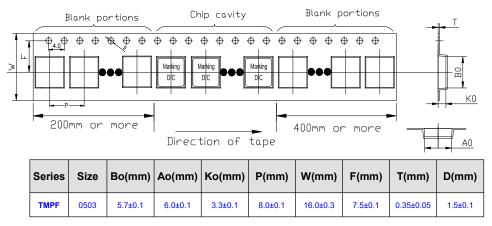
# 7. Packaging Information

(1) Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x16mm	16.4+2/-0	100±2	13+0.5/-0.2	330

### (2) Tape Dimension

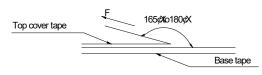


COVER TAPE

(3) Packaging Quantity

TMPF	0503
Chip / Reel	2000

### (4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(℃)	(%)	(hPa)
300±10%	5~35	45~85	860~1060

# 8. Reliability and Test Condition

Item	Performance	Test Condition		
Operating temperature	-40~+125°C (Including self - temperature rise)			
Storage temperature 110~+40°C,50~60%RH (Product without taping) 240~+125°C (on board)				
Electrical Performance	Test			
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.		
DCR		CH16502, Agilent33420A Micro-Ohm Meter.		
Saturation Current (Isat)	Approximately △L30%.	Saturation DC Current (Isat) will cause L0 to drop △L(%)		
Heat Rated Current (Irms)	Approximately △T20°C &△T40°C	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer		
Reliability Test				
Life Test		Preconditioning: Run through IR reflow for 3times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Temperature : 125±2°C(Inductor · ambient + temp rise) Applied current : rated current Duration : 1000±12hrs Measured at room temperature after placing for 24±2 hrs.		
Load Humidity		Preconditioning: Run through IR reflow for 3times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Humidity: 85±2% R.H. Temperature: 85℃±2℃ Duration: 1000hrs Min. Bead:with 100% rated current, Inductance: with 100% rated current Measured at room temperature after placing for 24±2 hrs.		
Moisture Resistance	Appearance <sup>:</sup> No damage. Inductance <sup>:</sup> within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDECJ-STD-020E Classification Reflow Profiles) 1. Baked at50°C for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs. 3. Raise temperature to $65\pm2°C$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25°C$ in 2.5hrs,keep at $25°C$ for 2 hrs then keep at $-10°C$ for 3 hrs 4. Keep at $25°C$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1–2 hrs.		
Thermal shock		$eq:preconditioning: Run through IR reflow for 3 times. (IPC/JEDEC/STD-02DE Classification Reflow Profiles) Condition for 1 cycle Step1: -40±2°C 30±5min Step2: 125±2°C \leq 0.5min Step3: 125±2°C \leq 0.5min Step3: 125±2°C \leq 0.5min Measured at room fempraturc after placing for 24±2 hrs.$		
Vibration		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations).		

## TAI-TECH

Item	Performance			Test	Cond	ition	
Bending	Appearance : No damage.	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.					
	Inductance : within±10% of initial value         Q : Shall not exceed the specification value.	Туре	Peak value (g's)	durati	rmal on (D) າຣ)	Wave form	Velocity change (Vi)ft/sec
Shock	RDC : within $\pm 15\%$ of initial value and shall not exceed the specification value	SME	50	1	1	Half-sine	11.3
		Lead	1 50	1	1	Half-sine	11.3
		3 shock	s in each di	rection a	long 3 pe	erpendicula	r axes(18
Solderability	More than 95% of the terminal electrode should be covered with solder₀	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds.					
		<ul> <li>b. Method D category 3. (steam aging 8hours ± 15 min)@</li> <li>260°C±5°C</li> <li>Test time: 30 +0/-0.5 seconds.</li> </ul>					
		Depth: completely cover the termination					
Resistance to Soldering Heat		Temp	erature(°C)	Time(s)	ramp/i	perature mmersion lersion rate	Number of heat cycles
		-	260 ±5 der temp)	10 ±1	25mm/	's ±6 mm/s	1
Terminal Strength	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 3 times.( IPC/ J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the devic tested, applyaforce(>0805inch(2012mm):1kg,<=0805inch mm):0.5kg)to the side of a device being tested. This for be applied for 60 +1 seconds. Also the force shall be gradually as not to apply a shock to the component being				the device to be 0805inch(2012 . This force shall shall be applied	

Note : When there are questions concerning measurement result : measurement shall be made after  $48 \pm 2$  hours of recovery under the standard condition.

# 9. Soldering Specifications

## (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

### (2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

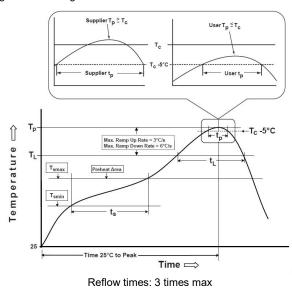
#### (3) Iron Reflow:

- Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)
- Preheat circuit and products to 150  $^\circ\!{\rm C}$ · Never contact the ceramic with the iron tip • 355℃ tip temperature (max)

Table (1.1): Reflow Profiles

- · 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow



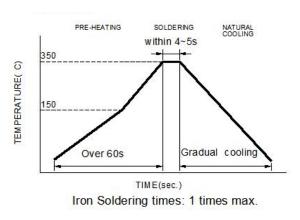


Fig.2 Iron soldering temperature profiles

Soldering iron Method : 350± 5℃ max

	1
Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T <sub>smin</sub> )	150℃
-Temperature Max(T <sub>smax</sub> )	200℃
-Time(ts)from(Tsmin to Tsmax)	60-120seconds
Ramp-up rate( $T_L$ to $T_p$ )	3℃/second max.
Liquidus temperature(T <sub>L</sub> )	<b>217℃</b>
Time(t <sub>L</sub> )maintained above $T_L$	60-150 seconds
Classification temperature(T <sub>c</sub> )	See Table (1.2)
$Time(t_p)$ at Tc- $5^\circ\!\!\mathrm{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate( $T_p$ to $T_L$ )	6℃ /second max.
Time 25 $^\circ\!\!\!\!^\circ {\rm C}$ to peak temperature	8 minutes max.

# Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

\* Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

### Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
	<1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E °

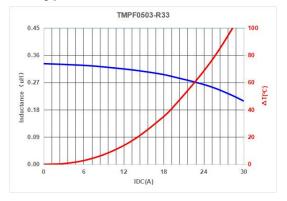
## 10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method , and dry it off immediately .
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification, Or equivalent to the sample provided.

#### Application Notice

- Storage Conditions
  - To maintain the solderability of terminal electrodes:
- 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40  $^\circ\!{\rm C}$  and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

# 11. Typical Performance Curves



# 12 • Appearance criterion

## 1 · PAD residual powder · inner wire exposed · imprinting

The residual powder on both side of pad is norm and within following criteria are acceptable.

a	10% max of the length of pad.	Note 1	
b	5% Max of the area on one single pad.	Note 2	
t	0.08mm max.		

Note:

- 1 . The imprinting mark below the part, are norm in manufacturing process and does not affect the function and it is acceptable.
- 2 . Residual powder on the product body is inevitable and accepted.



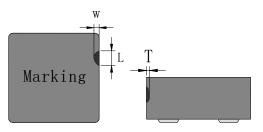
# 2 . Chip

Chip off is generated during molding and manufacturing process.

Chip off acceptance limits subjected to the product size.

Our current Defect limit is based on the IPC-A-610-2017standard of Method 9.4.

Some chip off does not impact the product function, see the IPC standard 1 & 2.



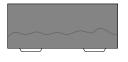
Т	25 % Max of the thickness
W	25 % Max of the width
L	50 % Max of the length

Defects usually occur at the corners and edges of the product,There will be a slight defect black and rough, but not exposed copper, and does not affect the product performance and reliability. Each considered separately

# 3 · Crack

Production process of cracks appearing in the body is inevitable, some slight crack is caused because the molding, is not oxidized, crack on the product will not affect product performance. We have done a reliability test of crack products, even if cracks is more than 0.2mm width also will not

affect the electrical properties of the product, crack limits as follows(Length $\leq$ 1/3 body, width  $\leq$ 0.2mm---OK):









Severely crack: not acceptable. More obvious cracks extended from side to side.

Moderate crack: not acceptable. So Very obvious and may result in powder come off and exposed of copper wire.

Slight wire expose: acceptable r wire exposed during Press process.

Lead wire imprint: acceptable Turns of wires that close to the edge and imprint show.



### Slight crack: acceptable.

Products from a slight crack in the baking process due to thermal expansion, and it is not obvious by visual inspection (Must not exceed the blue square area).

### 4 • oxidation(rust)

the contains iron composite, although the resin has a protective effect of oxidation, but there will be small amount of product that may occur oxidation, The oxidation area of each surface is allowed to be about 25% (in the case of non-reliability test), it is recommend that customer use this product in humidity controlled environment. The basic steps should be to protect the surface oxidation, including the sealed packages to PCB mount inductors. To avoid the adverse effects caused by oxidation, Oxidation occurs at the surface only allows the internal oxidation is not allowed, oxidized surface will not affect the reliability of the product.





4sides slightly oxidized side: Acceptable

Top and bottom slightly oxidized side: Acceptable

Oxidation (rust) area refer to Standard Chip 2

Ps: Visual Condition: visual inspection.

Spray printing effect : can be accepted if recognizable

单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)