



Specification for Approval

Date: 2021/03/04

	Custor	mer :	
-	TAI-TECH P/N:	TBMA1004P4-220MN-HD	
-	CUSTOMER P/N:		
-	DESCRIPTION:		
•	QUANTITY:	pcs	
REM	ARK:		
	Cu	ıstomer Approval Feedback	

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SMD Power Inductor

TBMA1004P4-220MN-HD

		ECN HISTORY	LIST		
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	21/03/04	New Issue	Sky Luo	Mr.Liang	Xu jing
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備					
注					

1. Features

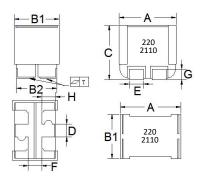
- 1. Shielded construction.
- 2. Capable of corresponding high frequency.
- 3. Low loss realized with low DCR.
- 4. High performance (Isat) realized by metal dust core.
- 5. Ultra low buzz noise, due to composite construction.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125 $^{\circ}\text{C}\textsc{(Including self temperature rise)}$



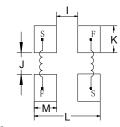
2. Applications

- 1. DC/DC converters in distributed power systems.
- 2. DC/DC converter for Field Programmable Gate Array(FPGA).
- 3. Battery powered devices.
- 4. Thin type on-board power supply module for exchanger.
- 5. VRM for server.
- 6. High current, low profile POL converters.
- 7. PDA/notebook/desktop/server and battery powered devices.

3. Dimensions



Recommend PC Board Pattern



Unit:mm

ı	J	K	L	М
2.9	1.5	3.3	9.7	3.4

Note: 1.PCB layout is referred to standard IPC-7351B

- 2. The above PCB layout reference only.
- 3. Recommend solder paste thickness at
- 0.15mm and above.

Series	Α	B1	B2	C	D	E	F	G	Н	Т
TBMA1004	12.0±0.20	9.60±0.20	8.7±0.25	11.3±0.30	1.95±0.15	2.80±0.10	3.4 MIN	2.3±0.3	2.5±0.3	≤0.15

Unit:mm

4. Part Numbering

TBMA	1004	P4	-	220	MN -	HD
Α	В	С		D	Е	F

A: Series

B: Dimension BxC C: Type Standard. D: Inductance 220=22.0uH E: Inductance Tolerance M=±20%

F: Code Marking: Black.220 and 2110(21 YY, 10 WW,follow production date).

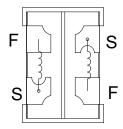
5. Specification

Part Number	Inductance L0 A(uH) ±20%	Heat Rating Current DC I rms (A) Typ	Saturation Current DC I sat (A) Typ	DCR (mΩ)Typ	DCR (mΩ)Max
TBMA1004P4V-220MN-HD	22.0	4.0	5.0	62.0	74.4

Note:

- 1. Test frequency: Ls: 100KHz /1.0V.
- All test data referenced to 25^oC ambient.
- $3. \ \ \mathsf{Testing\ Instrument} (\mathsf{or\ equ}) : \mathsf{L} : \mathsf{HP4284A, CH11025, CH3302, CH1320, CH1320S \ LCR \ METER \ / \ Rdc: CH16502, Agilent 33420A \ MICRO \ OHMMETER.$
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately $\,^\Delta T$ of 40 $^\circ\! C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately30%
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Special inquiries besides the above common used types can be met on your requirement.
- 8. Rated DC Current : The less value whith is Irms or Isat.

6. Schematic Diagram

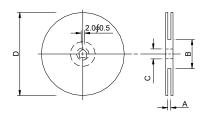


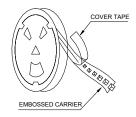


NO	Items	Materials
1	Base	Plastic
2	Core	Alloy Powder
3	Wire	Polyester Wire or equivalent.
4	Clip	100% Pb free solder(Ni+SnPlating)
5	Glue	Ероху
6	Ink	Halogen-free ketone

8. Packaging Information

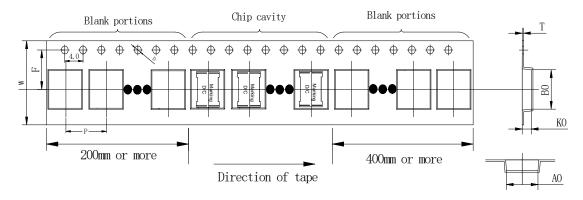
(1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4+2/-0	100±2	13+0.5/-0.2	330

(2) Tape Dimension

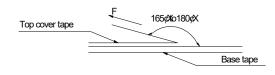


Series	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)	D(mm)
ТВМА	1004	12.5±0.1	10.0±0.1	11.55±0.1	16.0±0.1	24±0.3	11.5±0.1	0.50±0.05	1.5±0.1

(3) Packaging Quantity

ТВМА	1004
Chip / Reel	300

(4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Ī	Room Temp.	Room Humidity	Room atm	Tearing Speed		
	(℃)	(%)	(hPa)	mm/min		
	5~35	45~85	860~1060	300		

9. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125℃ (Including self - temperature rise)	N/A
Storage temperature	110~+40°C,50~60%RH (Product with taping) 240~+125°C (on board)	N/A
Electrical Performance Te	est	·
Inductance		Agilent4284A,E4991A,KEYSIGHTE4980A/AL,chroma3302,3205
DCR	Refer to standard electrical characteristics list.	Agilent 4339B,chrom16502
Saturation Current (Isat)	Approximately △30%	Saturation DC Current (Isat) will cause L0 to drop \triangle L(%)
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise △ T(°C). 1.Applied the allowed DC current 2.Temperature measured by digital surface thermometer
Reliability Test		• • • • • • • • • • • • • • • • • • • •
Life Test		Preconditioning: Run through IR reflow for 3 times. times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Temperature: 125±2°C(Inductor, ambient + temp rise) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 3 times times.(IPC/JEDECJ-STD-020E Classification Reflow Profiles) Humidity: 85±2% R.H, Temperature: 85°±2°C Duration: 1000hrs Min.(No load current) Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times. times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) 1. Baked at50 ℃ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs. 3. Raise temperature to 65±2℃ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to 25℃ in 2.5hrs, keep at 25℃ for 2 hrs then keep at -10℃ for 3 hrs 4. Keep at 25℃ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Condition for 1 cycle Step1: $-40\pm2^\circ\mathbb{C}$ 30 ± 5 min Step2: $125\pm2^\circ\mathbb{C} = 20$ S Step3: $125\pm2^\circ\mathbb{C} = 30\pm5$ min Step4: $-40\pm2^\circ\mathbb{C} = 20$ S Number of cycles: 500 Measured at room fempraturc after placing for 24 ± 2 hrs.
Vibration		Preconditioning: Run through IR reflow for 3 times. times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations).

TAI-TECH

Item	Performance		Test Condition				
Bending	Appearance: No damage. Inductance: within±10% of initial value	followin <0805 i Bendin <0805 i	Shall be mounted on a FR4 substra following dimensions: >=0805 inch(;<0805 inch()2012mm);40x100x0.8m Bending depth: >=0805 inch(2012m;0805 inch()2012mm):0.8mm duration of 10 sec.		2012mm):4 nm		
Shock	RDC: within ±15% of initial value and shall not exceed the specification value	Туре	Peak value (g's)	Norm duration (ms)	n (D)	Wave form	Velocity change (Vi)ft/sec
		SMD	50	11		Half-sine	11.3
		Lead	50	11		Half-sine	11.3
Solderability	More than 95% of the terminal electrode should be covered with solder _o	Method Temper Dip time	Solder: Sn96.5% Ag3% Cu0.5% Method B, 4 hrs @ 155°C dry heat Temperature: 245±5°C 。 Dip time: 5+0/-0.5s。				
Resistance to Soldering Heat		Tempe 2	Depth: completely cover the termination Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate heat cycles (solder temp) 10 ±1 25mm/s ±6 mm/s 1				heat cycles
Terminal Strength	Appearance: No damage. Inductance: within±10% of initial value RDC: within ±15% of initial value and shall not exceed the specification value	(IPC/JE With th tested, mm):0.9 be app	EDEC J-ST e compone applyaforce 5kg)to the lied for 60 ly as not to	ioning: Run through IR reflow for 3 times. IEC J-STD-020E Classification Reflow Profiles component mounted on a PCB with the device to be plyaforce(>0805inch(2012mm):1kg,<=0805inch(2012 glothe) side of a device being tested. This force shall d for 60 +1 seconds. Also the force shall be applied as not to apply a shock to the component being tested.			

Note: When there are questions concerning measurement result: measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

10. Soldering Specifications

(1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

(2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

(3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Fig. 2)

- $\cdot \ 355 ^{\circ}\mathbb{C} \ \ \text{tip temperature (max)} \qquad \qquad \cdot \ \ 1.0 \text{mm tip diameter (max)}$
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- · Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

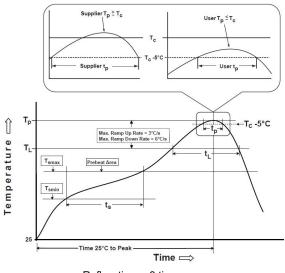
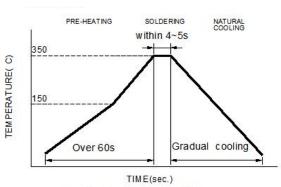


Fig.2 Iron soldering temperature profiles



Iron Soldering times: 1 times max.

Soldering iron Method : 350± 5℃ max

Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(t _s)from(T _{smin} to T _{smax})	150℃ 200℃ 60-120seconds
Ramp-up rate(T_L to T_p)	3℃/second max.
$\label{eq:Liquidus} \begin{array}{c} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \text{maintained above } T_L \end{array}$	217 [°] C 60-150 seconds
Classification temperature(T _c)	See Table (1.2)
Time(t_p) at Tc- $5^{\circ}\mathbb{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate(T _p to T _L)	6℃ /second max.
Time 25°C to peak temperature	8 minutes max.

 $[\]textbf{\textit{Tp}}: \ \ \text{maximum peak package body temperature, } \ \ \textbf{\textit{Tc}}: \ \ \text{the classification temperature.}$

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260℃
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	245℃	245℃

For user (customer) **Tp** should be equal to or less than **Tc.**

^{*} Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

Reflow is referred to standard IPC/JEDEC J-STD-020E o

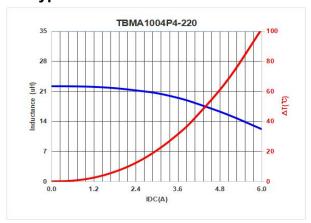
11.Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48 \pm 2 hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body $_{\circ}$

Application Notice

- · Storage Conditions
- To maintain the solderability of terminal electrodes:
- 1. TAI-TECHproducts meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

12.Typical Performance Curves



单击下面可查看定价,库存,交付和生命周期等信息

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