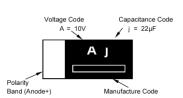
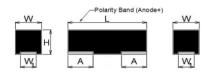
# **TCS Series** Chip Tantalum Capacitors (Extra Large Capacitance)



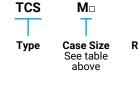


# MARKING





# **HOW TO ORDER**



#### | **Rated DC Voltage** 0G = 4Vdc 0J = 6.3Vdc 1A = 10Vdc 1C = 16Vdc 1D = 20Vdc

1**A** 

### FEATURES

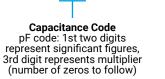
- Ta-MnO₂ technology
- Low DCL
- High CV
- Parameters stability over voltage and time
  Undertab LF

### **APPLICATIONS**

- For high component density PCB design
- DC/DC
- Industrial
- Telecom
- IoT
- Home applications
- Sensors

### **CASE DIMENSIONS:**

Code	EIA Code	EIA Metric	L+0.20-0.00 (0.008)	W±0.10 (0.004)	H+0.20-0.00 (0.008)	W₁±0.10 (0.004)	A±0.10 (0.004)
M	0603	1608-10	1.60 (0.063)	0.85 (0.033)	0.80 (0.031)	0.55 (0.022)	0.50 (0.020)
Р	0805	2012-12	2.00±0.20 (0.079±0.008	1.25±0.20 (0.049±0.008)	1.20 (0.047) max.	0.85±0.20 (0.033±0.008)	0.50 (0.020)
PS	0805	2012-09	2.00±0.20 (0.079±0.008)	1.25±0.20 (0.049±0.008)	0.90 (0.035) max.	0.85±0.20 (0.033±0.008)	0.50 (0.020)



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8R



#### **TECHNICAL SPECIFICATIONS**

Technical Data:	All technical data relate to an ambient temperature of +25°C	
Capacitance Range:	10µF to 220µF	
Capacitance Tolerance:	±20%	
Leakage Current DCL:	Please see the ratings and part number reference table below	
Temperature Range:	-55°C to +125°C	

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LEAD-FREE LEAD-FREE COMPATIBLE

COMPONENT

#### millimeters (inches)



### CAPACITANCE AND RATED VOLTAGE RANGE (LETTER DENOTES CASE SIZE)

Capac	itance	Rated Voltage DC (V <sub>R</sub> ) @ 85°C							
μF	Code	4V(g)	6.3V(j)	10V (A)	16V (C)	20V(D)	Code		
10	106				М	Р	а		
22	226			M			j		
47	476		М	P, PS			s		
100	107	М	Р				ā		
150	157		Р				ē		
220	227	Р					j		

Released ratings

Note: Voltage ratings are minimum values. KYOCERA AVX reserves the right to supply higher ratings in the same case size, to the same reliability standards.

#### **RATINGS & PART NUMBER REFERENCE**

Part No.	Part No. Case Size Capacitance (µF)		Size Capacitance Voltage Operating Ma		DCL Max. (µA)	DF Max. (%)	ESR @100kHz (Ω)	MSL		
	4 Volt									
TCSM0G107M8R-AV1	М	100	4	125	80.0	40	2	2		
TCSM0G107M8R-V1	М	100	4	125	80.0	40	4	2		
TCSP0G227M8R-V1	Р	220	4	125	88.0	40	3	2		
	6.3 Volt									
TCSM0J476M8R-V1	М	47	6.3	125	29.7	40	4	2		
TCSP0J107M8R-V1	Р	100	6.3	125	63.0	40	3	2		
TCSP0J157M8R-V1	Р	150	6.3	125	95.0	40	3	2		
			10	Volt						
TCSM1A226M8R	М	22	10	125	11.0	30	5	2		
TCSM1A226M8R-02	М	22	10	125	11.0	30	5	2		
TCSP1A476M8R	Р	47	10	125	24.0	30	4	2		
TCSPS1A476M8R-V1	PS	47	10	125	23.5	30	*	2		
	16 Volt									
TCSM1C106M8R	М	10	16	125	8.0	20	6	2		
	20 Volt									
TCSP1D106M8R	Р	10	20	125	10.0	20	6	2		

\*Impedance 4Ω

Moisture Sensitivity Level (MSL) is defined according to J-STD-020.

All technical data relates to an ambient temperature of +25C.

Capacitance and DF are measured at 120Hz, 0.5RMS with DC bias of 1.5 volts.

DCL is measured at rated voltage after 5 minutes.

ESR allowed to move up to 1.25 times catalog limit post mounting.

NOTE: KYOCERA AVX reserves the rights to supply higher voltage rating in the same case size, to the same reliability standards.

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# **TCS Series** Chip Tantalum Capacitors (Extra Large Capacitance)



# **QUALIFICATION TABLE**

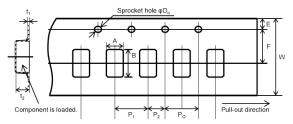
TEST	TCS series (Temperature range -55°C to +125°C)									
1551		Condition			Character	istics				
	Apply rated volta	ge (Ur) at 85°C for	1000brs through	Visual examination	tion no visible damage					
		e of ≤3.0Ω. Stabiliz		DCL	2x initial limit	2x initial limit				
Endurance	temperature for 2	24 hours before me	asuring.	ΔC/C	within ±20% of initial value (M, PS case), ±30% (P case)					
				DF	2x initial limit	2x initial limit				
				Visual examination	no visible damage	no visible damage				
		90-95% relative hur	,	DCL	2x initial limit	2x initial limit				
Humidity		ilize at room tempe ours before measu		ΔC/C	within ±30% of initial value (M, P case), ±20% (PS)					
		iours before measu	ning.	DF	2x initial limit					
	Step	Temperature°C	Duration(min)		-55°C	+85°C	+125°C			
	1	-55	15	DCL	n/a	10xIL*	12.5xIL*			
Temperature	2	+85	15	_	0/-30% (M case)					
Stability	3 +125 15				+15/-50% (P case)	+15/-5% (M, P case)				
					0/-15% (PS case)	+15/0% (PS case)	+20/0% (PS case)			
				DF	IL*	IL*	IL*			
	Apply 1 3y rated	voltage (Ur) at 85±:	2°C for	Visual examination	no visible damage					
Surge Voltage	1000 cycles, 300	sec charge and 30		DCL	2x initial limit					
ourge voltage	resistance 10000	ם.		ΔC/C	±30% of initial limit (M	±30% of initial limit (M, P case), ±20% (PS)				
				DF	2x initial limit	2x initial limit				
	4.17 JIS C 5101-	1		Visual examination	no visible damage	no visible damage				
Vibration	Frequency: 10 to	55 to 10Hz/min.		DCL	initial limit					
VIDIATION	Amplitude: 1.5m	m		ΔC/C	within ± 5% of initial va	llue				
	Time: 2hours ead	ch in X and Y direct	ons	DF	initial limit					

\*Initial Limit

For use outside of recommended conditions and special request, please contact KYOCERA AVX.

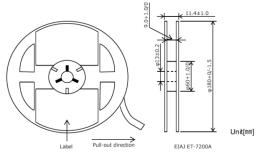
Initial measurement max. 1hr after the removal from dry pack or after pretreatment at 85°C for 24 hours.

#### **PACKAGING SPECIFICATIONS**



Case	A±0.10	B±0.10	W±0.20	E±0.10	F±0.05	P1±0.10	P2±0.05	PO±0.10	DO+0.10/0	t1±0.05	t2±0.10	Standard packaging quantity
М	1.15	2.00	8.00	1.75	3.50	4.00	2.00	4.00	φ1.50	0.20	1.00	3,000 pcs
Р	1.55	2.30	8.00	1.75	3.50	4.00	2.00	4.00	φ1.55±0.05	0.25	1.32	3,000 pcs
PS	1.60	2.40	8.00	1.75	3.50	4.00	2.00	4.00	φ1.50	0.25	1.05±0.05	3,000 pcs

#### **REEL DIMENSIONS**



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单击下面可查看定价,库存,交付和生命周期等信息

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