



# Specification for Approval

Date: 2024/06/03

Customer: 信和達

TAI-TECH P/N:

TMIM322520AV-Series(G)-HD

	CUSTOMER P/N:			
	DESCRIPTION:			
	QUANTITY:	рс	s	
REM	IARK:			
	Cı	ıstomer Approval Feedb	ack	
			1	

□ 西北臺慶科技股份有限公司

TAI-TECH Advanced Electronics Co., Ltd <u>Headquarter:</u>
NO.1 YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN HSIEN, TAIWAN, R.O.C.
TEL: +886-3-4641148 FAX: +886-3-4643565

http://www.tai-tech.com.tw E-mail: sales@tai-tech.com.tw

□ 臺慶精密電子(昆山)有限公司

TAI-TECH ADVANCED ELECTRONICS(KUNSHAN) CO., LTD SHINWHA ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA

TEL: +86-512-57619396 FAX: +86-512-57619688

E-mail: sales@tai-tech.cn

■ 慶邦電子元器件(泗洪)有限公司

TAIPAQ ELECTRONICS (SIHONG) CO., LTD
THESOUTH HANGZHOU ROAD AND THE EAST JIANSHEBEI ROAD,
ECONOMIC DEVELOPMENT ZONE, SIHONG COUNTY, JIANGSU
PROVINCE, P. R, CHINA.

TEL: +86-527-88601191 FAX: +86-527-88601190

E-mail: sales@taipaq.cn

#### Sales Dep.

APPROVED	CHECKED
Eric Kuan	Zhang mengmeng

#### **R&D** Center

APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Tang pingping

# **SMD Power Inductor**

TMIM322520AV-Series(G)-HD

		ECN HISTORY	LIST		
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	24/06/03	New Issue	Sky Luo	Mr.Liang	Tang pingping
備					
注					

TAI-TECH

# **SMD Power Inductor**

#### 1. Features

- 1. Low loss realized with low DCR.
- 2. High performance realized by metal dust core.
- 3. Ultra low buzz noise, due to composite construction.
- 4. 100% Lead(Pb)-Free and RoHS compliant.
- 5. High reliability -Reliability test complied to AEC-Q200.



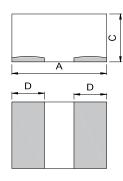




# 2. Applications

Automotive applications.

### 3.Dimensions







Α	В	С	D
3.2±0.3	2.5±0.3	1.8±0.2	1.1±0.3

Unit: mm

# 4. Part Numbering









	G			
[G]				H
G				
		H	H	
		H	H	

**Recommend PC Board Pattern** 

L	G	Н
3.7	0.7	2.8

- e.

  1. PCB layout is referred to standard IPC-7351B

  2. The above PCB layout reference only.

  3. Recommend solder paste thickness at

  0.10mm and above.

- A: Series
- B: Dimension
- AxBxC
- C: Material D: Inductance
- E: Inductance Tolerance
- F: Code

- 4R7=4.7uH
- M=±20%, G: Coating

# 5. Specification

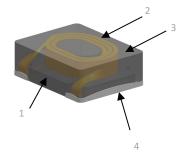
Part Number	Inductance (uH) ±20%	Irn	ns( A )	I sat	(A)	DCR	(mΩ)
	@ 0 A DC	Тур	Max	Тур	Max	Тур	Max
TMIM322520AV-R10MG-HD	0.10	11.5	10.5	22.0	20.0	3.3	4.0
TMIM322520AV-R22MG-HD	0.22	10.0	9.0	15.0	13.0	5.3	6.4
TMIM322520AV-R33MG-HD	0.33	8.5	8.0	11.0	10.0	8.0	9.6
TMIM322520AV-R47MG-HD	0.47	8.0	7.5	9.0	8.0	9.3	11.2
TMIM322520AV-R56MG-HD	0.56	7.5	7.0	8.5	7.5	11.4	13.7
TMIM322520AV-R68MG-HD	0.68	7.0	6.4	8.0	7.0	13.3	16.0
TMIM322520AV-1R0MG-HD	1.00	6.2	5.8	7.5	6.2	18.3	22.0
TMIM322520AV-1R5MG-HD	1.50	5.3	4.8	6.0	5.0	25.8	31.0
TMIM322520AV-2R2MG-HD	2.20	3.7	3.2	5.0	4.5	38.0	46.0
TMIM322520AV-3R3MG-HD	3.30	3.2	2.7	4.2	3.7	56.0	65.0
TMIM322520AV-4R7MG-HD	4.70	2.8	2.4	3.4	2.9	90.0	98.0

#### Note:

- 1. Test frequency : Ls : 1MHz /1.0V.
- 2. All test data referenced to 25℃ ambient.
- $3. \ \ \text{Testing Instrument} (\text{or equ}): A \textit{gilent 4284A}, \textit{E4991A}, 4339B, \textit{KEYSIGHT E4980A/AL}, chroma 3302, 3250, 16502.$
- 4. Heat Rated current (Irms) will cause the coil temperature rise approximately  $\,\Delta\,T$  of 40  $^{\circ}\!C$
- 5. Saturation current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 150°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Irms Testing: temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.

  Therefore temperature rise should be verified in application conditions.
- 8. Rated DC current : the lower value of Irms and Isat.
- Rated voltage 35V DC, the application of voltage depends on many factors, over voltage may cause components failure. high temperature.
   and burn-out, user needs to verify for appropriate usage.

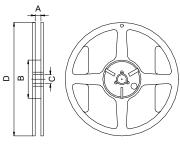
### 6. Material List



NO	Items	Materials
1	Core	Metal Powder.
2	Wire	Polyester Wire.
3	paint	Epoxy resin.
4	Terminal	Plating.

# 7. Packaging Information

### 7-1. Reel Dimension



7"x8mm

Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	8.4+1.5/-0.0	60±1.0	13+0.5/-0.2	178±2.0

7-2. Tape Dimension



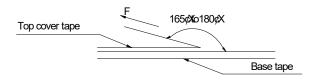
× L	Blank portions P2 200mm or more	Chip cavity	Blank portions  + + + + + + + + + + + + + + + + + + +	T KO
	ZUUIIIII OF HOFE	Direction of tape	400mm or more	
			AO	

В0	A0	K0	W	Р	P0	P2	E	F	Т	D/D1
3.6±0.1	2.9±0.1	2.2±0.1	8.0±0.1	4.0±0.1	4.0±0.1	2.0±0.1	1.75±0.1	3.5±0.1	0.22±0.05	1.5+0.1/-0.0
Unit: mm										

7-3. Packaging Quantity

TMIM	322520	
Pcs/Reel	2000	

### 7-4. Tearing Off Force



The force for tearing off cover tape is 10 to 100 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(℃)	(%)	(hPa)
300±10	5~35	45~85	860~1060

# 8. Reliability and Test Condition

Item	Item Performance Test Condition			
Operating temperature	-55~+150°C(Including self - temperature rise)	NA		
Storage temperature and Humidity range	110~+40°C, 50~60%RH (Product with taping) 255~+150°C (on board)  NA			
Electrical Performance Test				
Inductance		HP4284A, CH11025, CH3302, CH1320, CH1320S LCR Meter.		
DCR	Refer to standard electrical characteristics list.	CH16502, Agilent33420A Micro-Ohm Meter.		
Saturation Current (Isat)	Approximately △L30%	Saturation DC Current (Isat) will cause L0 to drop $\triangle$ L(%).		
Heat Rated Current (Irms)	Approximately △T40℃	Heat Rated Current (Irms) will cause the coil temperature rise $\triangle$ T( $\mathbb{C}$ ).  1. Applied the allowed DC current.  2. Temperature measured by digital surface thermometer.		
Reliability Test				
High Temperature Exposure(Storage) AEC-Q200  Temperature Cycling AEC-Q200		Preconditioning: run through IR reflow for 3 times. (IPC/JEDEC J-STD-020EClassification Reflow Profiles) Temperature: 150±2°C (Inductor, ambient + temp rise). Duration: 1000hrs Min. Measured at room temperature after placing for 24±2 hrs. Preconditioning: run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Condition for 1 cycle Step1: -55±2°C 30min Min.(Inductor) Step2: 150±2°C 30min Min. Step4: low temp. transition time 1min MAX. Step3: 150±2°C 30min Min. Step4: low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24±2 hrs.  I=24 hours/cycle. Note: Steps 7a & 7b Unpowered.    Total   Tota		
Moisture Resistance (AEC-Q200)	Appearance: no damage. Inductance: within $\pm$ 10% of initial value. Q: shall not exceed the specification value. RDC: within $\pm$ 15% of initial value and shall not exceed the specification value.			
Biased Humidity (AEC-Q200)		Preconditioning: run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity: 85±3 % R.H. Temperature: 85°C±2°C Duration: 1000hrs Min. Measured at room temperature after placing for24±2hrs.		
High Temperature Operational Life (AEC-Q200)		Preconditioning : run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Temperature : 150±2°C(Inductor) Duration : 1000hrs Min. With 100% rated current. Measured at room temperature after placing for24±2hrs.		
External Visual	Appearance : no damage.	Inspect device construction, marking and workmanship. Electrical Test not required.		
Physical Dimension	According to the product specification size measurement.	According to the product specification size measurement.		
Resistance to Solvents	Appearance : no damage.	Add aqueous wash chemical - OKEM clean or equivalent.		
Mechanical Shock	Appearance : no damage. Inductance : within±10% of initial value. Q : shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the specification value.	Preconditioning : run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition  Type Peak value (g's) duration (D) (ms) form change (Vi)ft/sec  SMD 100 6 Half-sine 12.3  Lead 100 6 Half-sine 12.3  3 shocks in each direction along 3 perpendicular axes(18 shocks).		

Item	Performance	Test Condition			
Vibration		Preconditioning: run through IR reflow for 3 times. ( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment: vibration checker Total Amplitude: 5g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations). Test condition: (MIL-STD-202 Condition B)			
		Temperature Time ramp/immersion and emersion rate Number of heat cycles			
Appearance : no damage. Impedance : within $\pm$ 15% of initial value Inductance :within $\pm$ 10% of initial value Q : shall not exceed the specification value. RDC : within $\pm$ 15% of initial value and shall not exceed the specification value.	Appearance : no damage. Impedance : within±15% of initial value Inductance :within±10% of initial value Q : shall not exceed the specification value. RDC : within ±15% of initial value and shall not exceed the	260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1  Depth : completely cover the termination			
		Continental  Temperature  time 25°C to peak temperature  component  T <sub>Stree</sub>			
	Component Size Ramp up to Tomo ts ts Tomo Tt, ts Tomo 15°C to				
Thermal shock (AEC-Q200)		Condition   Cond			
ESD	Appearance : no damage.	Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESI Discharge Waveform to a Coaxial Target Test method: AEC-Q200-002 Test mode: Contact Discharge Discharge level: 4 KV (Level: 2)			
Solderability	More than 95% of the terminal electrode should be covered with solder.	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time: 5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C			
Electrical Characterization	Refer Specification for Approval.	Test time: 30 +0/-0.5 seconds.  Summary to show Min, Max, Mean and Standard deviation.			
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.			
Board Flex	Appearance : no damage.	Preconditioning: run through IR reflow for 3 times.  ( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board.  Support  Solder Chip Printed circuit board before testing  Probe to exert bending force  Radius 340			

		Preconditioning: run through IR reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.	
Terminal Strength(SMD)	Appearance : no damage.	substrate press tool shear force	

Note: When there are questions concerning measurement result: measurement shall be made after  $48 \pm 2$  hours of recovery under the standard condition.

### 9. Soldering Specifications

#### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) Soldering Reflow:

Fig.1 Soldering Reflow

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

#### (3) Iron Reflow:

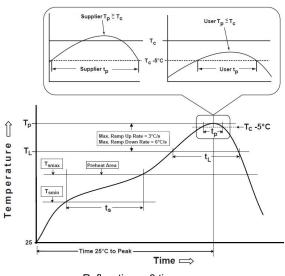
Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)

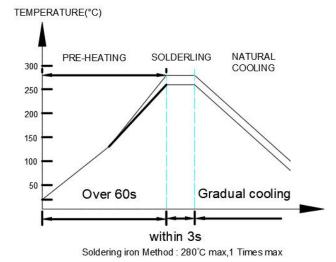
- · Preheat circuit and products to 150℃
- · Never contact the ceramic with the iron tip
- $\cdot\,$  Use a 20 watt soldering iron with tip diameter of 1.0mm

· Limit soldering time to 3sec.

- · 1.0mm tip diameter (max)
- · 280°C tip temperature (max)

Fig.2 Iron soldering temperature profiles





Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T <sub>smin</sub> )	150℃
-Temperature Max(T <sub>smax</sub> )	200℃
$-Time(t_s) from(T_{smin} \ to \ T_{smax})$	60-120seconds
Ramp-up rate(T <sub>L</sub> to T <sub>p</sub> )	3℃/second max.
Liquidus temperature(T <sub>L</sub> )	217℃
Time(t∟)maintained above T∟	60-150 seconds
Classification temperature(T <sub>c</sub> )	See Table (1.2)
Time(tp) at Tc- $5^{\circ}\mathrm{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate(Tp to TL)	6℃ /second max.
Time 25℃ to peak temperature	8 minutes max.

**Tp**: maximum peak package body temperature, **Tc**: the classification temperature.

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260℃
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	245℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020E.

For user (customer) Tp should be equal to or less than Tc.

<sup>\*</sup> Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

#### 10. Notes

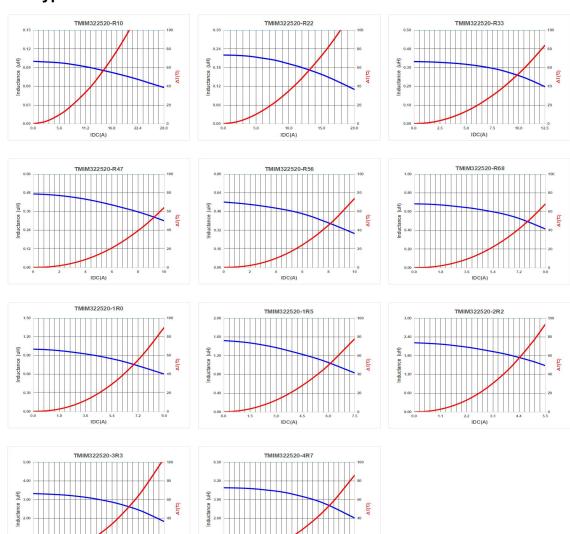
(1) When there are questions concerning measurement result : measurement shall be made after 48  $\pm$  2 hours of recovery under the standard condition.

- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, our company only guarantees that the product meets the requirements of this specification.

#### **Application Notice**

- · Storage Conditions
  - To maintain the solderability of terminal electrodes:
  - 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
  - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

# 11. Typical Performance Curves



# 单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)