

# Specification for Approval

**Date:** 2023/06/25

**Customer :** \_\_\_\_\_

**TAI-TECH P/N:** TMPC1206HPV-Series(G)-D

**CUSTOMER P/N:** \_\_\_\_\_

**DESCRIPTION:** \_\_\_\_\_

**QUANTITY:** \_\_\_\_\_ pcs

**REMARK:**

Customer Approval Feedback

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APPROVED	CHECKED
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R&D Center

APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Cui lingling

**SMD Power Inductor****TMPC1206HPV-Series(G)-D****ECN HISTORY LIST**

REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	23/02/28	New Issue	Sky Luo	Mr.Liang	Cui lingling
1.1	23/06/25	增加電流 MAX 值	Sky Luo	Mr.Liang	Cui lingling
備注					

# SMD Power Inductor

TMPC1206HPV-Series(G)-D

## 1. Features

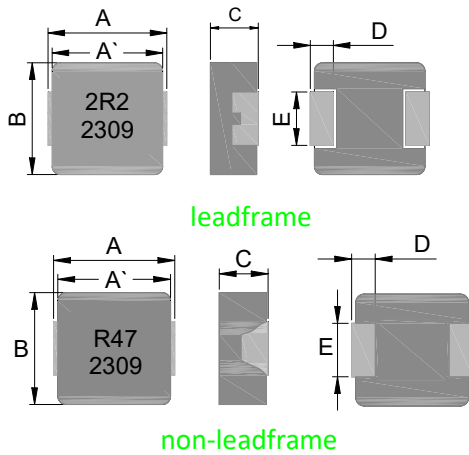
1. Low loss realized with low DCR.
2. High performance realized by metal dust core.
3. Ultra low buzz noise, due to composite construction.
4. 100% Lead(Pb)-Free and RoHS compliant.
5. High reliability -Reliability test complied to AEC-Q200.



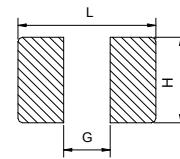
## 2. Applications

Automotive applications.

## 3. Dimensions



### Recommend PC Board Pattern

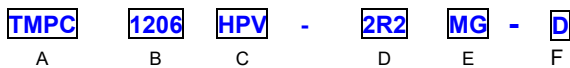


L(mm)	G(mm)	H(mm)
14.2	8.0	5.0

- Note:
1. PCB layout is referred to standard IPC-7351B
  2. The above PCB layout reference only.
  3. Recommend solder paste thickness at 0.15mm and above.

Series	A(mm)	A'(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC1206HPV	13.5±0.5	12.5±0.3	12.5±0.3	5.7±0.3	2.3±0.3	4.7±0.3

## 4. Part Numbering



- A: Series
  - B: Dimension
  - C: Type
  - D: Inductance
  - E: Inductance Tolerance
  - F: DateCode
- BxC
  - HP:H: Carbonyl Powder,P:PAD broaden.V: vehicle.
  - 2R2=2.2uH
  - M=±20%
  - Marking: Black.2R2 and 2309(23 YY,09 WW, follow production date).

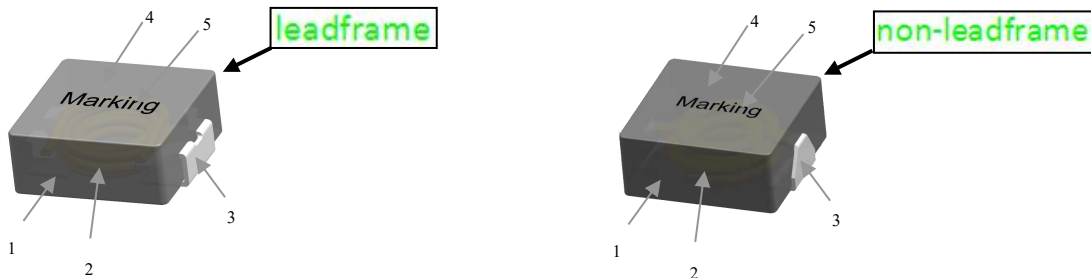
## 5. Specification

Part Number	Inductance L0 (uH) ±20%	I rms ( A )		I sat 1 ( A ) Typ	I sat 2 ( A )		DCR (mΩ)		Type
		Typ	Max		Typ	Max	Typ	Max	
TMPC1206HPV-R47MG-D	0.47	38	35	60	64	58	0.92	1.3	non-leadframe
TMPC1206HPV-R56MG-D	0.56	35	32	56	60	54	1.15	1.5	non-leadframe
TMPC1206HPV-R68MG-D	0.68	33	30	53	57	50	1.33	1.7	non-leadframe
TMPC1206HPV-1R0MG-D	1.00	29	27	45	53	47	1.8	2.4	non-leadframe
TMPC1206HPV-1R5MG-D	1.50	26	24	43	50	43	2.7	3.2	non-leadframe
TMPC1206HPV-2R2MG-D	2.20	21	19	34	43	39	4.0	4.7	leadframe
TMPC1206HPV-3R3MG-D	3.30	17	15	28	35	32	5.8	7.1	leadframe
TMPC1206HPV-4R7MG-D	4.70	16	13	25	30	26	9.5	11.5	leadframe
TMPC1206HPV-5R6MG-D	5.60	15.5	12	22	28	24	10.8	12.6	leadframe
TMPC1206HPV-6R8MG-D	6.80	15	11	19	25	21	12	13.8	leadframe
TMPC1206HPV-8R2MG-D	8.20	11	10	17	23	19	13.6	16	leadframe
TMPC1206HPV-100MG-D	10.0	11	9	15.5	21	18	18	20.7	leadframe
TMPC1206HPV-150MG-D	15.0	9.0	7	13	16	14	25	29	leadframe
TMPC1206HPV-220MG-D	22.0	8.0	6.0	11	14	11	34	39.5	leadframe
TMPC1206HPV-330MG-D	33.0	6.0	5.0	8.0	12.0	9	65	75	leadframe
TMPC1206HPV-470MG-D	47.0	5.5	4.5	7.0	11.0	8	80	90	leadframe
TMPC1206HPV-560MG-D	56.0	5.3	4.0	6.5	10	7	101	118	leadframe
TMPC1206HPV-680MG-D	68.0	5.0	3.5	6.0	9.0	6	120	140	leadframe
TMPC1206HPV-820MG-D	82.0	4.5	3.0	5.5	8.5	5.5	138	161	leadframe
TMPC1206HPV-101MG-D	100	4.0	2.5	5.0	8.0	5.0	180	200	leadframe
TMPC1206HPV-151MG-D	150	3.0	1.8	4.0	6.0	4.0	300	350	leadframe

Note:

1. Test frequency : Ls : 100KHz /1.0V.
2. All test data referenced to 25°C ambient.
3. Testing Instrument(or equ) : Agilent 4284A,E4991A,4339B,KEYSIGHT E4980A/AL,chroma3302,3250,16502.
4. Heat Rated Current (Irms) will cause the coil temperature rise approximately ΔT of 40°C
5. Saturation Current (Isat1) will cause L0 to drop approximately 20%.  
Saturation Current (Isat2) will cause L0 to drop approximately 30%.
6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
7. I rms Testing : Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.  
Therefore temperature rise should be verified in application conditions.
8. Rated DC current: The lower value of I rms and Isat

### 6. Material List

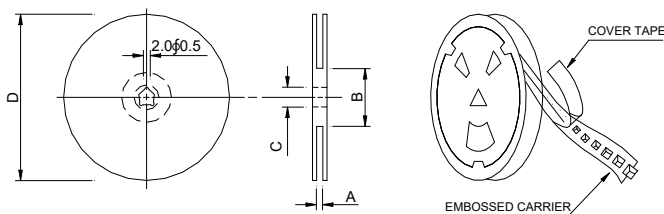


NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+Sn---Plating)
4	Ink	Halogen-free ketone
5	paint	Epoxy resin

NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Solder	100% Pb free solder
4	Ink	Halogen-free ketone
5	paint	Epoxy resin

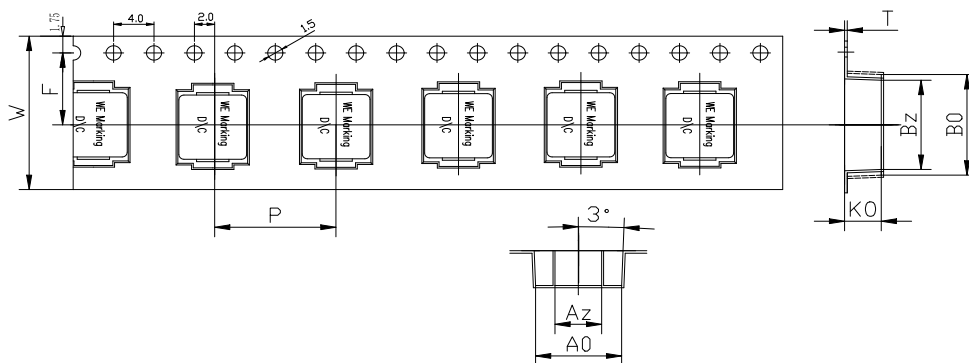
### 7. Packaging Information

#### (1) Reel Dimension



Type	A(mm)	B(mm)	C(mm)	D(mm)
13"x24mm	24.4±2/-0	100±2	13±0.5/-0.2	330

#### (2) Tape Dimension

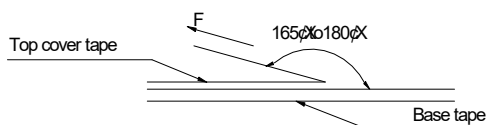


Series	Size	Bo(mm)	Bz(mm)	Ao(mm)	Az(mm)	Ko(mm)	P(mm)	W(mm)	F(mm)	t(mm)
TMPC	1206	14.1±0.1	13.0±0.1	12.9±0.1	7.0±0.1	6.5±0.1	16.0±0.1	24±0.3	11.5±0.1	0.35±0.05

#### (3) Packaging Quantity

TMPC	1206
Chip / Reel	500

#### (4) Tearing Off Force

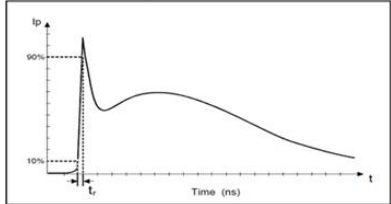
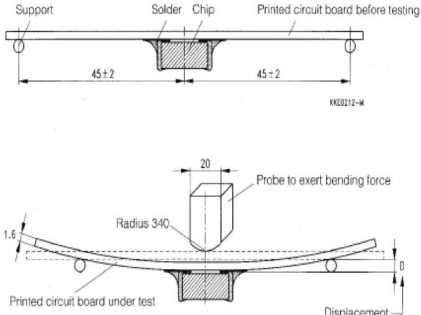
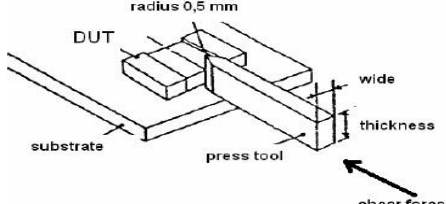


The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 standard).

Tearing Speed mm	Room Temp. (°C)	Room Humidity (%)	Room atm (hPa)
300±10%	5~35	45~85	860~1060

### 8. Reliability and Test Condition

Item	Performance	Test Condition															
Operating temperature	-55~+125°C (Including self - temperature rise)																
Storage temperature and Humidity range	1. -10~+40°C, 50~60%RH (Product with taping) 2. -55~+125°C (on board)																
<b>Electrical Performance Test</b>																	
Inductance	Refer to standard electrical characteristics list.	HP4284A, CH11025, CH3302, CH1320, CH1320S LCR Meter.															
DCR		CH16502, Agilent33420A Micro-Ohm Meter.															
Saturation Current (Isat)	Approximately $\Delta$ L20%(Isat 1) Approximately $\Delta$ L30%(Isat 2)	Saturation DC Current (Isat) will cause L0 to drop $\Delta$ L(%)															
Heat Rated Current (Irms)	Approximately $\Delta$ T40°C	Heat Rated Current (Irms) will cause the coil temperature rise $\Delta$ T(°C). 1. Applied the allowed DC current 2. Temperature measured by digital surface thermometer															
<b>Reliability Test</b>																	
High Temperature Exposure(Storage) AEC-Q200	Appearance : No damage. Inductance : within $\pm$ 10% of initial value Q : Shall not exceed the specification value. RDC : within $\pm$ 15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 125 $\pm$ 2°C (Inductor + ambient + temp rise) Duration : 1000hrs Min. Measured at room temperature after placing for 24 $\pm$ 2 hrs															
Temperature Cycling AEC-Q200		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles Condition for 1 cycle Step1: -55 $\pm$ 2°C 30min Min.(Inductor) Step2: 125 $\pm$ 2°C transition time 1min MAX. Step3: 125 $\pm$ 2°C 30min Min. Step4: Low temp. transition time 1min MAX. Number of cycles: 1000 Measured at room temperature after placing for 24 $\pm$ 2 hrs t=24 hours/cycle. Note: Steps 7a & 7b Unpowered.															
Moisture Resistance (AEC-Q200)																	
Biased Humidity (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Humidity : 85 $\pm$ 3% R.H, Temperature : 85°C $\pm$ 2°C Duration : 1000hrs Min Measured at room temperature after placing for 24 $\pm$ 2hrs															
High Temperature Operational Life (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times. ( IPC/JEDEC J-STD-020E Classification Reflow Profiles Temperature: 125 $\pm$ 2°C (Inductor) Duration : 1000hrs Min. With 100% rated current. Measured at room temperature after placing for 24 $\pm$ 2hrs															
External Visual	Appearance : No damage.	Inspect device construction, marking and workmanship. Electrical Test not required.															
Physical Dimension	According to the product specification size measurement	According to the product specification size measurement															
Resistance to Solvents	Appearance : No damage.	Add aqueous wash chemical - OKEM clean or equivalent.															
Mechanical Shock	Appearance : No damage. Inductance : within $\pm$ 10% of initial value Q : Shall not exceed the specification value. RDC : within $\pm$ 15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Test condition <table border="1" style="margin-top: 10px;"> <thead> <tr> <th>Type</th> <th>Peak value (g's)</th> <th>Normal duration (D) (ms)</th> <th>Wave form</th> <th>Velocity change (Vi)ft/sec</th> </tr> </thead> <tbody> <tr> <td>SMD</td> <td>100</td> <td>6</td> <td>Half-sine</td> <td>12.3</td> </tr> <tr> <td>Lead</td> <td>100</td> <td>6</td> <td>Half-sine</td> <td>12.3</td> </tr> </tbody> </table> 3 shocks in each direction along 3 perpendicular axes(18 shocks).	Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	SMD	100	6	Half-sine	12.3	Lead	100	6	Half-sine	12.3
Type	Peak value (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec													
SMD	100	6	Half-sine	12.3													
Lead	100	6	Half-sine	12.3													

Item	Performance	Test Condition						
Vibration		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minute Equipment : Vibration checker Total Amplitude: 5g Testing Time : 12 hours(20 minutes, 12 cycles each of 3 orientations) -						
Resistance to Soldering Heat	Appearance : No damage. Inductance : within±10% of initial value Q : Shall not exceed the specification value. RDC : within ± 15% of initial value and shall not exceed the specification value	Test condition:(MIL-STD-202 Condition B) Number of heat cycles:1 <table border="1" data-bbox="954 416 1321 510"> <thead> <tr> <th>Temperature(°C)</th> <th>Time(s)</th> <th>Temperature ramp/immersion and emersion rate</th> </tr> </thead> <tbody> <tr> <td>260±5 (solder temp)</td> <td>10±1</td> <td>25mm/s±6mm/s</td> </tr> </tbody> </table> Depth: completely cover the termination	Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate	260±5 (solder temp)	10±1	25mm/s±6mm/s
Temperature(°C)	Time(s)	Temperature ramp/immersion and emersion rate						
260±5 (solder temp)	10±1	25mm/s±6mm/s						
Thermal shock (AEC-Q200)		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Condition for 1 cycle Step1: -55±2°C 15±1min(Inductor) Step2: 125±2°C within 20Sec. Step3: 125±2°C 15±1min Number of cycles : 300 Measured at room tempraturc after placing fo24±2hrs						
ESD	Appearance : No damage.	 <p>Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target                      Test method: AEC-Q200-002                      Test mode: Contact Discharge                      Discharge level: 4 KV (Level: 2 )</p>						
Solderability	More than 95% of the terminal electrode should be covered with solder -	a. Method B1, 4 hrs @155°C dry heat @255°C±5°C Test time:5 +0/-0.5 seconds. b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.						
Electrical Characterization	Refer Specification for Approval	Summary to show Min, Max, Mean and Standard deviation .						
Flammability	Electrical Test not required.	V-0 or V-1 are acceptable.						
Board Flex	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be applied only once to the board. 						
Terminal Strength(SMD)	Appearance : No damage	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) With the component mounted on a PCB with the device to be tested, apply a 17.7 N (1.8 Kg) force to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested. 						

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

## 9. Soldering Specifications

### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

### (2) Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

### (3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)

- Preheat circuit and products to 150°C
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm
- 355°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

Fig.1 Soldering Reflow

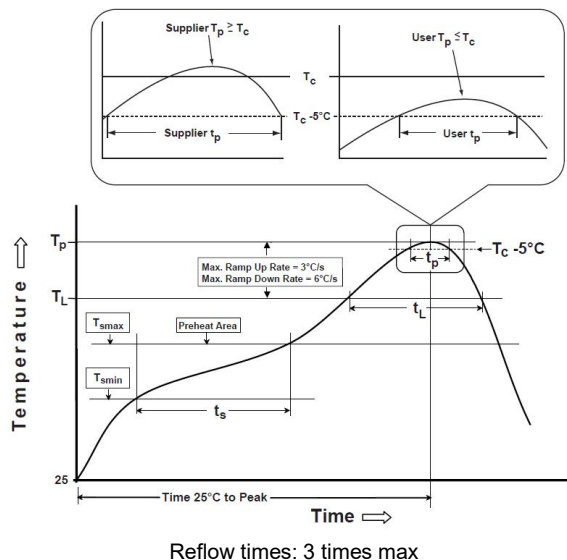


Fig.2 Iron soldering temperature profiles

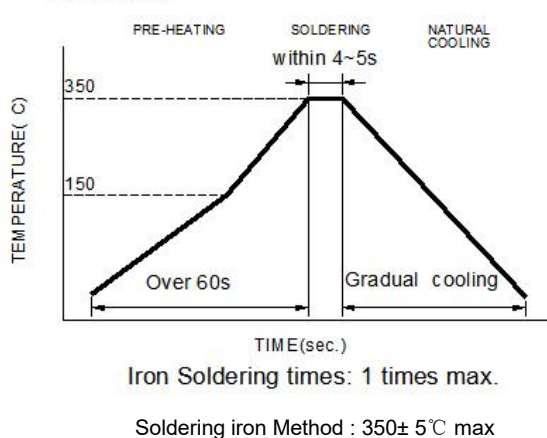


Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min( $T_{smin}$ )	150°C
-Temperature Max( $T_{smax}$ )	200°C
-Time( $t_s$ )from( $T_{smin}$ to $T_{smax}$ )	60-120seconds
Ramp-up rate( $T_L$ to $T_p$ )	3°C/second max.
Liquidus temperature( $T_L$ )	217°C
Time( $t_L$ )maintained above $T_L$	60-150 seconds
Classification temperature( $T_c$ )	See Table (1.2)
Time( $t_p$ ) at $T_c - 5^\circ\text{C}$ ( $T_p$ should be equal to or less than $T_c$ .)	* < 30 seconds
Ramp-down rate( $T_p$ to $T_L$ )	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

$T_p$ : maximum peak package body temperature,  $T_c$ : the classification temperature.

For user (customer)  $T_p$  should be equal to or less than  $T_c$ .

\* Tolerance for peak profile temperature ( $T_p$ ) is defined as a supplier minimum and a user maximum.

Table (1.2) Package Thickness/Volume and Classification Temperature ( $T_c$ )

	Package Thickness	Volume $\text{mm}^3$ <350	Volume $\text{mm}^3$ 350-2000	Volume $\text{mm}^3$ >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	$\geq 2.5\text{mm}$	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E ◦



## 10. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after  $48 \pm 2$  hours of recovery under the standard condition
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method 1 and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappears.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design. Our company only guarantees that the product meets the requirements of this specification.

### **Application Notice**

- Storage Conditions

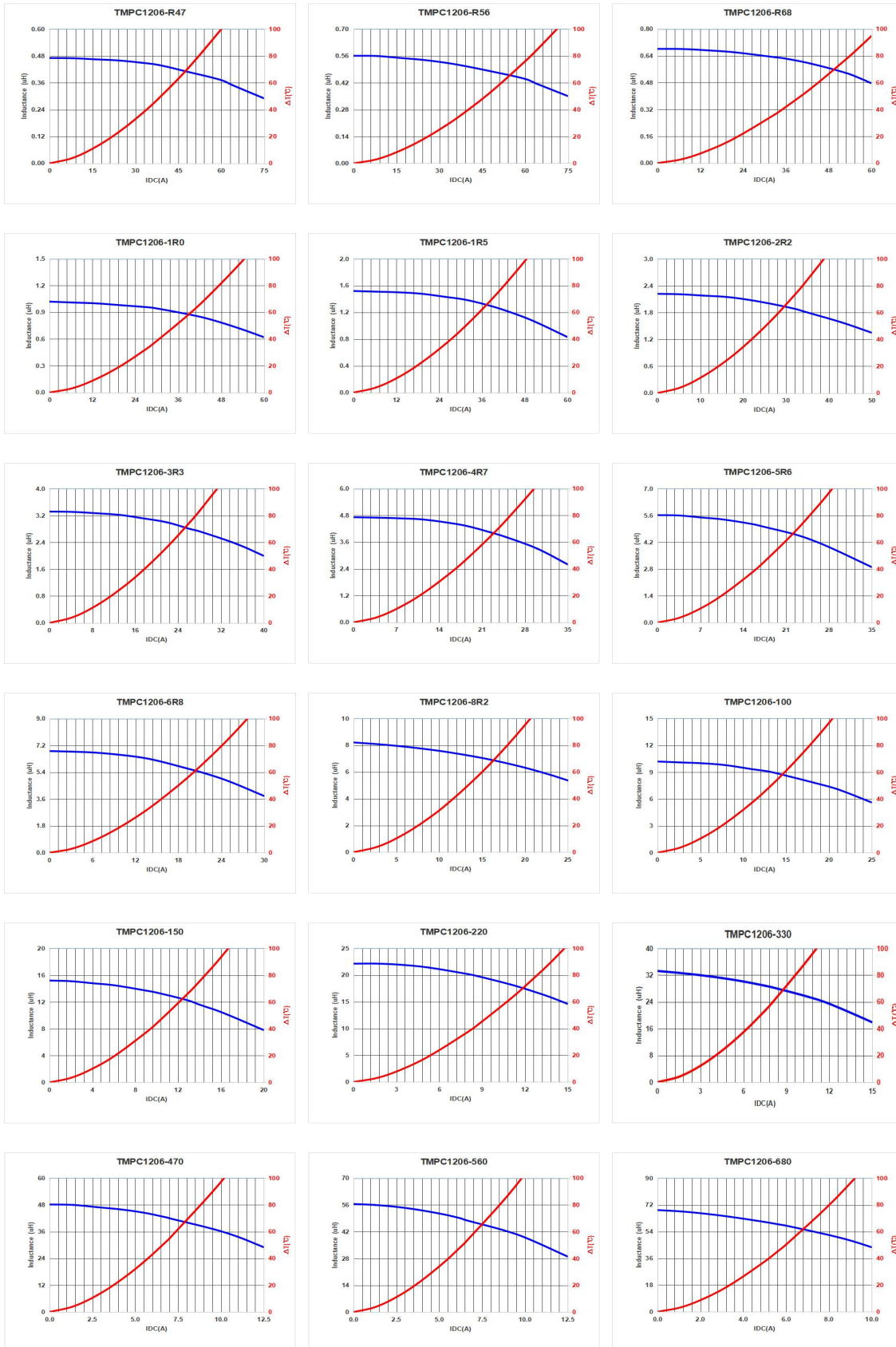
To maintain the solderability of terminal electrodes:

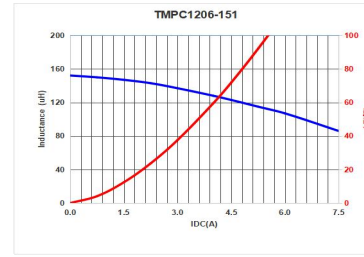
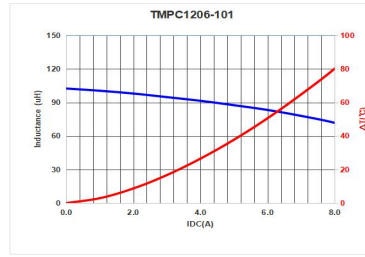
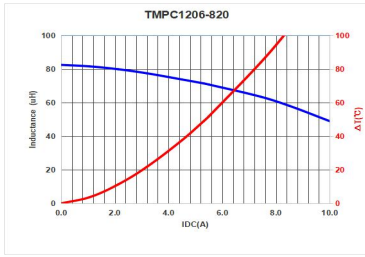
1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
2. Temperature and humidity conditions: Less than 40°C and 60% RH.
3. Recommended products should be used within 12 months from the time of delivery.
4. The packaging material should be kept where no chlorine or sulfur exists in the air.

- Transportation

1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

### 11. Typical Performance Curves





单击下面可查看定价，库存，交付和生命周期等信息

[>>TAI-TECH\(台庆\)](#)