

		Date: 2024/06/2	27	
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_	TAI-TECH P/N:	PAS3015EF-122K		_
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-	DESCRIPTION:			_
	QUANTITY:	рс	es	
EM	ARK:			
	Cı	ustomer Approval Feedt	back	

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# **Telecoil-antennas Inductors**

# PAS3015EF-122K

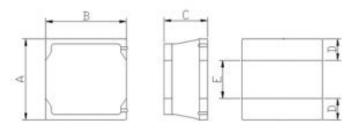
		ECN HISTORY LIS	ST		
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	24/06/27	New Issue	Sky Luo	Mr.Liang	Xu yaoyao
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# **Telecoil-antennas Inductors**

# 1. Features

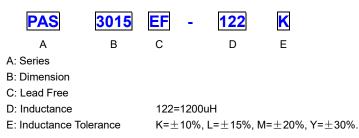
- 1. Hearing Aid Compatibility-/ Telecoil -antennas;
- 2. PAS3012F-series realizes small size and low profile. .3.0x3.0x1.2 mm.
- 3. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 4. Meets the T3/T4 FCC .Requirements (HAC-Act) acc. ANSI C63.19
- 5. Operating temperature :-40~+125  $^\circ\!\!\mathbb{C}$  (Including self temperature rise)

# 2. Dimension



Α	В	С	D	E
3.0±0.2	3.0±0.2	1.5max	1.0 ref	1.0 ref

# 3. Part Numbering



# 4. Specification

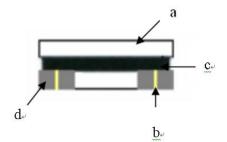
Part Number	Inductance (uH)±20%	Test Frequency	I rms (A)	l sat (A)	RDC(Ω)	Rated current (mA)	SRF MHz
	@ 0 A DC (Hz)				Мах	Max	(min)
PAS3015EF-122K	1200	1V/10K	0.08	0.08	39.0	80	2.00

Note:

Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\Delta t$  of 40 °C. Saturation Current (Isat) will cause L0 to drop approximately 30%. Rated DC current: The lower value of Irms and Isat.

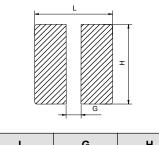
## 5. Material

No.	Description	Specification
а	Core	Ferrite Core
b	Wire	Enameled Copper Wire
с	Glue	Ероху
d	Terminal	Ag/Ni/Sn



RoHS

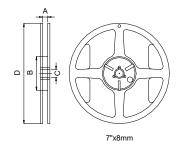
#### **Recommended Land pattern**



L	G	н
3.2	1.0	3.2

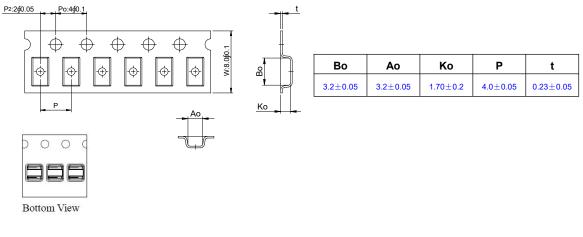
# 6. Packaging Information

### 6-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	8.4±1.0	50 min	13±0.8	178±2

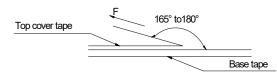
#### 6-2. Tape Dimension



#### 6-3. Packaging Quantity

Chip size	3015
Chip / Reel	2000

#### 6-4. Tearing Off Force



The force for tearing off cover tape is 10 to 100 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 standard).

Tearing Speed	Room Temp.	Room Humidity	Room atm
mm	(℃)	(%)	(hPa)
300±10%	5~35	45~85	

# 7. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	110~+40℃,50~60%RH (Product with taping) 240~+125℃(on board)	
Electrical Performance	Test	
		HP4284A,CH11025,CH3302,CH1320,CH1320S
Inductance	Refer to standard electrical characteristics list.	LCR Meter.
DCR		CH16502, Agilent33420A Micro-Ohm Meter.
Octometican Octoment (In ot)	American Antonio (1.200)	Saturation DC Current (Isat) will cause L0
Saturation Current (Isat)	Approximately △L30%.	to drop   ∆L(%)
		Heat Rated Current (Irms) will cause the coil temperature rise
Heat Rated Current (Irms)	Approximately △T40°C	$ riangle T(^{\circ}C)$ without core loss.
		1.Applied the allowed DC current.
		2. Temperature measured by digital surface thermometer.
Reliability Test		
Life Test		Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Temperature: 125±2°C(Inductor, ambient + temp rise) Applied current: rated current Duration: 1000±12hrs Measured at room temperature after placing for 24±2 hrs.
		Preconditioning: Run through IR reflow for 3times.
		(IPC/JEDECJ-STD-020E Classification Reflow Profiles)
Load Humidity		Humidity: 85±2% R.H,
Load Humany		Temperature: 85°C±2°C
		Duration: 1000hrs Min. Bead:with 100% rated current,
		Inductance: with 100% rated current
		Measured at room temperature after placing for 24±2 hrs.
		Preconditioning: Run through IR reflow for 3 times.
		(IPC/JEDECJ-STD-020E Classification Reflow Profiles) 1. Baked at50 ℃ for 25hrs, measured at room temperature
		after placing for 4 hrs.
		2. Raise temperature to $65 \pm 2^{\circ}$ C 90-100%RH in 2.5hrs, and
		keep 3 hours, cool down to $25^{\circ}$ in 2.5hrs.
Moisture Resistance	Appearance: No damage.	3. Raise temperature to 65 $\pm$ 2 $^\circ C$ 90-100%RH in 2.5hrs, and
	Inductance: within±10% of initial value. Q: Shall not exceed the specification value.	keep 3 hours, cool down to $25^\circ\!\mathbb{C}$ in
	RDC: within ±15% of initial value and shall not	2.5hrs,keep at 25 $^\circ\!\!{\rm C}$ for 2 hrs then keep at -10 $^\circ\!\!{\rm C}$ for 3 hrs
	exceed the specification value.	4. Keep at 25 $^\circ C$ 80-100%RH for 15min and vibrate at the
		frequency of 10 to 55 Hz to 10 Hz, measure at
		room temperature after placing for 1~2 hrs.
		Preconditioning: Run through IR reflow for 3 times.
		(IPC/JEDECJ-STD-020E Classification Reflow Profiles)
Thermal shock		Condition for 1 cycle
SHOCK		Step1: -40±2℃ 30±5min Step2: 125±2℃ ≅0.5min
		Step3: $125\pm2^{\circ}$ 30±5minNumber of cycles: 500
		Measured at room fempraturc after placing for 24±2 hrs.
	-	Preconditioning: Run through IR reflow for 3 times.
		( IPC/JEDECJ-STD-020E Classification Reflow Profiles)
N 61		Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes
Vibration		Equipment: Vibration checker
		Total Amplitude: 10g
		Testing Time : 12 hours(20 minutes, 12 cycles each of 3
		orientations).

## TAI-TECH

Item	Performance	Test Condition		
Bending	Appearance: No damage. ⊣Inductance: within±10% of initial value.	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.		
Shock	Q: Shall not exceed the specification value. RDC: within $\pm$ 15% of initial value and shall not exceed the specification value.	Peak         Normal duration (D)         Wave form         Velocity change (Vi)ft/sec		
		SMD         50         11         Half-sine         11.3		
		Lead 50 11 Half-sine 11.3		
		3 shocks in each direction along 3 perpendicular axes(18 shocks).		
Solderability		a. Method B1, 4 hrs @155°C dry heat @255°C±5°C		
	More than 95% of the terminal electrode should	Test time:5 +0/-0.5 seconds.		
	be covered with solder.	<ul> <li>b. Method D category 3. (steam aging 8hours ± 15 min)@</li> <li>260°C±5°C</li> <li>Test time: 30 +0/-0.5 seconds.</li> </ul>		
Resistance to Soldering Heat		Depth: completely cover the termination		
		Temperature(°C) Time(s) Temperature ramp/immersion Aumber of heat cycles		
		260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1		
Terminal Strength	Appearance: No damage. Inductance: within±10% of initial value. Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e.	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEG J-STD-020E Classification Reflow Profiles ) With the component mounted on a PCB with the device to b tested, applyaforce(>8080inch(2012mm):1kg,<=0805inch(2012 mm):0.5kg)to the side of a device being tested. This force sha be applied for 60 +1 seconds. Also the force shall be applie gradually as not to apply a shock to the component being tested.		

Note : When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition.

# 8.Soldering Specifications

#### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

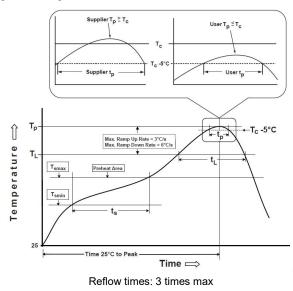
#### (3) Iron Reflow:

- Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended.(Fig. 2)
- $\cdot$   $\,$  Preheat circuit and products to 150  $^\circ\!\!\mathbb{C}$

Table (1.1): Reflow Profiles

- $\cdot$  355  $^\circ\!\mathrm{C}$  tip temperature (max)  $\cdot$  1.0mm tip diameter (max)
- Never contact the ceramic with the iron tip Use a 20 watt soldering iron with tip diameter of 1.0mm
  - Limit soldering time to 4~5sec.

#### Fig.1 Soldering Reflow



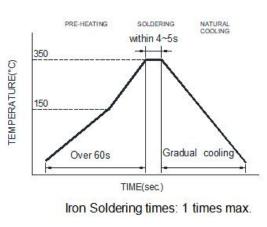


Fig.2 Iron soldering temperature profiles

Soldering iron Method : 350± 5  $^\circ\!\mathrm{C}$  max

Profile Type:	Pb-Free Assembly
Preheat	
-Temperature Min(T <sub>smin</sub> )	150℃
-Temperature Max(T <sub>smax</sub> )	200℃
-Time(ts)from(Tsmin to Tsmax)	60-120seconds
Ramp-up rate(T∟to T <sub>P</sub> )	3℃/second max.
Liquidus temperature(TL)	217℃
Time(t∟)maintained above T∟	60-150 seconds
Classification temperature(T <sub>c</sub> )	See Table (1.2)
$Time(t_p)$ at Tc- $5{}^\circ\!\mathrm{C}$ (Tp should be equal to or less than Tc.)	*< 30 seconds
Ramp-down rate( $T_P$ to $T_L$ )	6℃ /second max.
Time 25 $^\circ\!\!\!\!^\circ {\rm C}$ to peak temperature	8 minutes max.

Tp: maximum peak package body temperature, Tc: the classification temperature.

For user (customer) Tp should be equal to or less than Tc.

\* Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

#### Table (1.2) Package Thickness/Volume and Classification Temperature (T<sub>c</sub>)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm <sup>3</sup> >2000
PB-Free Assembly	<1.6mm	260°C	260°C	260°C
	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E.

### 9. Notes

- (1) When there are questions concerning measurement result : measurement shall be made after 48±2 hours of recovery under the standard condition.
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design, Our company only guarantees that the product meets the requirements of this specification.

#### Application Notice

- · Storage Conditions
  - To maintain the solderability of terminal electrodes:
  - 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
  - 2. Temperature and humidity conditions: Less than 40  $^\circ\!\mathrm{C}$   $\,$  and 60% RH.
  - Recommended products should be used within 12 months form the time of delivery.
     The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
  - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
  - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)