

Specification for Approval

Date: 2024/02/20





	Custom	er:	
	TAI-TECH P/N:	HFZ1608PV-121T3	0A
	CUSTOMER P/N:		
	DESCRIPTION:		
	QUANTITY:	pcs	<u>-</u>
REM	MARK:		
	Cu	stomer Approval Feedba	nck
		慶科技股份有 I Advanced Electronic	

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High Current Ferrite Chip Bead(Lead Free)

HFZ1608PV-121T30A

		ECN HISTO	RY LIS	Γ	
REV	DATE	DESCRIPTION	APPROVED	CHECKED	DRAWN
1.0	21/12/15	初版發行	鄧福興	浦冬生	王俞琴
2.0	22/12/05	更新可靠度及更正 Reflow 敘述	鄧福興	浦冬生	王俞琴
3.0	23/12/01	可靠度全面修訂為 REV E 版本	鄧福興	浦冬生	王俞琴
備					
註					

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TAI-TECH KBM01-240200382 P2.

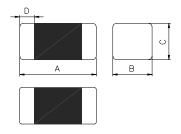
High Current Ferrite Chip Bead(Lead Free)

HFZ1608PV-121T30A

1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. High Current Bead Low RDC
- 6. Excellent solder ability and heat resistance.
- 7. High reliability. Reliability test meet AEC-Q200.
- 8.100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.
- 10. Operating Temperature: -55~+150 $^{\circ}$ C (Including self-temperature rise)

2.Dimensions



Chip Size					
Α	1.60±0.15				
В	0.80±0.15				
С	0.80±0.15				
D	0.30±0.20				

Units: mm

3.Part Numbering



A: Series

B: Dimension

n L x W Lead Free Material

C: Material D: Category Code

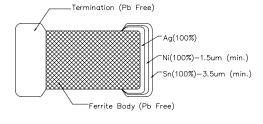
V=Vehicle 121=120 Ω

E: Impedance F: Packaging

T=Taping and Reel, B=Bulk(Bags)

G: Rated Current 30=3000mA

H:Category Code

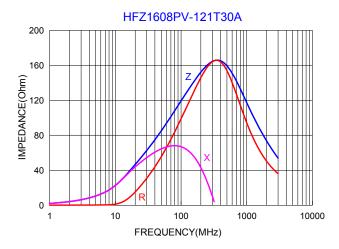


4. Specification

Tai-Tech Part Number	Impedance (C)	Test Frequency (MHz)	DC Resistance (Ω) max.	Rated Current (mA) max.
HFZ1608PV-121T30A	120±25%	100	0.035	3000

- Rated current: based on temperature rise test
- In compliance with EIA 595

■ Impedance-Frequency Characteristics



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5. Reliability and Test Condition

Item	Performance	Test Condition
Series No.	HFZ	
Operating Temperature	-55~+150℃ (Including self-temperature rise)	-
Transportation Storage Temperature	-55~+150℃ (on board)	For long storage conditions, please see the Application Notice
Impedance (Z)		Agilent4291 Agilent E4991 Agilent4287 Agilent16192
DC Resistance	Refer to standard electrical characteristics list	Agilent 4338
Rated Current		DC Power Supply Over Rated Current requirements, there will be some risk
Temperature Rise Test	Rated Current < 1A ∆T 20°C Max Rated Current ≧ 1A ∆T 40°C Max	Applied the allowed DC current. Temperature measured by digital surface Thermometer.
High Temperature Exposure(Storage)		Preconditioning:Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Unpowered Temperature: 150±2°C Upper Temperature: maximum specified operating temperature or maximum specified storage temperature (whichever is higher). Minimum test temperature shall be 85°C (For ferrite EMI suppressors/filters only) Duration: 1000hrs Min. Measured at room temperature after placing for 24±4 hrs
Temperature Cycling	Appearance: No damage. Impedance: within±15% of initial value RDC: Within ±15% of initial value and shall not exceed the specification value	Preconditioning:Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Unpowered Lower Temperature of the Chamber: -40°C (For Inductors/Transformers) -55°C (For ferrite EMI suppressors/filters) Upper Temperature of the Chamber: maximum specified operating temperature (temperature and shall not exceed 125°C) Condition for 1 cycle Step1: -55±2°C 30min Min Step2: 150±2°C 30min Min. Step3: 150±2°C 30min Min. Step4: Dwell Time (Soak Time) 15 minutes minimum, 30 minutes minimum, 30 minutes minimum if component weighs above 28g Transition Time: 1 minute maximum Number of cycles: 1000 Measured at room temperature at least 24 hours after test conclusion.
Destructive Physical Analysis	According to design guide standards	For ferrite EMI suppressors/filters only Pre and Post Electrical Test not required.
Humidity Bias	Appearance: No damage. Impedance: within±15% of initial value RDC: Within ±15% of initial value and shall not exceed the specification value	Preconditioning:Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Unpowered(For Inductors/Transformers) Apply 10% of maximum rated power.(For ferrite EMI suppressors/filters) Humidity :85±3%RH. Temperature:85±2°C. Duration :1000 hrs Min. Measured at room temperature after placing for 24±4

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Item	Performance	Test Condition					
High Temperature Operational Life	Appearance: No damage. Impedance: within±15% of initial value RDC: Within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through Reflow for 3 times. (IPC/JEDEC J-STD-020E Classification Reflow Profiles) Temperature: 150±2°C Upper Temperature of the Chamber: maximum specified operating temperature (not including heat rise) at maximum rated power and shall not exceed 125°C. (For Inductors/Transformers) Temperature of the Chamber: maximum specified operating temperature up to 150°C(For ferrite EMI suppressors/filters) Duration: 1000hrs Min. with 100% rated current. Measured at room temperature after placing for 24±4 hrs Rated I, applied.(For ferrite EMI suppressors/filters)					
External Visual	Appearance : No damage.	Inspect d	evice co ship. Pr	onstruction	, marking a t Electrical	ind	
Physical Dimension	According to the product specification size measurement	compone Pre and F	nt detail Post Ele	l specificat ctrical Tes	t not requir	ed.	
Terminal Strength (for axial and radial THT components)		Test Conc Nom	dition A wind created and a constant of the c	(pull test) DSS- sectic (mm²) 0.05 to 0.10 to 0.20 to 0.50 1.20 (wire-lead ulus (Zx) (ii 5x10-3 to 4.2x10-1 to 0.5x10-1 stions: ZX = tectangula is the other	bend test) mm³) 3 2 1 1 1 2 (πd3)/32 ε (ba2)/6 wh	Force (N) 1 2.5 5 10 20 40 Force (N) 0.5 1.25 2.5 5 10 20 where d is the endicular to the	
Resistance to Solvents	Appearance: No damage. Impedance: within±15% of initial value RDC: Within±15% of initial value and shall not exceed the specification value	rectangular strip. Add an Aqueous wash chemical and follow chemical manufacturer's recommended parameters (i.e.,solution temperature and immersion time). Applicable to ink marked components and not laser marked components					
		Preconditioning:Run through reflow for 3 times.(IPC/JEDEC J-STD-020E Classification Reflow Profiles Test condition:					
Mechanical Shock		Туре	Peak alue (g's)	Normal duration (D) (ms)	Wave form	Velocity change (Vi)ft/sec	
		THT 3 shocks (18shock)		6 6 direction a	Half-sine Half-sine along 3 per	12.3 12.3 pendicular axes	
Vibration		Precondit times.(IP Profiles Oscillation minute Equipment Total Amp	tioning:F PC/JEDE n Frequent: Vibrolitude:5 ime: 12	uency: 10 ration chec	020E Class Hz∼2KHz sker	reflow for 3 siffication Reflow \sim 10Hz for 20 2 cycles each or	

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Item	Performance			Condit	ion		
		Test condition THT: Condition Number of the	ions B or C				
		Solder	Test Ten	nperature		Temperature ramp/immersi	
		technique simulation	conditi on (°C)		Time (s)	on and emersion	
			000			rate 25mm/s±6mm	
		Dip		der temp)	10±1	/s	
		Wave: Topside board-mount product	C 260 (sol	±5 der temp)	20±1		
		Depth: com	pletely cove	er the tern	nination		
	Appearance : No damage.	SMD: Cond Num	ition K, time ber of heat		17℃,6	Os - 150s ·	
Resistance to Soldering	Impedance : within±15% of initial value	Temperature	time 25°C to peak	temperature			
Heat	RDC: Within ±15% of initial value and shall not exceed the specification value	component T ₅	non ls	T _{Serige} T ₁	ow process	t _p	
		1000		1611			
						Tim	
			tang up to Tam	1 to 1	I Too	U" time Ramp do	
		Congeneral Services of Congeneral Services of Congeneral Processing of Congeneral Services	Sed PICH to suppose of the property of the pro	soldering stop of the compor	c you size	C 200s 6.340 TO (The component compo	
						VE	
		Direct Contact and Air Discharge PASSIVE COMPONENT HBM ESD Discharge Waveform to a Coaxial Target Test method: AEC-Q200-002 Test mode: Contact Discharge Discharge level: 4 KV (Level: 2)					
ESD	Appearance: No damage. Impedance: within±15% of initial value RDC: Within ±15% of initial value and shall not exceed the specification value	90%		lime (ns)			
		Through-h Method A	ole Techno			rv 2	
		SMD: Method B	1, Coating , Coating D ion 50x ost Electric	Durability Ourability (Categor Categor	ory 2 y 2 ed.	
		参照	Method A1	Metho	1 R1	Method D	
Solder ability	Mars than OFO of the territorial electronic by the second control of the second control			其他器件			
Solder ability	More than 95% of the terminal electrode should be covered with solder.	焊接工藝	再流焊			無鉛銲接	
		焊接類型	錫銀銅焊料			錫銀銅焊料	
		浸入助焊劑	2 100	5-10)s	5-10s	
		浸入錫爐角	20 ° ~45 °	20°~	45 °	20°~45	
		焊料溫度	245 ±5°C	245 ±	5°C	260 ±5°C	
		浸入焊料時	5+0/-0.5s	5+0/-0).5s	30+5/-0s	
		浸入和提出	25 ±6mm/s	25 ±6n	nm/s	25 ±6mm/s	
	I control of the second of the						

Flammability In accordance with Referenced Standards UL-94 or IEC 60985-11-5 Appearance: No damage. Impedance: within::15% of initial value RDC: Within::15% of initial value Board Flex (SMD) Appearance: No damage. Impedance: within::15% of initial value RDC: Within::15% of initial value	Item	Performance	Test Condition
Appearance: No damage. Impedance: within £15% of initial value RDC: Within ±15% of initial value and shall not exceed the specification value Board Flex (SMD) Terminal strength (SMD) Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Terminal strength (SMD) Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: within £15% of initial value and shall not exceed the specification value Appearance: No damage. Impedance: No damage.		Refer Specification for Approval	requirements, (inductance only unless otherwise agreed upon) Summary to show minimum, maximum, mean and standard deviation at room, minimum and maximum operating temperatures.
Appearance: No damage. Impedance: within: 15% of initial value and shall not exceed the specification value Board Flex (SMD) Region 1	Flammability	In accordance with Referenced Standards	UL-94 or IEC 60695-11-5
Terminal strength (SMD) Appearance: No damage. Impedance: within±15% of initial value RDC: Within±15% of initial value and shall not exceed the specification value ethod: AEC-Q200-002 Test mode: Contact Discharge Discharge level: 4 KV (Level: 2) Table A.1 — Examples of test pulse severity levels for nominal 12 V system Test pulse Selected Test pulse Severity level; Winn, number of pulses repetition time Test pulse Severity level: III AEC-Q200,TAI-TECH SPEC.10N AEC-Q200,TAI-TECH SPEC.10N AEC-Q200,TAI-TECH SPEC.10N A selected level and test time for testing at or between these values may be chosen according the agreement between vehicle manufacturer ar supplier. In cases where no specific values a defined, it is recommended to use Test pulses: 2b Test pulses severity level: III Test pulse Severity level: III Test pulse severity level: III		Impedance : within±15% of initial value RDC : Within ±15% of initial value and shall not exceed the specification value Support Solder Chip Printed circuit board before testing	(IPC/JEDEC J-STD-020E Classification Reflow Profiles) Place the 100mm X 40mm board into a fixture similar to the one shown in below Figure with the component facing down. The apparatus shall consist of mechanical means to apply a force which will bend the board (D) x = 2 mm minimum. The duration of the applied forces shall be 60 (+ 5) sec. The force is to be
Appearance: No damage. Impedance: within±15% of initial value RDC: Within±15% of initial value and shall not exceed the specification value ethod: AEC-Q200-002 Test mode: Contact Discharge Discharge level: 4 KV (Level: 2) Table A.1 — Examples of test pulse severity levels for nominal 12 V system Test pulse* Selected Test pulse* Test pulse* Selected Test pulse* Test pulse* Test pulse* Test pulse* Selected Test pulse* Test pulse* Test pulse* Selected Test pulse* Test pulse* Test pulse* Selected Test pulse* Test pulse*	`	Printed circuit board under test	
Test mode: Contact Discharge Discharge level: 4 KV (Level: 2) Table A.1 — Examples of test pulse severity levels for nominal 12 V system Test pulses Test pulses Selected test level? V or pulses repetition time or test time Test pulses a defined, it is recommended to use Test pulses severity level. Test pulse severity level, V or pulse repetition time or test time Test pulses severity level: III	_	Appearance : No damage. Impedance : within±15% of initial value RDC : Within±15% of initial value and shall not exceed the specification value	95
Test pulse ^a Selected test level ^b V Test pulse severity level, U _k ^{cd} Min. number of pulses pulse repetition time or test time Test pulse severity level: III		Test mode : Contact Discharge Discharge level : 4 KV (Level: 2)	The state of the s
			Test pulse severity level: III

Test pulse ^a	Selected test level ^b			Min. number of pulses or test time	Burst cycle/ pulse repetition time		
		IV	Ш	1/11		min.	max.
1		-150	-112	-75	500 pulses	0,5 s	e
2a		+112	+55	+37	500 pulses	0,2 s	5 s
2b		+10	+10	+10	10 pulses	0,5 s	5 s
3a		-220	-165	-112	1 h	90 ms	100 ms
3b		+150	+112	+75	1 h	90 ms	100 ms

Electrical Transient Conduction

- Test pulses as in 5.0.

 Yalves agree between vehicle manufacturer and equipment supplier.

 The amplitudes are the values of U_i as defined for each test pulse in 5.0.

 The former levels I and III are revised because they did not ensure sufficient immunity in subsequent road vehicles' design.

 The maximum pulse regettion the shall be chosen such that it is the minimum time for the DUT to be correctly initialized before the application of the next pulse and shall be ±0.5 s.

Table A.2 — Suggested test pulse severity levels for nominal 24 V system

Test pulse ^a	Selected test level ^b	Test pulse severity level, $U_{\mathbf{S}}^{\mathrm{cd}}$		Min. number of pulses or test time	Burst cycle/ pulse repetition time							
		IV	III III	1/11		min.	max.					
1		-600	-450	-300	500 pulses	0,5 s	0					
2a		+112	+55	+37	500 pulses	0,2 s	5 s					
2b		+20	+20	+20	10 pulses	0,5 s	5 s					
3a		-300	-220	-150	1 h	90 ms	100 ms					
3b		+300	+220	+150	1 h	90 ms	100 ms					
a Test pulse	a Test pulses as in 5.6.											

- rest puses as in 5.6. Values agreed between vehicle manufacturer and equipment supplier. The amplitudes are the values of U_c as defined for each test pulse in 5.6. The former levels I and II are revised because they did not ensure sufficient immunity in subsequent road vehicles' design. The former levels I and II are revised because they did not ensure sufficient immunity in subsequent road vehicles' design. The maximum pulse repetition time shall be chosen such that It is the minimum time for the DUT to be correctly infilialized befor application of the next pulse and shall be ≥ 0.5 s.

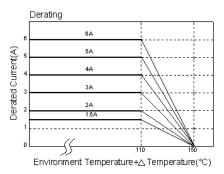
Supply voltages					
Supply voltage	Nominal 12 V system V	Nominal 24 V system V			
ΠΔ	13.5 ± 0.5	27 + 1			

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**Derating Curve

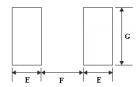
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over 110°C , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



6. Soldering and Mounting

6-1. Recommended PC Board Pattern

Chip Size						Pattern ow Sold		
Series	Туре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)
	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	0.50	0.40	0.60
HFZ	<mark>1608</mark>	<mark>1.6±0.15</mark>	0.80±0.15	0.80±0.15	0.30±0.20	<mark>0.80</mark>	<mark>0.85</mark>	<mark>0.95</mark>



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

6-2. Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

6-2.1 Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Figure 2.)

- Preheat circuit and products to 150℃
- Never contact the ceramic with the iron tip
- Use a 20 watt soldering iron with tip diameter of 1.0mm

- 350°C tip temperature (max)
- 1.0mm tip diameter (max)
- Limit soldering time to 4~5sec.

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Fig.1 Soldering Reflow

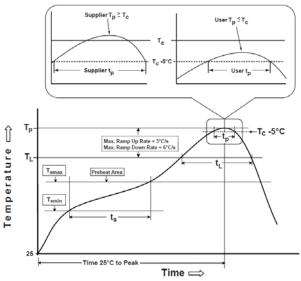
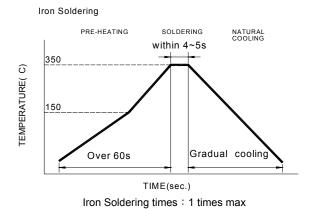


Fig.2 Iron soldering temperature profiles



Reflow times: 3 times max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly
Preheat -Temperature Min(T _{smin}) -Temperature Max(T _{smax}) -Time(t _s)from(T _{smin} to T _{smax})	150℃ 200℃ 60-120seconds
Ramp-up rate(T_L to T_p)	3°C/second max.
$\label{eq:Liquidus} \begin{array}{c} \text{Liquidus temperature}(T_L) \\ \text{Time}(t_L) \\ \text{maintained above } T_L \\ \end{array}$	217°C 60-150 seconds
Classification temperature(T _c)	See Table (1.2)
$\label{eq:total_power} \mbox{Time}(t_p) \mbox{ at Tc-} \mbox{ 5°C (Tp should be equal to or less than Tc.)}$	< 30 seconds
Ramp-down rate(T_p to T_L)	6°C /second max.
Time 25°C to peak temperature	8 minutes max.

 $\textbf{\textit{Tp}} : \mbox{maximum peak package body temperature, } \textbf{\textit{Tc}} : \mbox{the classification temperature.}$

For user (customer) **Tp** should be equal to or less than **Tc**.

Table (1.2) Package Thickness/Volume and Classification Temperature (T_c)

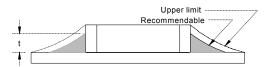
	Package Thickness	Volume mm ³ <350	Volume mm ³ 350-2000	Volume mm ³ >2000
	<1.6mm	260°C	260°C	260°C
PB-Free Assembly	1.6-2.5mm	260°C	250°C	245°C
	≥2.5mm	250°C	245°C	245°C

Reflow is referred to standard IPC/JEDEC J-STD-020E ∘

6-2.3 Solder Volume:

Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

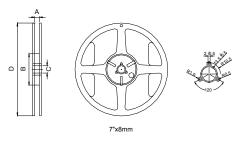
Minimum fillet height = soldering thickness + 25% product height



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7. Packaging Information

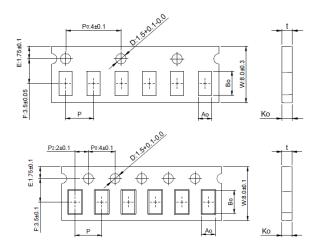
7-1. Reel Dimension



Туре	A(mm)	B(mm)	C(mm)	D(mm)
7"x8mm	9.0±0.5	60±2	13.5±0.5	178±2

7-2.1 Tape Dimension / 8mm

■Material of taping is paper



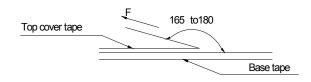
Ī	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
	100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
<mark>160808</mark>	<mark>1.90±0.05</mark>	1.10±0.05	<mark>0.95±0.05</mark>	<mark>4.0±0.10</mark>	<mark>0.95±0.05</mark>

7-3. Packaging Quantity

Chip Size	<mark>160808</mark>	100505
Chip / Reel	<mark>4000</mark>	10000
Inner box	<mark>20000</mark>	50000
Middle box	<mark>100000</mark>	250000
Carton	<mark>200000</mark>	500000

7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp.	Room Humidity	Room atm	Tearing Speed
(℃)	(%)	(hPa)	mm/min
5~35	45~85	860~1060	300

Application Notice

• Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
 - 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
 - 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
 - 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

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Test Report

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西北臺慶科技股份有限公司 (TAI-TECH ADVANCED ELECTRONICS CO., LTD.)

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以下測試樣品係由申請廠商所提供及確認 (The following sample(s) was/were submitted and identified by the applicant as):

樣品名稱(Sample Name)

: FERRITE CHIP BEAD - FERRITE CHIP INDUCTOR - ARRAY - MCF - MCM -

YMV APM SERIES

樣品型號(Style/Item No.)

FERRITE CHIP BEAD . FERRITE CHIP INDUCTOR . ARRAY . MCF . MCM .

YMV . APM SERIES

收件日(Sample Receiving Date)

28-Nov-2023

測試期間(Testing Period)

28-Nov-2023 to 05-Dec-2023

測試需求(Test Requested)

依據客戶要求進行測試‧測試項目請參閱測試結果表格。 (Testing item(s) is/are

specified by client. Please refer to result table for testing item(s).)

測試結果(Test Results)

請參閱下一頁 (Please refer to following pages.)

Troy Chang / Department Ma ager Signed for and on behalf of SGS TAIWAN LTD.
Chemical Laboratory - Taipei

CHECK REPORT

PIN CODE: E94C4B9

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SGS Taiwan Ltd. 台灣檢驗科技股份有限公司



Test Report

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測試部位敘述 (Test Part Description)

No.1 : 整體混測 (MIXED ALL PARTS)

測試結果 (Test Results)

測試項目	測試方法	單位	MDL	結果
(Test Items)	(Method)	(Unit)		(Result)
				No.1
鎘 (Cd) (Cadmium (Cd))	參考IEC 62321-5: 2013 · 以感應耦合電漿發	mg/kg	2	n.d.
	」射光譜儀分析。(With reference to IEC			
鉛 (Pb) (Lead (Pb))	62321-5: 2013, analysis was performed by	mg/kg	2	n.d.
	ICP-OES.)			=
汞 (Hg) (Mercury (Hg))	參考IEC 62321-4: 2013+ AMD1: 2017,以	mg/kg	2	n.d.
	感應耦合電漿發射光譜儀分析。(With			,
	reference to IEC 62321-4: 2013+ AMD1:			
	2017, analysis was performed by ICP-OES.)			
六價鉻 Cr(VI) (Hexavalent Chromium	參考IEC 62321-7-2: 2017 · 以紫外光-可見光	mg/kg	8	n.d.
Cr(VI))	分光光度計分析。(With reference to IEC			
	62321-7-2: 2017, analysis was performed			
	by UV-VIS.)			
一溴聯苯 (Monobromobiphenyl)		mg/kg	5	n.d.
二溴聯苯 (Dibromobiphenyl)]	mg/kg	5	n.d.
三溴聯苯 (Tribromobiphenyl)		mg/kg	5	n.d.
四溴聯苯 (Tetrabromobiphenyl)]	mg/kg	5	n.d.
五溴聯苯 (Pentabromobiphenyl)	参考IEC 62321-6: 2015·以氣相層析儀/質譜	mg/kg	5	n.d.
六溴聯苯 (Hexabromobiphenyl)	儀分析。(With reference to IEC 62321-6:	mg/kg	5	n.d.
七溴聯苯 (Heptabromobiphenyl)	2015, analysis was performed by GC/MS.)	mg/kg	5	n.d.
八溴聯苯 (Octabromobiphenyl)		mg/kg	5	n.d.
九溴聯苯 (Nonabromobiphenyl)]	mg/kg	5	n.d.
十溴聯苯 (Decabromobiphenyl)]	mg/kg	5	n.d.
多溴聯苯總和 (Sum of PBBs)		mg/kg	i s	n.d.

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測試項目	測試方法	單位	MDL	結果
(Test Items)	(Method)	(Unit)		(Result)
				No.1
一溴聯苯醚 (Monobromodiphenyl ether)		mg/kg	5	n.d.
二溴聯苯醚 (Dibromodiphenyl ether)		mg/kg	5	n.d.
三溴聯苯醚 (Tribromodiphenyl ether)	& 孝IEC 62221 6:2015 . 以気料扇板俤/矫諩	mg/kg	5	n.d.
四溴聯苯醚 (Tetrabromodiphenyl ether)		mg/kg	5	n.d.
五溴聯苯醚 (Pentabromodiphenyl ether)	參考IEC 62321-6: 2015 · 以氣相層析儀/質譜	mg/kg	5	n.d.
六溴聯苯醚 (Hexabromodiphenyl ether)	儀分析。(With reference to IEC 62321-6:	mg/kg	5	n.d.
七溴聯苯醚 (Heptabromodiphenyl ether)	2015, analysis was performed by GC/MS.)	mg/kg	5	n.d.
八溴聯苯醚 (Octabromodiphenyl ether)		mg/kg	5	n.d.
九溴聯苯醚 (Nonabromodiphenyl ether)		mg/kg	5	n.d.
十溴聯苯醚 (Decabromodiphenyl ether)		mg/kg	5	n.d.
多溴聯苯醚總和 (Sum of PBDEs)		mg/kg	-	n.d.
鄰苯二甲酸丁苯甲酯 (BBP) (Butyl benzyl		mg/kg	50	n.d.
phthalate (BBP))				
鄰苯二甲酸二丁酯 (DBP) (Dibutyl		mg/kg	50	n.d.
phthalate (DBP))				
鄰苯二甲酸二(2-乙基己基)酯 (DEHP) (Di-		mg/kg	50	n.d.
(2-ethylhexyl) phthalate (DEHP))	A 老IFC 63231 0: 2017 以复担展长梯 /所述	1000		
鄰苯二甲酸二異丁酯 (DIBP) (Diisobutyl	參考IEC 62321-8: 2017 · 以氣相層析儀/質譜 儀分析。(With reference to IEC 62321-8:	mg/kg	50	n.d.
phthalate (DIBP))	According According Commence of the According Commence of the			
鄰苯二甲酸二異癸酯 (DIDP) (Diisodecyl	2017, analysis was performed by GC/MS.)	mg/kg	50	n.d.
phthalate (DIDP)) (CAS No.: 26761-40-	1			
0, 68515-49-1)				
鄰苯二甲酸二異壬酯 (DINP) (Diisononyl		mg/kg	50	n.d.
phthalate (DINP)) (CAS No.: 28553-12-		æ 15		
0, 68515-48-0)				

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測試項目 (Test Items)	測試方法 (Method)	單位 (Unit)	MDL	結果 (Result)
 郷苯二甲酸二正辛酯 (DNOP) (Di-n-octyl		mg/kg	50	No.1 n.d.
phthalate (DNOP)) (CAS No.: 117-84-0)		AT		DOUGH PERM
鄰苯二甲酸二正戊酯 (DNPP) (Di-n-pentyl phthalate (DNPP)) (CAS No.:	參考IEC 62321-8: 2017 · 以氣相層析儀/質譜 儀分析。(With reference to IEC 62321-8:	mg/kg	50	n.d.
131-18-0)	2017, analysis was performed by GC/MS.)			
鄰苯二甲酸二正己酯 (DNHP) (Di-n-hexyl		mg/kg	50	n.d.
phthalate (DNHP)) (CAS No.: 84-75-3) 六溴環十二烷及所有主要被辨別出的異構	 参考IEC 62321-9: 2021 · 以氣相層析儀/質譜	mg/kg	20	n.d.
物(HBCDD) (α- HBCDD, β- HBCDD, γ-	儀分析。(With reference to IEC 62321-9:	mg/kg	20	11.0.
HBCDD) (Hexabromocyclododecane	2021, analysis was performed by GC/MS.)			
(HBCDD) and all major diastereoisomers identified (α- HBCDD,				
β- HBCDD, γ- HBCDD)) (CAS No.:				
25637-99-4, 3194-55-6 (134237-51-7,				
134237-50-6, 134237-52-8))				
氟 (F) (Fluorine (F)) (CAS No.: 14762-94-8)		mg/kg	50	n.d.
氯 (CI) (Chlorine (CI)) (CAS No.: 22537-	 参考BS EN 14582: 2016·以離子層析儀分	mg/kg	50	n.d.
15-1)	析。(With reference to BS EN 14582: 2016,	g/ Ng	3.0	1.1.3.
溴 (Br) (Bromine (Br)) (CAS No.: 10097-	analysis was performed by IC.)	mg/kg	50	n.d.
32-2)		Л	F0	
碘 (I) (Iodine (I)) (CAS No.: 14362-44-8)	☆*CEN/TC 150C0 2010 N/さ42尺と中間	mg/kg	50	n.d.
全氟辛烷磺酸及其鹽類 (PFOS and its salts) (CAS No.: 1763-23-1 and its salts)	參考CEN/TS 15968: 2010 · 以液相層析串聯 質譜儀分析。(With reference to CEN/TS	mg/kg	0.01	n.d.
全氟辛酸及其鹽類 (PFOA and its salts)	15968: 2010, analysis was performed by	mg/kg	0.01	n.d.
(CAS No.: 335-67-1 and its salts)	LC/MS/MS.)			

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測試項目	測試方法	單位	MDL	結果
(Test Items)	(Method)	(Unit)		(Result)
				No.1
銻 (Sb) (Antimony (Sb)) (CAS No.: 7440-		mg/kg	2	n.d.
36-0)	參考US EPA 3052: 1996 · 以感應耦合電漿發			
鈹 (Be) (Beryllium (Be)) (CAS No.: 7440-	射光譜儀分析。(With reference to US EPA	mg/kg	2	n.d.
41-7)	3052: 1996, analysis was performed by			
砷 (As) (Arsenic (As)) (CAS No.: 7440-	ICP-OES.)	mg/kg	2	n.d.
38-2)		-22 -392		
聚氯乙烯 (Polyvinyl chloride) (PVC)	參考ASTM E1252: 2021 · 以傅立葉轉換紅外	**	123	Negative
	線光譜儀及焰色法分析。(With reference to			
	ASTM E1252: 2021, analysis was			
	performed by FT-IR and Flame Test.)			

備註(Note):

- 1. mg/kg = ppm; 0.1wt% = 0.1% = 1000ppm
- 2. MDL = Method Detection Limit (方法偵測極限值)
- 3. n.d. = Not Detected (未檢出); 小於MDL / Less than MDL
- 4. "-" = Not Regulated (無規格值)
- 5. ** = Qualitative analysis (No Unit) 定性分析(無單位)
- 6. Negative = Undetectable 陰性(未偵測到); Positive = Detectable 陽性(已偵測到)
- 7. 樣品的測試是基於申請人要求混合測試,報告中的混合測試結果不代表其中個別單一材質的含量。
 The sample(s) was/were analyzed on behalf of the applicant as mixing sample in one testing. The above result(s) was/were only given as the informality value.

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PFAS Remark:

現有PFAS定量技術是分析PFAS物質的特定結構,但同碳數族群之PFAS酸及鹽類物質,其可被辨識的特定結構相同,因此無法區別所分析的特定結構是來自酸或者鹽類,故測試結果為同碳數族群之PFAS之酸及鹽類物質的濃度總合。下表PFAS物質濃度皆已包含在測試結果中,相關資訊請參見下表:(下表列舉PFAS物質僅為範例,並不包含所有同碳數族群之PFAS鹽類。)

(The quantitative technology of PFAS is to analyze the specific structure of PFAS substances. However, PFAS acid and its salts with the same carbon number group have the same specific structure that can be identified. The tested results of the analyzed specific structure cannot be distinguished to identify the contribution from PFAS acid or its salts. Therefore, the tested results display the sum of concentrations of PFAS acids and its salts with the same carbon number group. The concentration of PFAS substances in the below table have been included in the tested results, please refer to the table for relevant information: (The listed PFAS substances are examples only, it do not include all PFAS salts with the same carbon number group.))

物質濃度分類 (Classification of Substance Concentration)	物質名稱 (Substance Name)	CAS No.
全氟辛烷磺酸及其鹽類 Perfluorooctane sulfonates and its salts (PFOS and its salts) (CAS No.: 1763-23-1 and its salts)	全氟辛基磺酸鉀 (PFOS-K) Potassium perfluorooctanesulfonate (PFOS-K)	2795-39-3
	全氟辛基磺酸鋰 (PFOS-Li) Perfluorooctanesulfonic acid, lithium salt (PFOS-Li)	29457-72-5
	全氟辛基磺酸銨 (PFOS-NH ₄) Perfluorooctanesulfonic acid, ammonium salt (PFOS-NH ₄)	29081-56-9
	全氟辛基磺酸二乙醇銨 (PFOS-NH(OH) ₂) Perfluorooctane sulfonate diethanolamine salt (PFOS-NH(OH) ₂)	70225-14-8
	全氟辛基磺酸四乙基銨 (PFOS-N(C_2H_5) ₄) Perfluorooctanesulfonic acid,tetraethylammonium salt (PFOS-N(C_2H_5) ₄)	56773-42-3
	全氟辛基磺酸二癸二甲基銨 (PFOS-DDA) N-decyl-N,N-dimethyldecan-1-aminium 1,1,2,2,3,3,4,4,5,5,6,6,7,7,8,8,8-heptadecafluorooctane-1-sulfonate (PFOS-DDA)	251099-16-8

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SGS Taiwan Ltd. 台灣檢驗科技股份有限公司



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物質濃度分類 (Classification of Substance Concentration)	物質名稱 (Substance Name)	CAS No.
全氟辛烷磺酸及其鹽類 Perfluorooctane sulfonates and its salts (PFOS and its salts) (CAS No.: 1763-23-1 and its salts)	全氟辛基磺醯氟 (POSF) Perfluorooctane sulfonyl fluoride (POSF)	307-35-7
	全氟辛基磺酸鎂 (PFOS-Mg) Perfluorooctanesulfonic acid, magnesium salt (PFOS-Mg)	91036-71-4
	全氟辛基磺酸鈉 (PFOS-Na) Perfluorooctanesulfonic acid, sodium salt (PFOS-Na)	4021-47-0
全氟辛酸及其鹽類 Perfluorooctanoic acid and its salts (PFOA and its salts) (CAS No.: 335-67-1 and its salts)	全氟辛酸鈉 (PFOA-Na) Sodium perfluorooctanoate (PFOA-Na)	335-95-5
	全氟辛酸鉀 (PFOA-K) Potassium perfluorooctanoate (PFOA-K)	2395-00-8
	全氟辛酸銀 (PFOA-Ag) Silver perfluorooctanote (PFOA-Ag)	335-93-3
	全氟辛氟 (PFOA-F) Perfluorooctanoyl fluoride (PFOA-F)	335-66-0
	全氟辛酸銨 (APFO) Ammonium pentadecafluorooctanoate (APFO)	3825-26-1
	全氟辛酸鋰 (PFOA-Li) Lithium perfluorooctanoate (PFOA-Li)	17125-58-5

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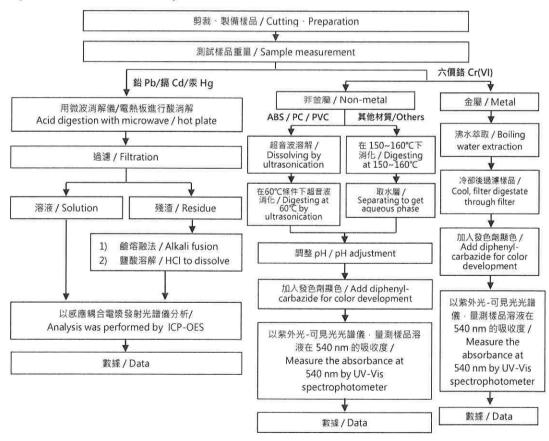
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重金屬流程圖 / Analytical flow chart of heavy metal

根據以下的流程圖之條件,樣品已完全溶解。(六價鉻測試方法除外)

These samples were dissolved totally by pre-conditioning method according to below flow chart. (Cr^{6+} test method excluded)



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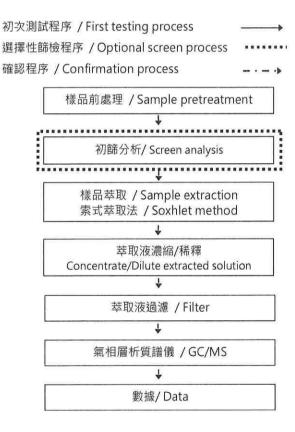
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多溴聯苯/多溴聯苯醚分析流程圖 / Analytical flow chart - PBBs/PBDEs



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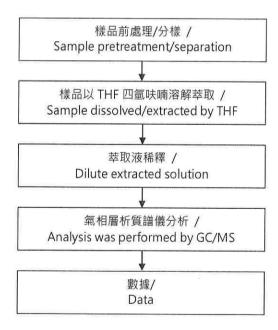
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可塑劑分析流程圖 / Analytical flow chart - Phthalate

【測試方法/Test method: IEC 62321-8】



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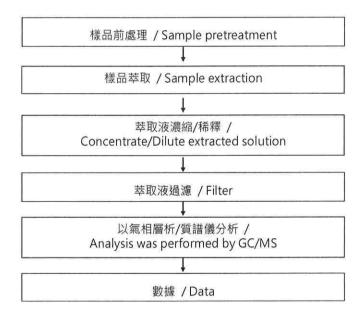
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六溴環十二烷分析流程圖 / Analytical flow chart - HBCDD



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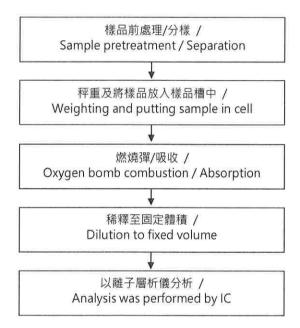
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鹵素分析流程圖 / Analytical flow chart - Halogen



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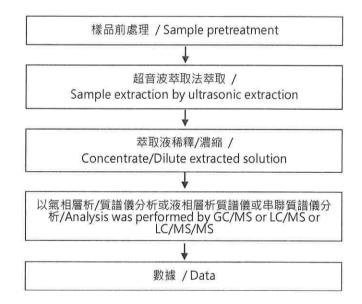
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全氟化合物(包含全氟辛酸/全氟辛烷磺酸/其相關化合物等等)分析流程圖 / Analytical flow chart – PFAS (including PFOA/PFOS/its related compound, etc.)



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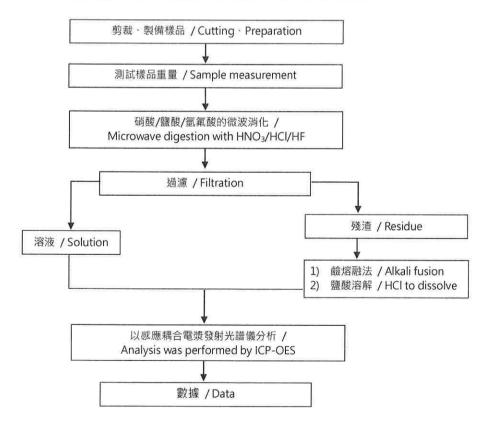
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元素(含重金屬)分析流程圖 / Analytical flow chart of elements (Heavy metal included)

根據以下的流程圖之條件,樣品已完全溶解。

These samples were dissolved totally by pre-conditioning method according to below flow chart.

【參考方法/Reference method: US EPA 3051A、US EPA 3052】



* US EPA 3051A 方法未添加氫氟酸 / US EPA 3051A method does not add HF.

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西北臺慶科技股份有限公司 (TAI-TECH ADVANCED ELECTRONICS CO., LTD.)

臺慶精密電子(昆山)有限公司 (TAI-TECH ADVANCED ELECTRONICS (KUN-SHAN) CO., LTD.)

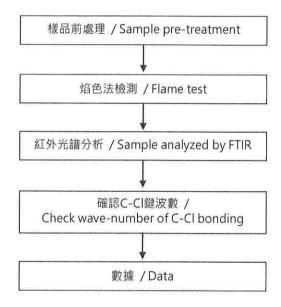
慶邦電子元器件 (泗洪) 有限公司 (TAIPAQ ELECTRONICS (SI-HONG) CO., LTD.)

桃園市楊梅區幼獅工業區幼四路1號 (NO. 1, YOU 4TH ROAD, YOUTH INDUSTRIAL DISTRICT, YANG-MEI, TAO-YUAN CITY, TAIWAN R. O. C.)

江蘇省昆山市篷朗昆嘉高科技工業區郭澤路 (GUO-ZE ROAD, KUNJIA HI-TECH INDUSTRIAL PARK, KUN-SHAN, JIANG-SU, CHINA)

中國·江蘇省·宿遷市·泗洪縣·經濟開發區杭州路南側·建設北路東側 (THE SOUTH HANGZHOU ROAD AND THE EAST JIANSHE ROAD · ECONOMIC DEVELOPMENT ZONE · SIHONG COUNTY · SUQIANCITY · JIANGSU PROVINCE · P,R · CHINA)

聚氯乙烯物質判定分析流程圖 / Analysis flow chart - PVC



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SGS Taiwan Ltd. 台灣檢驗科技股份有限公司



Test Report

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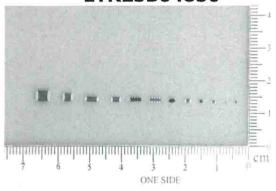
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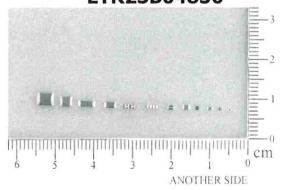
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* 照片中如有箭頭標示,則表示為實際檢測之樣品/部位. * (The tested sample / part is marked by an arrow if it's shown on the photo.)

ETR23B04850



ETR23B04850



** 報告結尾 (End of Report) **

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>>TAI-TECH(台庆)