

# Specification for Approval

Date: 2021/05/19

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	Custo	omer:				
	TAI-TECH P/N:	TMPC2313F	IP-Series	s(G)-D		
	CUSTOMER P/N	V:				
	DESCRIPTION:					
	QUANTITY:		pcs			
	REMARK:					
	(	Customer Approval	Feedback	(		
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			APPRO	OVED	CHECKED	
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APPROVED	CHECKED	DRAWN
Sky Luo	Mr.Liang	Cui lingling

# **SMD Power Inductor**

TMPC2313HP-Series(G)-D

ECN HISTORY LIST							
REV DATE		DESCRIPTION	APPROVED	CHECKED	D DRAWN		
1.0	21/05/19	New Issue	Sky Luo	Mr.Liang	Cui lingling		
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注							

## **SMD Power Inductor**

TMPC2313HP-Series(G)-D

#### 1. Features

- 1. Carbonyl Powder.
- 2. Compact design.
- 3. High current , low DCR , high efficiency.
- 4. Very low acoustic noise and very low leakage flux noise.
- 5. High reliability.
- 6. 100% Lead(Pb)-Free and RoHS compliant.
- 7. Operating temperature -40~+125  $^{\circ}\text{C}\textsc{(Including self temperature rise)}$

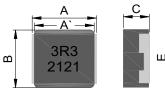


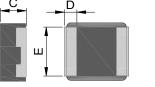


## 2. Applications

Note PC power system , incl. IMVP-6 DC/DC converter .

### 3. Dimensions

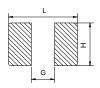






Series	A(mm)	A`(mm)	B(mm)	C(mm)	D(mm)	E(mm)
TMPC2313HP	23.8±0.5	22.7±0.3	22.0±0.3	12.6±0.4	5.0±0.4	19.0±0.3

#### **Recommend PC Board Pattern**



L(mm)	G(mm)	H(mm)
24	12.5	19.5

#### Note: 1.PCB layout is referred to standard IPC-7351B

- 2. The above PCB layout reference only.
- Recommend solder paste thickness at 0.15mm and above.

## 4. Part Numbering



A: Series

B: Dimension E

C: Type HP:H:Carbonyl Powder,P:PAD broaden.

D: Inductance 3R3=3.30uH

E: Inductance Tolerance M=±20%

F: Code Marking: Black.3R3 and 2121(21 YY, 21 WW,follow production date).

# 5. Specification

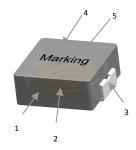
Part Number	Inductance L0 (uH)±20%	I rms (A) Typ	I sat ( A ) Typ	DCR (mΩ) Typ. @25℃	DCR (mΩ) Max. @25℃
TMPC2313HP-2R0MG-2M8-D	2.00	45.0	100	2.3	2.8
TMPC2313HP-3R3MG-D	3.30	42.0	70.0	1.7	2.0
TMPC2313HP-820MG-D	82.0	11.5	14.0	33.5	40.0

#### Note:

- 1. Test frequency: Ls: 100KHz /1.0V.
- 2. All test data referenced to 25°C ambient.
- 3. Testing Instrument(or equ): L: HP4284A, CH11025, CH3302, CH1320, CH1320S LCR METER / Rdc: CH16502, Agilent33420A MICRO OHMMETER.
- 4. Heat Rated Current (Irms) will cause the coil temperature rise approximately  $\,\Delta T$  of 40  $^{\circ}\! C$
- 5. Saturation Current (Isat) will cause L0 to drop approximately 30%.
- 6. The part temperature (ambient + temp rise) should not exceed 125°C under worst case operating conditions. Circuit design, component, PCB trace size and thickness, airflow and other cooling provisions all affect the part temperature. Part temperature should be verified in the end application.
- 7. Irms Testing: Temperature rise is highly dependent on many factors including pcb land pattern, trace size, and proximity to other components.

  Therefore temperature rise should be verified in application conditions.
- 8. Rated DC Current : The less value whith is Irms or Isat

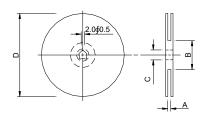
## 6. Material List

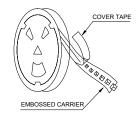


NO	Items	Materials
1	Core	Carbonyl Powder.
2	Wire	Polyester Wire or equivalent.
3	Clip	100% Pb free solder(Ni+SnPlating)
4	lnk	Halogen-free ketone
5	paint	Epoxy resin

# 7. Packaging Information

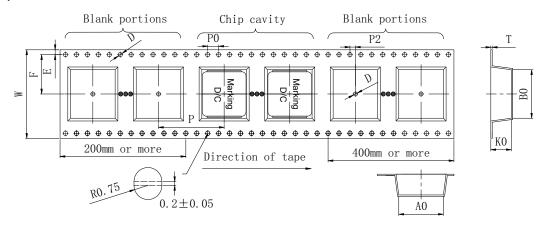
#### (1) Reel Dimension





Туре	A(mm)	B(mm)	C(mm)	D(mm)
13"x44mm	44.4+2/-0	100±2	13.5±0.5	330

#### (2) Tape Dimension

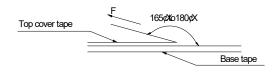


s	eries	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	Po(mm)	P2(mm)	W(mm)	F(mm)	E(mm)	T(mm)	D(mm)
1	МРА	2313	25±0.1	23.0±0.1	13.6±0.1	32±0.1	4.0±0.1	2.0±0.1	44±0.3	20.2±0.1	1.75±0.1	0.5±0.05	1.5±0.1

## (3) Packaging Quantity

ТМРС	2313
Chip / Reel	80
Inner box	80
Carton	320

#### (4) Tearing Off Force



The force for tearing off cover tape is 10 to 130 grams in the arrow direction under the following conditions(referenced ANSI/EIA-481-D-2008 of 4.11 stadnard).

Tearing Speed	Room Temp.	Room Humidity	Room atm	
mm	(℃)	(%)	(hPa)	
300±10%	5~35	45~85	860~1060	

# 8. Reliability and Test Condition

Item	Performance	Test Condition
Operating temperature	-40~+125°C (Including self - temperature rise)	
Storage temperature	110~+40°C,50~60%RH (Product with taping) 240~+125°C (on board)	
Electrical Performance Te	est	
Inductance	Refer to standard electrical characteristics list.	HP4284A,CH11025,CH3302,CH1320,CH1320S LCR Meter.
DCR	7	CH16502,Agilent33420A Micro-Ohm Meter.
Saturation Current (Isat)	Approximately △L20% (Isat 1) Approximately △L30% (Isat 2)	Saturation DC Current (Isat) will cause L0 to drop $\triangle$ L(%)
Heat Rated Current (Irms)	Approximately △T40°C	Heat Rated Current (Irms) will cause the coil temperature rise △T(℃) without core loss.  1.Applied the allowed DC current. 2.Temperature measured by digital surface thermometer
Reliability Test		2.10mporataro modourou 2) algitar cariaco tromomoto.
Life Test		Preconditioning: Run through IR reflow for 3times.  ( IPC/JEDECJ-STD-020E Classification Reflow Profiles)  Temperature: 125±2°C (Inductor · ambient + temp rise)  Applied current: rated current  Duration: 1000±12hrs  Measured at room temperature after placing for 24±2 hrs.
Load Humidity		Preconditioning: Run through IR reflow for 3times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Humidity: 85±2% R.H, Temperature: 85℃±2℃ Duration: 1000hrs Min. Bead:with 100% rated current, Inductance: with 100% rated current Measured at room temperature after placing for 24±2 hrs.
Moisture Resistance	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) 1. Baked at50 $^{\circ}$ for 25hrs, measured at room temperature after placing for 4 hrs. 2. Raise temperature to $65\pm2$ $^{\circ}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}$ in 2.5hrs. 3. Raise temperature to $65\pm2$ $^{\circ}$ 90-100%RH in 2.5hrs, and keep 3 hours, cool down to $25^{\circ}$ in 2.5hrs, keep at $25^{\circ}$ for 2 hrs then keep at -10 $^{\circ}$ for 3 hrs 4. Keep at $25^{\circ}$ 80-100%RH for 15min and vibrate at the frequency of 10 to 55 Hz to 10 Hz, measure at room temperature after placing for 1~2 hrs.
Thermal shock		Preconditioning: Run through IR reflow for 3 times. (IPC/JEDECJ-STD-020E Classification Reflow Profiles) Condition for 1 cycle $Step1: -40\pm2^{\circ}C  30\pm5min$ $Step2: 125\pm2^{\circ}C  \le 0.5min$ $Step3: 125\pm2^{\circ}C  30\pm5minNumber of cycles:  500$ Measured at room fempraturc after placing for 24 $\pm2$ hrs.
Vibration		Preconditioning: Run through IR reflow for 3 times.  ( IPC/JEDECJ-STD-020E Classification Reflow Profiles) Oscillation Frequency: 10Hz~2KHz~10Hz for 20 minutes Equipment: Vibration checker Total Amplitude: 10g Testing Time: 12 hours(20 minutes, 12 cycles each of 3 orientations).

Item	Performance	Test Condition						
Bending	Appearance : No damage. -Inductance : within±10% of initial value	Shall be mounted on a FR4 substrate of the following dimensions: >=0805 inch(2012mm):40x100x1.2mm <0805 inch(2012mm):40x100x0.8mm Bending depth: >=0805 inch(2012mm):1.2mm <0805 inch(2012mm):0.8mm duration of 10 sec.						
Shock	Q: Shall not exceed the specification value.  RDC: within ±15% of initial value and shall not exceed the specification value		Туре	Peak value (g's)	dura	ormal tion (D) ms)	Wave form	Velocity change (Vi)ft/sec
			SMD	50		11	Half-sine	11.3
			Lead	50		11	Half-sine	11.3
Solder ability	More than 95% of the terminal electrode should be covered with solder.	a. Method B1, 4 hrs @155°C dry heat @235°C±5°C Test time:5 +0/-0.5 seconds.						
		<ul> <li>b. Method D category 3. (steam aging 8hours ± 15 min)@ 260°C±5°C Test time: 30 +0/-0.5 seconds.</li> <li>Depth: completely cover the termination</li> </ul>						
Resistance to Soldering Heat		Temperature (°C) Time(s) Temperature ramp/immersion and emersion rate heat cycles  260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s 1					heat cycles	
Terminal Strength	Appearance: No damage. Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification value e	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles With the component mounted on a PCB with the device to be tested, applyaforce(>0805inch(2012mm)):1kg,<=0805inch(2012 mm):0.5kg)to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to apply a shock to the component being tested.						

Note: When there are questions concerning measurement result: measurement shall be made after  $48 \pm 2$  hours of recovery under the standard condition.

## 9. Soldering Specifications

#### (1) Soldering

Mildly activated rosin fluxes are preferred. TAI-TECH terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

#### (2) IR Soldering Reflow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. Table 1.1&1.2 (J-STD-020E)

#### (3) Iron Reflow:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. In the event that a soldering iron must be employed the following precautions are recommended. (Fig. 2)

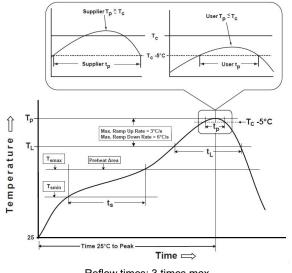
- Preheat circuit and products to 150  $^\circ\!\mathbb{C}$
- Never contact the ceramic with the iron tip
- 1.0mm tip diameter (max)
- Use a 20 watt soldering iron with tip diameter of 1.0mm

Fig.2 Iron soldering temperature profiles

· Limit soldering time to 4~5sec.

Fig.1 IR Soldering Reflow

• 355°C tip temperature (max)



Reflow times: 3 times max

PRE-HEATING SOLDERING NATURAL COOLING

Within 4~5s

150

Over 60s

Gradual cooling

TIME(sec.)

Iron Soldering times: 1 times max.

Soldering iron Method: 350±5°C max

Table (1.1): Reflow Profiles

Profile Type:	Pb-Free Assembly			
Preheat -Temperature Min(T <sub>smin</sub> )	150℃			
-Temperature Max(T <sub>smax</sub> ) -Time(t <sub>s</sub> )from(T <sub>smin</sub> to T <sub>smax</sub> )	200°C 60-120seconds			
Ramp-up rate(T₋to Tթ)	3°C/second max.			
Liquidus temperature( $T_L$ ) Time( $t_L$ )maintained above $T_L$	217°C 60-150 seconds			
Classification temperature(T <sub>c</sub> )	See Table (1.2)			
$\label{eq:tp} \mbox{Time}(t_p) \mbox{ at Tc-}  5^{\circ}\! \mbox{C} \mbox{ (Tp should be equal to or less than Tc.)}$	*< 30 seconds			
Ramp-down rate(Tp to TL)	6°C /second max.			
Time 25℃ to peak temperature	8 minutes max.			

**Tp**: maximum peak package body temperature, **Tc**: the classification temperature.

Table (1.2) Package Thickness/Volume and Classification Temperature (Tc)

	Package Thickness	Volume mm <sup>3</sup> <350	Volume mm <sup>3</sup> 350-2000	Volume mm³ >2000
PB-Free Assembly	<1.6mm	260℃	260℃	260℃
	1.6-2.5mm	260℃	250℃	245℃
	≥2.5mm	250℃	245℃	245℃

Reflow is referred to standard IPC/JEDEC J-STD-020E °

For user (customer) **Tp** should be equal to or less than **Tc.** 

<sup>\*</sup> Tolerance for peak profile temperature (Tp) is defined as a supplier minimum and a user maximum.

#### 10. Notes

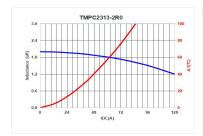
(1) When there are questions concerning measurement result : measurement shall be made after 48 ± 2 hours of recovery under the standard condition

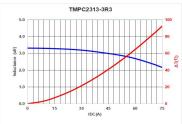
- (2) This power choke coil itself does not have any protective function in abnormal condition such as overload, short-circuit and open-circuit conditions, etc. Therefore, it shall be confirmed as the end product that there is no risk of smoking, fire, dielectric withstand voltage, insulation resistance, etc. in abnormal conditions to provide protective devices and/or protection circuit in the end product.
- (3) When this power choke coil was used in a similar or new product to the original one, sometimes it might not be able to satisfy the specifications due to different condition of use.
- (4) Dielectric withstanding test with higher voltage than specific value will damage insulating material and shorten its life.
- (5) This power choke coil must not be used in wet condition by water, coffee or any liquid because insulation strength becomes very low in this condition.
- (6) Please consult our company to confirm the reliability of the process required to wash or use or exposure to a chemical solvent used in this product. PCB washing tested to MIL-STD-202 Method, and dry it off immediately.
- (7) The rated current as listed is either the saturation current or the heating current depending on which value is lower.
- (8) If this power choke is dipped in the cleaning agent, such as toluene, xylene, ketone, and ether system, there is a possibility that the performance decreases greatly, and marking disappearnc.
- (9) The high power ultrasonic washing may damage the choke body.
- (10) Before use, the user should determine whether this product is suitable for their own design. Our company only guarantees that the product meets the requirements of this specification.

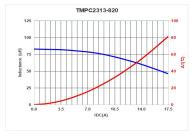
#### **Application Notice**

- · Storage Conditions
  - To maintain the solderability of terminal electrodes:
- 1. TAI-TECHproducts meet IPC/JEDEC J-STD-020E standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40°C and 60% RH.
- 3. Recommended products should be used within 12 months form the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- $3. \ \mbox{Bulk}$  handling should ensure that abrasion and mechanical shock are minimized.

# 11. Typical Performance Curves







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