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			SPEC.No. Date:	ASDIQ-SPE-234(00) Jul.14,2023
То :				
	CUSTOME	R'S PRODUCT NAM	16	
	000 TOME			
	ASDI P	RODUCT NAME:		
	STPM160	808A-2R2M		
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RECEIPT CONFIRM	MATION			
UNCOND	ITIONAL CONSENT		CONDITIONAL C	ONSENT
	APPROVED		CHECKED	
ASDI SIGNATURE				
	APPROVED	CHECKED	PREPARED	1
	Xianglong Li	Liang Wang	Jiayin Cai	
				J



Xiamen ASDI Electronics Co.,Ltd.

REV.	DATE	DESCRIPTION	APPROVED	CHECKED	PREPARED
00	Jul.14,2023	New release	Xianglong Li	Liang Wang	Jiayin Cai
			<u> </u>		

CUSTOMER	CUSTOMER AS STPM		CUSTOMER'S DWG NC
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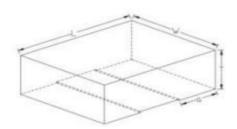
### 1. Features

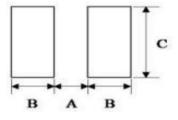
- 1. Soft saturation .
- 2. High current, low DCR, high efficiency.
- 3. Very low acoustic noise and very low leakage flux noise.
- 4. High reliability.
- 5. 100% Lead (Pb)-Free and RoHS compliant.

### 2. Applications

Note PC power system  $\cdot$  incl. IMVP-6 DC/DC converter.

3. Dimensions





Series	L(mm)	W(mm)	T(mm)	a(mm)	Α	В	С
160808	1.6±0.2	0.8±0.2	0.8Max	0.4±0.2	0.6~0.8	0.6~0.8	0.6~0.8

#### Note:

1. Inductance tolerance code (M=±20%).

2. Rated current: Isat or Irms, whichever is smaller.

3. Isat: Max.Value, DC current at which the inductance drops less than 30% from its value without current; Typ. Value, DC current at which the inductance drops 30% from its value without current.

4. Irms: DC current that will cause the temperature rise ( $\Delta T$ ) from 22°C ambient.

5. For Max. Value,  $\Delta T < 40C$ ; for Typ. Value,  $\Delta T$  is approximate 40C.

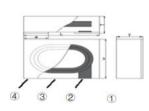
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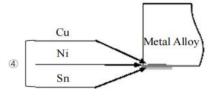
4	Part Numbering									
	STPM         160808           ①         ②	<b>A</b> ③	-	<b>2R2</b> ④	M (5)					
	<ol> <li>Series Name</li> <li>Dimension</li> <li>Type</li> </ol>		160808=1	1.6×0.8×0.	8mm					
	<ul> <li>④ Inductance</li> <li>⑤ Inductance Tolerar</li> </ul>	ice	2R2 =2.2 M = ±20%							
5	.Specification									
	ASDI Part Number	Inductance (µH)	DCR Typ. (mΩ)	DCR Max. (mΩ)	I sat Typ. (A)	I sat Max. (A)	I rms Typ. (A)	l rms Max. (A)	Test Frequency (MHz)	
	STPM160808A-2R2M	2.2	220	260	1.5	1.3	1.4	1.2	1	

SRF (MHz)

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#### 6.Material List





No.	Description	Specification
1	Metal Alloy Body	Metal Alloy powder
2	Inner Wire	Enamelled Copper Wire
3	Pull-out Electrode	Cu
(4)	Terminal	Electro-Plating: Cu/Ni/Sn

#### 7.Electrical characteristic test

Test item	Judgement standard	Test conditions	
DCR		Test equipment: High Accuracy Milliohmmeter-AX 1152D	
L	Reference electrical characteristics	<ul> <li>a. Test equipment: High Accuracy RF ImpedanceAnalyzer- WK 6500B.</li> <li>b.Test signal:1V.8</li> <li>c. Test frequency refers to Electrical Characteristics.</li> </ul>	
Temperat ure Rise Current (Irms)	Approximately △T≦40℃.	<ul> <li>a.Set test current to be 0 mA</li> <li>b.Measure initial temperature of chip surface.</li> <li>c.Gradually increase voltage and measure chip temperature for corresponding current.</li> <li>d.Definition ofTemperature Rise Current (Irms) :Irms is direct electric current as chip surface</li> <li>tempcraturc rose just 40°C against chip initial surfacc temperature.</li> </ul>	
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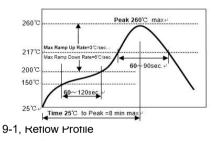
Saturation Current (Isat)	∆L≦30% typical.	a.Test equipment: High Accuracy RF ImpedanceAnalyzer- WK 6500B. b.Measuring Frequency: 1MHz. c.Test Current: 1mA. d.Definition of Saturation Current (Isat) : Isat is the value ofDC current as inductance L (uH) decreased just 30% against initial value.
Self-Resonant Frequency (SRF)	Refer to ElectricalCharacteristics.	a.Test equipment: High Accuracy RF ImpedanceAnalyzerWK 6500B. b.Test signal: 1V

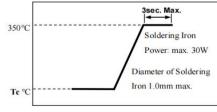
8.Reliability Tests

Test item	Requirements	Test conditions
Terminal Strength	No removal or split of the termination or otherdefects shall occur.	1.Solder the inductor to the testing jig (glass epoxyboard shown in Fig.8.1-1) using cutectic solder.Then apply a 10N force in the direction of thearrow. 2.Keep time: 10+ls 3.Speed: 1.0mm/s.
Resistance to Flexure	No visible mechanical damage. Unit: mm [inch] Type a b c 160808 0.6 2.2 1.2 201208 0.8 2.4 1.4 201210 0.8 2.4 1.4 201010 0.8 2.4 1.4 252010 1.3 3.0 2.3 Fig.8.2-1	<ul> <li>1.Solder the inductor to the test jig (glass epoxy0board shown in Fig8.2-1) Using a eutectic solder. Then apply a force in the direction shownFig.8.2-2.</li> <li>2.Flexure: 2mm.</li> <li>3.Pressurizing Speed: 0.5mm/sec</li> <li>4.Keep time: 30 sec.</li> <li>5.Test board size: 100x40x 1.0.</li> </ul>
Vibration	1.No visible mechanical damage. 2.Inductance changc: Within +10%. Cu pad Solder mask Glass Epoxy Board Fig. 8.3-1	<ol> <li>Solder the inductor to the testing jig (glass epoxy board shown in Fig.8.3-1) using cutectic solder.</li> <li>The inductor shall be subjected to a simple harmonic motion having total amplitude of 1.5mm, the frequency being varied uniformly between the approximate limits of 10 and 55 Hz.</li> <li>The frequency range from 10 to 55 Hz and return to 10 1z shall be traversed in approximately 1 minute. This motion shall be applied for a period of 2 hours in euch 3mutually perpendicular directions (total of 6 hours).</li> </ol>
Dropping	1.No visible mechanical damage. 2.Inductance change: Within +10%.	Drop chip inductor 10 times on a concrete floor from a height of 100 cm.
Temperature	Inductance change should be within +20% of initial value measuring at 25°C.	Temperature range: -40C~+125°C Reference temperature: +25°C
Solderability	1.No visible mechanical damage. 2.Wetting shall exceed 90% coverage.	1.Solder temperature: 245+2°C 2.Duration: 3 sec. 3.Solder: Sn/3.0Ag/0.5Cu 4.Flux: 25% Resin and 75% ethanol in weiht.
Resistance to Soldering Heat	1.No visible mechanical damage. 2.Inductance change: Within +10%	1.Re-flowing Profile: Please refer to Fig 8.7-1. 2.Test board thickness: 1.0mm 3.Test board material: glass epoxy resin 4.The chip shall be stabilized at normal condition for 1-2 hours before measuring.
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Thermal Shock	1.No mechanical damage. 2.Inductance change: Within +10% Ambient Temperature 40°C 30 min. Fig. 8.8-1	<ol> <li>Temperature, Time: (See Fig.8.8-1) 40°C for 30+3 min+ 125C for 30+3min.</li> <li>Transforming interval: 20 sec.(Max.).</li> <li>Tested cycle: 100 cycles.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>
Resistance to Low Temperature	1.1No mechanical damage. 2.Inductance change: Within +10%	1.Temperature: -40+2C 2.Duration: 1000+24 hours. 3.The chip shall be stabilized at normal condition for I-2 hours before measuring.
Resistance to High Temperature	1.No mechanical damage. 2.Inductance change: Within +10%	<ol> <li>Temperature: 125+2°C</li> <li>Duration: 1000<sup>+24</sup> hours.</li> <li>The chip shall be stabilized at normal condition for 1-2 hours before measuring.</li> </ol>
Damp Heat(SteadySt ates)	1.No visible mechanical damage. 2.Inductance change: Within +10%.	<ol> <li>Temperature: 60-2°C</li> <li>Humidity: 90% to 95% RH</li> <li>Duration: 1000+24 hours.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>
Loading Under Damp Heat	1.No visible mechanical damage. 2.Inductance change: Within +10%.	1.Temperature: 60-2°C 2.Humidity: 90% to 95% RH 3.Duration: 1000+24 hours. 4.Applied current: Rated current. 5.The chip shall be stabilized at normal condition for 1~2 hours before measuring.
Loading at High Iemperature (Life Test)	1.No visible mechanical damnage. 2.Inductance change: Within +10%.	<ol> <li>Temperature: 85+2°C</li> <li>Duration: 1000+24 hours.</li> <li>Applied current: Rated current.</li> <li>The chip shall be stabilized at normal condition for 1~2 hours before measuring.</li> </ol>

# 9.Soldering and Mounting 9-1,Reflow Profile



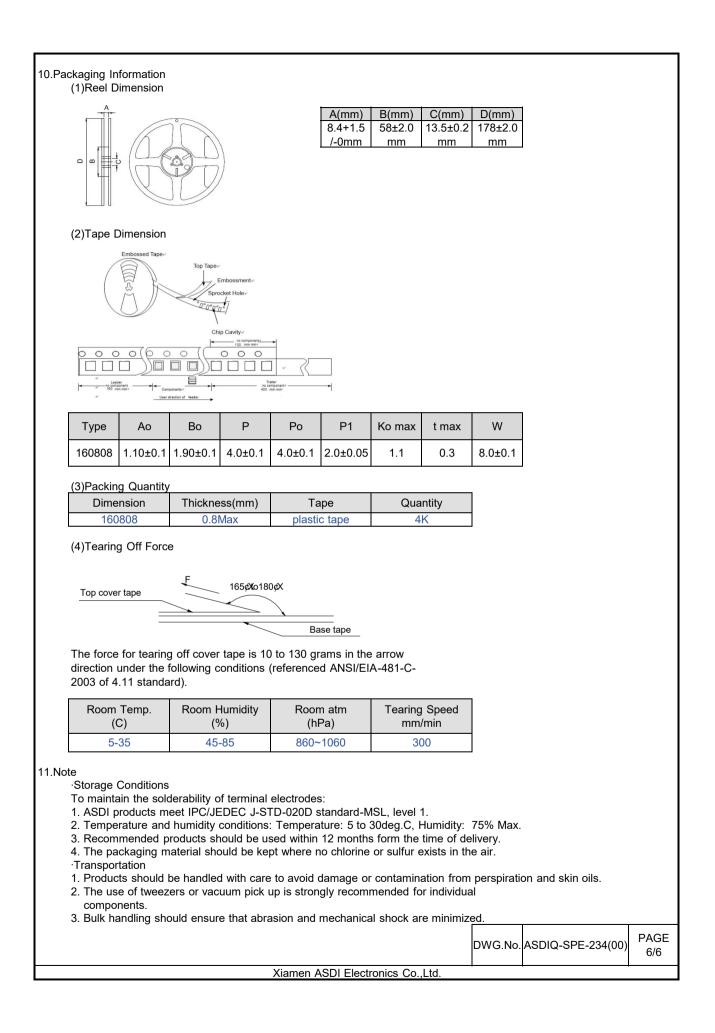


Preheat condition	150 ~200°C /60~120sec
Allowed time above	217°C: 60~90sec
Max temp	260°C
Max time at Max temp	10sec
Solder paste	Sn/3.0Ag/0.5Cu
Allowed Reflow time	2x Max

Iron soldering power	Max.30W
Pre-heating	150 °C / 60sec
Soldering Tip temperature	350°CMax
Soldering time	3sec Max
Solder paste	Sn/3.0Ag/0.5Cu
Max	1 times for iron

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