

SEA & LAND ELECTRONIC CORP.

www.sealand-pptc.com

ALPHA-TOP TECHNOLOGY CORP.



## APPROVAL SHEET

MODEL NO.:	nSM050-30V		
CUSTOMER:			
CUSTOMER'S	APPROVAL:		
ALITHODIZED (			
AUTHORIZED S	SIGNATURE/STAMP:		
DATE			

MANUFACTURER:

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Submitted by: Chen
Approved by: YC Lin
DATE: 12-Jul-22

SEA & LAND ELECTRONIC CORP.



## nSM050-30V

#### Features

- Surface Mount Devices
- Lead free device
- Size 3.2\*1.6 mm/0.12\*0.06 inch
- Surface Mount packaging for automated assembly

#### Applications

Almost anywhere there is a low voltage power supply, up to 60V and a load to be

protected, including:

- Computer mother board, Modern. USB hub
- PDAs & Charger, Analog & digital line card
- Digital cameras, Disk drivers, CD-ROMs,

Alpha-Top (Sea&Land Alliance)

### Performance Specification

Marshall	Mariatic or	V <sub>max</sub>		I <sub>hold</sub>	I <sub>trip</sub>	$P_d$		mum Го Trip	Resis	stance	Agency	Approval
Model	Marking		max	@25°C	@25°C	Max.	Current	Time	$Ri_{min}$	R1max	UL	TUV
		(Vdc)	(A)	(A)	(A)	(W)	(A)	(Sec)	$(\Omega)$	$(\Omega)$		
nSM050-30V	αF	30	100	0.50	1.00	0.6	8.00	0.10	0.150	0.700		

Ihold = Hold Current. Maximum current device will not trip in 25°C still air.

Itrip = Trip Current. Minimum current at which the device will always trip in 25°C still air.

Vmax = Maximum operating voltage device can withstand without damage at rated current (Imax).

Imax = Maximum fault current device can withstand without damage at rated voltage (Vmax).

d = Power dissipation when device is in the tripped state in 25°C still air environment at rated voltage.

Rimin/max = Minimum/Maximum device resistance prior to tripping at 25°C.

R1<sub>max</sub> = Maximum device resistance is measured one hour post reflow.

CAUTION: Operation beyond the specified ratings may result in damage and possible arcing and flame.

#### **Environmental Specifications**

Test	Conditions				
Passive aging	+85°C, 1000 hrs.				
Humidity aging	+85°C, 85% R.H., 168 hours				
Thermal shock	+85°C to -40°C, 20 times				
Resistance to solvent	MIL-STD-202, Method 215				
Vibration	MIL-STD-202, Method 201				
Ambient operating conditions :	- 40 °C to 85 °C				
Maximum surface temperature of the de	evice in the tripped state is 125 °C				
In case of special use, please contact our engineer					

#### Agency Approvals :

Regulation/Standard:



2015/863/EU



EN14582

#### I<sub>hold</sub> Versus Temperature

hold versus remperut	uic								
Model		Max	kimum ambie	ent operating	temperature	e (T <sub>mao</sub> ) vs. h	old current (	I <sub>hold</sub> )	
	-40°C	-20°C	0°C	25°C	40°C	50°C	60°C	70°C	85°C
nSM050-30V	0.710	0.640	0.570	0.500	0.420	ი ვვი	0.350	0.310	0.250



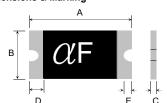
## nSM050-30V

Alpha-Top (Sea&Land Alliance)

Construction And Dimension (Unit:mm)

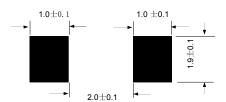
Model		A		3			D	E
Wodei	Min.	Max.	Min.	Max.	Min.	Max.	Min.	Min.
nSM050-30V	3.00	3.50	1.50	1.80	0.80	1.80	0.15	0.10

#### **Dimensions & Marking**



α = Trademark F = Part identification

#### Recommended Pad Layout (mm)



#### **Termination Pad Characteristics**

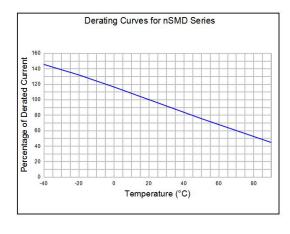
Terminal pad materials: Tin-plated Nickel-Copper

Meets EIA specification RS186-9E and ANSI/J-STD-002 Category 3. Terminal pad solderability:

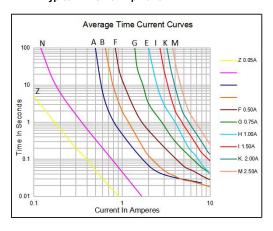
Rework

Use standard industry practices, the removal device must be replaced with a fresh one.

#### **Thermal Derating Curve**



#### Typical Time-To-Trip At 25°C



# WARNING:

- Use PPTC beyond the maximum ratings or improper use may result in device damage and possible electrical arcing and flame.
- PPTC are intended for protection against occasional over current or over temperature fault conditions and should not be used when repeated fault conditions or prolonged trip events are anticipated.

  Device performance can be impacted negatively if devices are handled in a manner inconsistent with recommended electronic, thermal, and mechanical procedures for electronic components.

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   Lovoid impact PPTC with a large inductance in circuit will generate a circuit voltage (L didt) above the rated voltage of the PPTC.

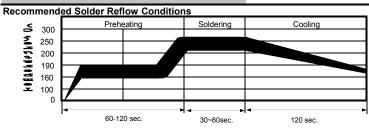
   Avoid impact PPTC device its thermal expansion like placed under pressure or installed in limited space.

   Contamination of the PPTC material with certain silicon based oils or some aggressive solvents can adversely impact the performance of the devices. PPTC SMD can be cleaned by standard methods.

   Requests that customers comply with our recommended solder pad layouts and recommended reflow profile. Improper board layouts or reflow profile could negatively impact solderability performance of our devices.



## nSM050-30V



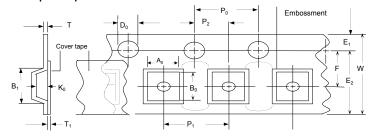
- Recommended reflow methods: IR, vapor phase oven, hot air oven.
- Devices are not designed to be wave soldered to the bottom side
- of the board.
- Recommended maximum paste thickness is 0.25 mm (0.010 inch).
- Devices can be cleaned using standard method and solvents.

  Note: If reflow temperatures exceed the recommended profile,
  - devices may not meet the performance requirements.

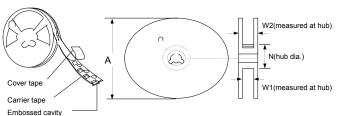
#### Tape And Reel Specifications (mm)

Governing Specifications	EIA 481-1
W	8.15 ± 0.3
P0	4.0 ± 0.10
P1	4.0 ± 0.10
P2	2.0 ± 0.05
A0	1.95 ± 0.10
B0	3.45 ± 0.10
B1max.	4.35
D0	1.5 + 0.1, -0
<u>F</u>	$3.5 \pm 0.05$
E1	1.75 ± 0.10
E2min.	6.25
Tmax.	0.6
T1max.	0.1
K0	1.04 ± 0.1
Leader min.	390
Trailer min.	160
Reel Dimensions	
A max.	178
N min.	60
W1	9 ± 0.5
W2	12.6 ± 0.5

#### **EIA Tape Component Dimensions**



#### **EIA Reel Dimensions**



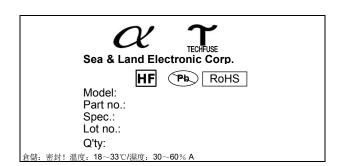
## Storage And Handling

- Storage conditions : 40°C max, 70% R.H.
- Devices may not meet specified performance if storage conditions are exceeded.

Order Information		Packaging				
	nSMD	050-30V	Tape & Reel Quantity			
	Product name	Hold				
	Size 3216 mm / 1206 inch	Current	3500 pcs/reel			
	SMD : surface mount device	0.50A				

Tape & reel packaging per EIA481-1

#### Labeling Information



## 单击下面可查看定价,库存,交付和生命周期等信息

# >>SEA-LAND(台湾陆海)