

# Specification for Approval

Date: 2021/12/27

**Customer:** 

	TAI-TECH F	P/N:	HCB3216KF-101T60	)		
	CUSTOME	R P/N:				
	DESCRIPT	ION:				
	QUANTITY:					
REMARK:						
	Cu	stomer App	roval Feedback			
			支股份有限公 d Electronics Co L			

#### 代理商:

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TA734003

**TAI-TECH KBM01-200700563** P2.

## High Current Ferrite Chip Bead(Lead Free)

HCB3216KF-101T60

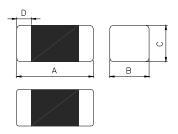
Certificate

Green Partner

### 1.Features

- 1. Monolithic inorganic material construction.
- 2. Closed magnetic circuit avoids crosstalk.
- 3. Suitable for reflow soldering.
- 4. Shapes and dimensions follow E.I.A. spec.
- 5. Available in various sizes.
- 6. Excellent solder ability and heat resistance.
- 7. High reliability.
- 8. 100% Lead(Pb) & Halogen-Free and RoHS compliant.
- 9. Low DC resistance structure of electrode to prevent wasteful electric power consumption.
- 10. Operating Temperature: -55~+125° (Including self-temperature rise)

### 2.Dimensions

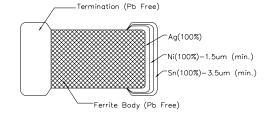


	Chip Size				
<b>A</b> 3.20±0.20					
В	1.60±0.20				
С	1.10±0.20				
D	0.50±0.30				

Units: mm

## 3.Part Numbering



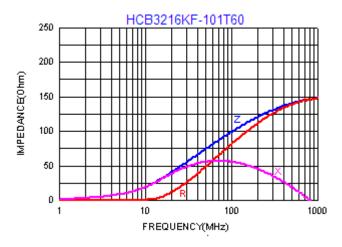


## 4. Specification

Tai-Tech Part Number	Impedance (())		DC Resistance $(\Omega)$ max.	Rated Current (mA) max.
HCB3216KF-101T60	100±25%	60mV/100M	0.018	6000

- Rated current: based on temperature rise test
- In compliance with EIA 595

#### ■ Impedance-Frequency Characteristics



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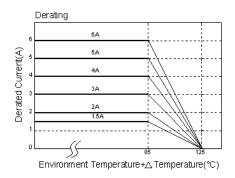
## 5. Reliability and Test Condition

Item			Performa	ance			Test Condition				
Series No.	FCB	FCM	HCB		GHB	FCA					
Operating Temperature		(Inclu	-55~+125 uding self-tem		e rise)						
Transportation Storage Temperature			-55~+125 (on boar				For long			ons, please	see the
Impedance (Z)	Defeate stee		-h	- li-4			Agilent4 Agilent E Agilent4 Agilent1	E4991 287			
DC Resistance	Refer to star	ndard electrical	cnaracteristics	S IIST			Agilent 4				
Rated Current							DC Pow Over Ra some ris	ted Curi		ements, the	ere will be
Temperature Rise Test		< 1A ΔT 20℃Max ≧ 1A ΔT 40℃M					2. Temp			current. by digital si	urface
Life test	Impedance:	: no damage. within±15%of ir					times.( I Reflow F Tempera Applied Duration Measure for 24±2	PC/JED Profiles) ature: 12 current: i: 1000± ed at ro hrs.	EC J-STE 25±2°C rated curr 12hrs. com tempe	erature afte	ssification
Load Humidity	Q : Shall no	within±10%of in t exceed the spo n ±15% of initial	ecification valu		exceed the spe	ecification value	times.( I Reflow F Humidity Tempera Duration ent , Ir	PC/JED Profiles) /: 85±2% ature: 85 ::1000hr nductanced at ro	EC J-STE 6R.H. 6±2℃. rsMin.Bead ce: with	bugh IR refl 0-020E Class d:with100%l 10% rated erature afte	ssification ratedcurr I current
Thermal shock	Impedance: Inductance: Q : Shall no	: no damage. within±15%of ir within±10%of in t exceed the sp n ±15% of initial	nitial value. ecification valu		exceed the spe	ecification value	Precond times.( I Reflow F Conditio Step1: -! Step2: 2 Step3: + Number	itioning: PC/JED Profiles) n for 1 c 55±2°C 5±2°C 125±2°C of cycle ed at ro	eycle 30±5 ≤ 0.5r 30±5nes: 500	min	ssification
Vibration	Impedance : Inductance : Q : Shall no	: No damage. : within±15% of : within±10% of t exceed the sp n ±15% of initial	initial value ecification valu		exceed the spe	ecification value	times.( I Reflow F Oscillation for 20 m Equipment Total Am	PC/JED Profiles) on Fred inutes ent: Vi aplitude: Time: 1	EC J-STE quency: 1 bration ch 10g 2 hours(2)	ough IR refl 0-020E Clas 0Hz ~ 2KHz ecker 0 minutes,	ssification z ∼ 10Hz
Bending	Impedance : Inductance : Q : Shall no	: No damage. : within±10% of : within±10% of t exceed the sp n ±15% of initial	initial value ecification valu		exceed the spe	ecification value	following >=0805i <0805in Bending >=0805i <0805in	dimens nch(201 ch(2012 depth: nch(201 ch(2012	sions: 2mm):40x	ım	
	Impedance	: No damage. : within±10% of					Test co	ndition Peak Value	Normal duration	Wave form	Velocity change
Shock		within±10% of t exceed the spe		ue.			SMD	(g's) 50	(D) (ms)	Half-sine	(Vi)ft/sec 11.3
		n ±15% of initial			exceed the spe	ecification value	Lead	50	11	Half-sine	11.3
										@155°C c +0/-0.5 seco	

Item	Performance	Test Condition		
		Number of heat cycles: 1		
Resistance to Soldering	Appearance: No damage. Impedance: within±15% of initial value	Temperature (°C) Time (s) Temperature ramp/immersion and emersion rate		
Heat	Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within ±15% of initial value and shall not exceed the specification va	ue 260 ±5 (solder temp) 10 ±1 25mm/s ±6 mm/s		
		Depth: completely cover the termination		
Terminal strength	Appearance: No damage. Impedance: within±15% of initial value Inductance: within±10% of initial value Q: Shall not exceed the specification value. RDC: within±15% of initial value and shall not exceed the specification value	Preconditioning: Run through IR reflow for 3 times.( IPC/JEDEC J-STD-020E Classification Reflow Profiles) Component mounted on a PCB apply a force >0805inch(2012mm):1kg <=0805inch(2012mm):0.5kg to the side of a device being tested. This force shall be applied for 60 +1 seconds. Also the force shall be applied gradually as not to shock the component being tested.		

#### \*\*Derating Curve

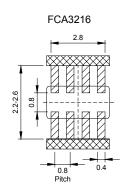
For the ferrite chip bead which withstanding current over 1.5A, as the operating temperature over  $85^{\circ}\mathrm{C}$ , the derating current information is necessary to consider with. For the detail derating of current, please refer to the Derated Current vs. Operating Temperature curve.



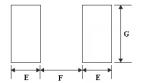
## 6. Soldering and Mounting

#### 6-1. Recommended PC Board Pattern

	Chip Size							Land Patterns For Reflow Soldering		
Series	Туре	A(mm)	B(mm)	C(mm)	D(mm)	E(mm)	F(mm)	G(mm)		
	0603	0.6±0.03	0.30±0.03	0.30±0.03	0.15±0.05	0.35	0.30	0.40		
FCB	1005	1.0±0.10	0.50±0.10	0.50±0.10	0.25±0.10	0.50	0.40	0.60		
FCM	1608	1.6±0.15	0.80±0.15	0.80±0.15	0.30±0.20	0.80	0.85	0.95		
HCB	0040	2.0±0.20	1.25±0.20	0.85±0.20	0.50±0.30	4.05	4.00	4.45		
GHB	2012	2.0±0.20	1.25±0.20	1.25±0.20	0.50±0.30	1.05	1.00	1.45		
FCI	<mark>3216</mark>	3.2±0.20	1.60±0.20	1.10±0.20	0.50±0.30	<mark>1.05</mark>	<mark>2.20</mark>	<mark>1.80</mark>		
FHI	3225	3.2±0.20	2.50±0.20	1.30±0.20	0.50±0.30	1.05	2.20	2.70		
FCH	4516	4.5±0.20	1.60±0.20	1.60±0.20	0.50±0.30	1.05	3.30	1.80		
HCI	4532	4.5±0.20	3.20±0.20	1.50±0.20	0.50±0.30	1.05	3.30	3.40		



∠
∠
∠
Land
∠
Solder Resist
∠



PC board should be designed so that products can prevent damage from mechanical stress when warping the board.

#### 6-2. Soldering

Mildly activated rosin fluxes are preferred. The terminations are suitable for re-flow soldering systems. If hand soldering cannot be avoided, the preferred technique is the utilization of hot air soldering tools.

Note.

If wave soldering is used ,there will be some risk.

Re-flow soldering temperatures below 240 degrees, there will be non-wetting risk

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#### 6-2.1 Lead Free Solder re-flow:

Recommended temperature profiles for lead free re-flow soldering in Figure 1. (Refered to J-STD-020C)

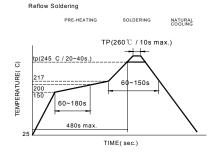
#### 6-2.2 Soldering Iron:

Products attachment with a soldering iron is discouraged due to the inherent process control limitations. If a soldering iron must be employed the following precautions are recommended. for Iron Soldering in Figure 2.

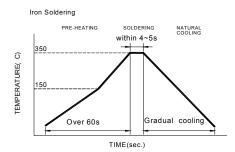
 $\bullet$  Preheat circuit and products to 150  $\!\!\!\!\!\!^{\circ}$ 

• 350 $^{\circ}$ C tip temperature (max)

- Never contact the ceramic with the iron tip1.0mm tip diameter (max)
- · Use a 20 watt soldering iron with tip diameter of 1.0mm
- Limit soldering time to 4~5sec.



Reflow times: 3 times max Fig.1

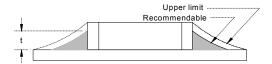


Iron Soldering times: 1 times max

#### 6-2.3 Solder Volume:

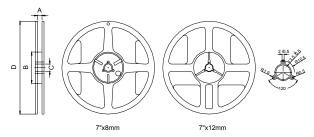
Accordingly increasing the solder volume, the mechanical stress to product is also increased. Exceeding solder volume may cause the failure of mechanical or electrical performance. Solder shall be used not to be exceed as shown in right side:

Minimum fillet height = soldering thickness + 25% product height



## 7. Packaging Information

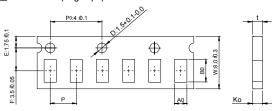
#### 7-1. Reel Dimension



Туре	Type A(mm)		C(mm)	D(mm)	
7"x8mm	9.0±0.5	<mark>60±2</mark>	<mark>13.5±0.5</mark>	<mark>178±2</mark>	
7"x12mm	13.5±0.5	60±2	13.5±0.5	178±2	

#### 7-2.1 Tape Dimension / 8mm

#### ■Material of taping is paper



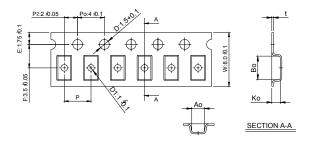
E:1.75.90.1	P22.0.1 P04.0.1 31.800.1.056	- t
F:3.5.00.1	P AQ	Ко

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
060303	0.70±0.06	0.40±0.06	0.45max	2.0±0.05	0.45max
100505	1.12±0.03	0.62±0.03	0.60±0.03	2.0±0.05	0.60±0.03

Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)
160808	1.80±0.05	0.96+0.05/-0.03	0.95±0.05	4.0±0.10	0.95±0.05
201209	2.10±0.05	1.30±0.05	0.95±0.05	4.0±0.10	0.95±0.05

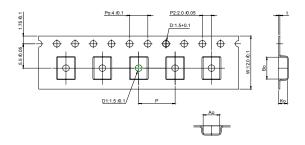
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#### ■Material of taping is plastic



Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
201212	2.10±0.10	1.28±0.10	1.28±0.10	4.0±0.10	0.22±0.05	1.0±0.10
<mark>321611</mark>	<mark>3.35±0.10</mark>	1.75±0.10	1.25±0.10	4.0±0.10	0.23±0.05	<mark>1.0±0.10</mark>
322513	3.42±0.10	2.77±0.10	1.55±0.10	4.0±0.10	0.22±0.05	1.0±0.10
321609	3.40±0.10	1.77±0.10	1.04±0.10	4.0±0.10	0.22±0.05	1.0±0.10

#### 7-2.2 Tape Dimension / 12mm

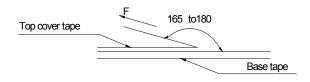


Ī	Size	Bo(mm)	Ao(mm)	Ko(mm)	P(mm)	t(mm)	D1(mm)
	451616	4.70±0.10	1.75±0.10	1.75±0.10	4.0±0.10	0.24±0.05	1.5±0.10
	453215	4.70±0.10	3.45±0.10	1.60±0.10	8.0±0.10	0.24±0.05	1.5±0.10

#### 7-3. Packaging Quantity

Chip Size	453215	451616	322513	<mark>321611</mark>	321609	201212	201209	160808	100505	060303
Chip / Reel	1000	2000	2500	<mark>3000</mark>	3000	2000	4000	4000	10000	15000
Inner box	4000	8000	12500	<mark>15000</mark>	15000	10000	20000	20000	50000	75000
Middle box	20000	40000	62500	<mark>75000</mark>	75000	50000	100000	100000	250000	375000
Carton	40000	80000	125000	150000	150000	100000	200000	200000	500000	750000

#### 7-4. Tearing Off Force



The force for tearing off cover tape is 15 to 60 grams in the arrow direction under the following conditions.

Room Temp. Room Humidity		Room atm	Tearing Speed	
(℃)	(%)	(hPa)	mm/min	
5~35	45~85	860~1060	300	

## **Application Notice**

Storage Conditions(component level)

To maintain the solder ability of terminal electrodes:

- 1. TAI-TECH products meet IPC/JEDEC J-STD-020D standard-MSL, level 1.
- 2. Temperature and humidity conditions: Less than 40  $^{\circ}$ C and 60% RH.
- 3. Recommended products should be used within 12 months from the time of delivery.
- 4. The packaging material should be kept where no chlorine or sulfur exists in the air.
- Transportation
- 1. Products should be handled with care to avoid damage or contamination from perspiration and skin oils.
- 2. The use of tweezers or vacuum pick up is strongly recommended for individual components.
- 3. Bulk handling should ensure that abrasion and mechanical shock are minimized.

## 单击下面可查看定价,库存,交付和生命周期等信息

>>TAI-TECH(台庆)