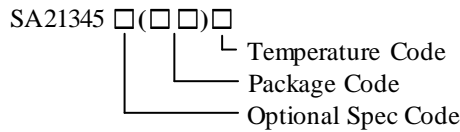


General Description

SA21345D is a 150mA high current capacity linear regulator. It fixed the output voltage at 5V, which features ultra-low ground current and low drop out voltage. The device with fully protection includes over current limit, output short protection and over temperature protection.

Ordering Information



Ordering Number	Package type	Note
SA21345DAAA	SOT23-5	

Features

- Wide Input Voltage Range: 4V to 36V
- Low Dropout Voltage (150mV @ 150mA)
- Ultra-low Quiescent Current
- Stability with Tantalum or Ceramic Capacitors
- Excellent Load and Line Regulation
- 150mA Maximum Load Current
- Over Current Protection
- Thermal Shutdown
- Compact SOT23-5 Package
- Automotive AEC- Q100 Grade 1 Certified

Applications

- Note Book
- Cell Phone
- Automotive LED Lighting ECU
- Automotive Body Modules

Typical Applications

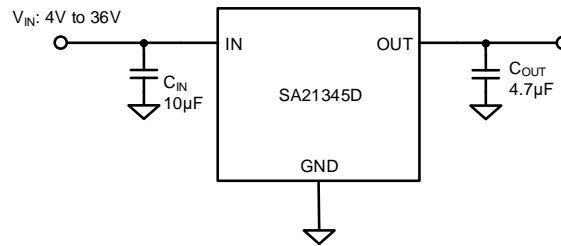
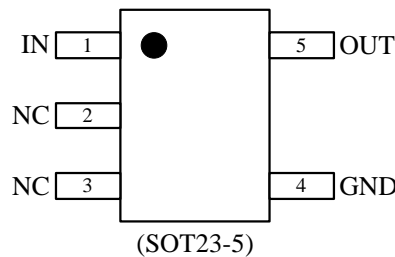


Figure 1 Schematic Diagram

Pinout (Top view)

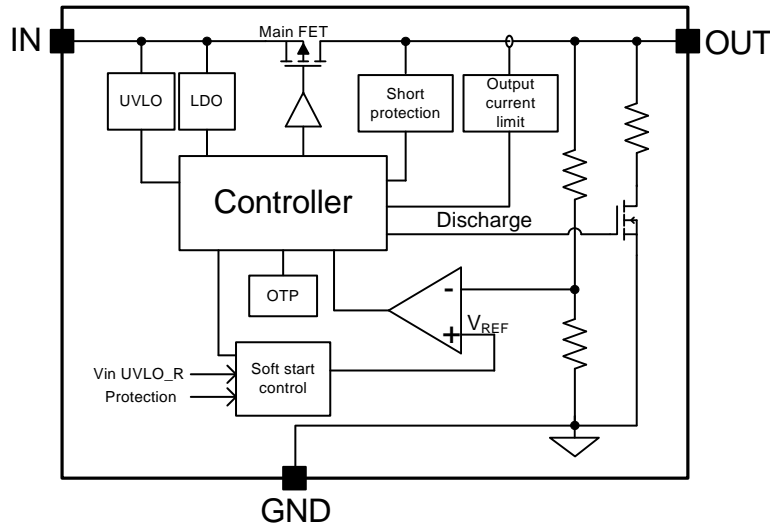


Top mark: **H3xyz** (Device code: H3, *x*=year code, *y*=week code, *z*=lot number code)

Pin Name	Pin number	Pin Description
IN	1	Input pin, decoupled with at least 1µF MLCC capacitor to GND.
GND	4	Ground pin
OUT	5	Output pin, decoupled with a 4.7µF MLCC capacitor to GND.
NC	2,3	No Connection



Block Diagram



Absolute Maximum Ratings (Note 1)

IN to GND	-----	-0.3V to 40V
OUT to GND	-----	-0.3V to 8V
Power Dissipation, Pd @ TA = 25°C	-----	0.47W
Package Thermal Resistance (Note 2)		
θJA	-----	210°C/W
θJC	-----	38.4°C/W
Junction Temperature	-----	-40°C to 150°C
Lead Temperature (Soldering, 10 sec.)	-----	260°C
Storage Temperature Range	-----	-65°C to 150°C
ESD Susceptibility		
HBM (Human Body Mode)	-----	2kV
CDM (Charged Device Mode)	-----	500V

Recommended Operating Conditions (Note 3)

Supply Input Voltage	-----	4V to 36V
OUT	-----	0V to 8V
Ambient Temperature Range	-----	-40°C to 125°C



Electrical Characteristics

$V_{IN} = 12V$, $T_J = -40^{\circ}C \sim 125^{\circ}C$, unless otherwise specified, the values are guaranteed by test design or statistical correlation

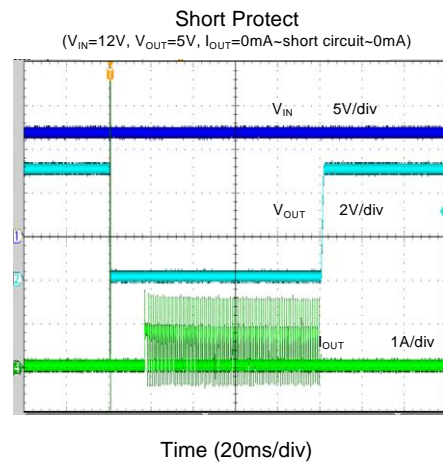
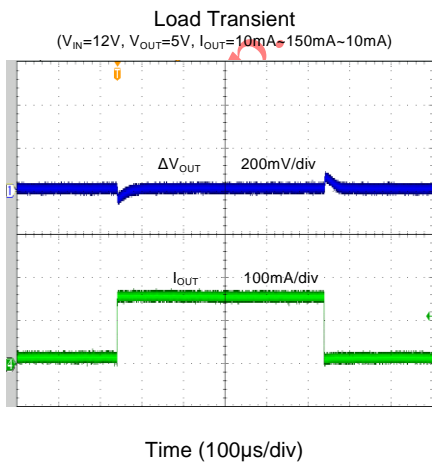
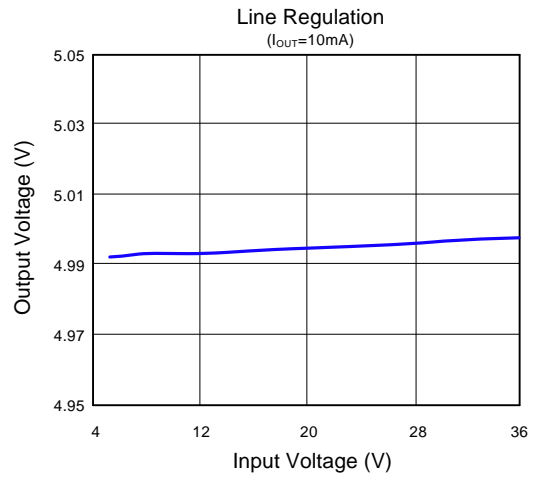
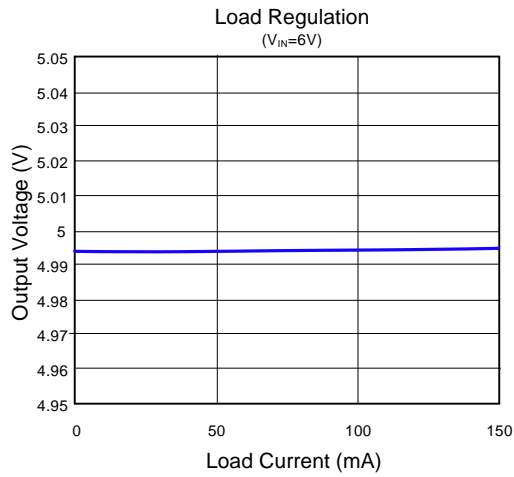
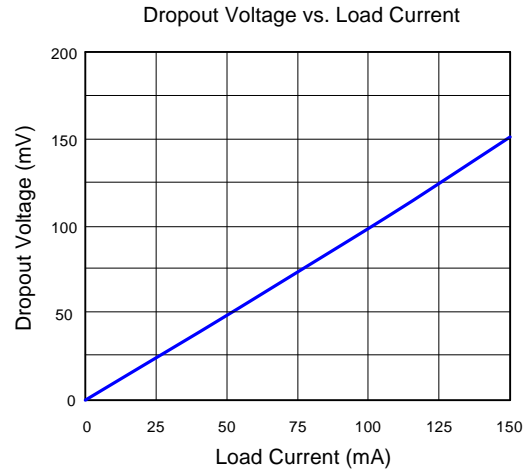
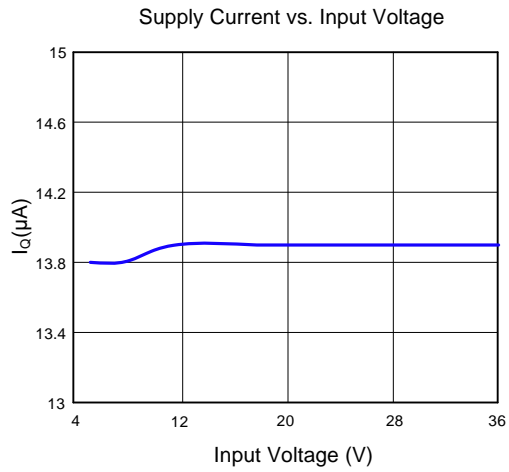
Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Input Voltage	V_{IN}		4		36	V
Input Voltage UVLO Threshold	V_{ULVO}	V_{IN} rising	2.9	3.3	4	V
UVLO Hysteresis	V_{UVLO_HYS}			200		mV
Output Voltage	V_{OUT}	$T_J = -40^{\circ}C \sim 125^{\circ}C$	4.9	5	5.1	V
		$T_J = 25^{\circ}C$	4.95	5	5.05	V
Line Regulation	ΔV_{LNR}	$I_{OUT} = 10mA$, $5.5V \leq V_{IN} \leq 36V$		1	1.5	mV/V
Load Regulation	ΔV_{LDR}	$V_{IN} = 6V$, $10mA \leq I_{OUT} \leq 0.15V$		0.25	0.5	%
Dropout Voltage	ΔV_{DROP}	$I_{OUT} = 10mA$		10	20	mV
		$I_{OUT} = 150mA$		150	300	mV
Quiescent Current	I_Q	$I_{OUT} = 0mA$ $V_{IN} = (V_{OUT} + 1V) \sim 36V$		15	22	μA
Current Limit	I_{LMT}	Force $V_{OUT} = 4.5V$	600			mA
Output Short Protection Threshold	V_{SHORT}	Force V_{OUT} from 5V to 0V	0.4	0.8	1.5	V
Output Short Off Time	t_{SHORT_OFF}			16		ms
Power Supply Rejection Ratio	PSRR	Frequency = 100Hz, $C_{OUT} = 4.7\mu F$, $I_{OUT} = 10mA$ $T_A = 25^{\circ}C$		60		dB
		Frequency = 100kHz, $C_{OUT} = 4.7\mu F$, $I_{OUT} = 10mA$ $T_A = 25^{\circ}C$		35		dB
Output Discharge Resistor	R_{DIS}	$V_{IN} = 3V$, $V_{OUT} = 2V$		500		Ω
Soft-start Time	t_{SS}			1		ms
Thermal Shutdown Temperature	T_{SD}			150		$^{\circ}C$
Thermal Shutdown Hysteresis	T_{HYS}			20		$^{\circ}C$

Note 1: Stresses beyond the “Absolute Maximum Ratings” may cause permanent damage to the device. These are stress ratings only. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

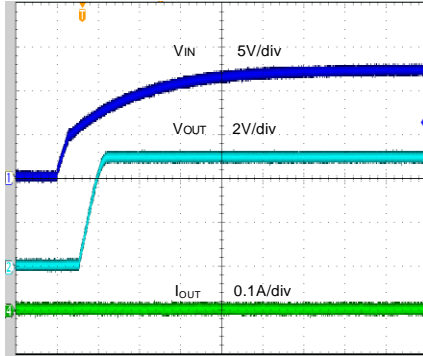
Note 2: θ_{JA} is measured in the natural convection at $T_A = 25^{\circ}C$ on a low effective single layer thermal conductivity test board of JEDEC 51-3 thermal measurement standard.

Note 3: The device is not guaranteed to function outside its operating conditions.

Typical Performance Characteristics

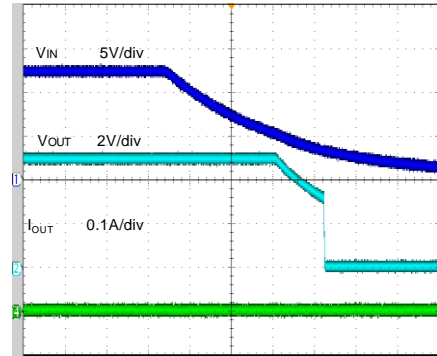


Startup from V_{IN}
($V_{IN}=12V$, $V_{OUT}=5V$, $I_{OUT}=0mA$)



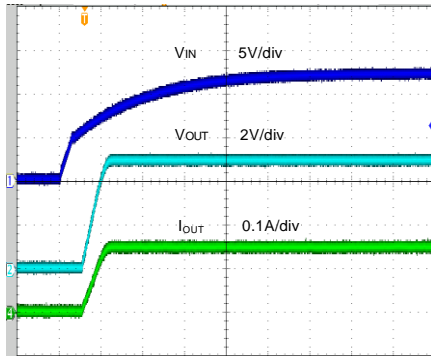
Time (2ms/div)

Shutdown from V_{IN}
($V_{IN}=12V$, $V_{OUT}=5V$, $I_{OUT}=0mA$)



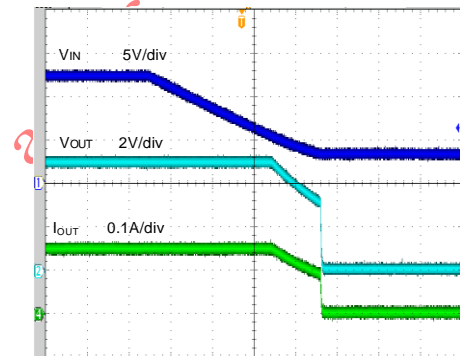
Time (400ms/div)

Startup from V_{IN}
($V_{IN}=12V$, $V_{OUT}=5V$, $I_{OUT}=150mA$)



Time (2ms/div)

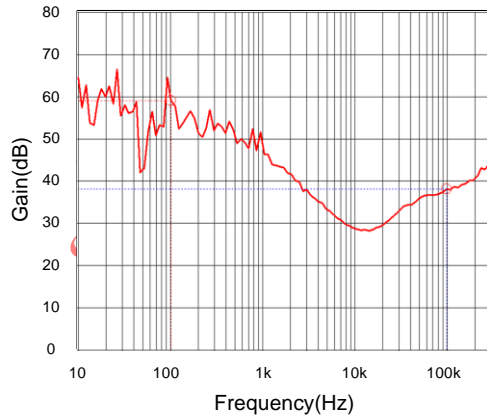
Shutdown from V_{IN}
($V_{IN}=12V$, $V_{OUT}=5V$, $I_{OUT}=150mA$)



Time (10ms/div)

PSRR

($V_{IN}=8V$, $V_{OUT}=5V$, $C_{OUT}=4.7\mu F$, 10mA Load)



Operation

SA21345D is a 150mA high current capacity linear regulator. It fixed the output voltage at 5V, which features ultra-low ground current and low drop out voltage. The device with fully protection includes over current limit, output short protection and over temperature protection.

Applications Information

Over Temperature Protection (OTP)

SA21345D includes over-temperature protection (OTP) circuitry to prevent overheating due to excessive power dissipation. This will turn off the device when the junction temperature exceeds 150°C. Once the junction temperature cools down by approximately 20°C the IC will resume normal operation

Output Short Circuit Protect

If V_{OUT} drop below than 0.8V, the short circuit protection mode will be initiated, and the device will be shut down for approximately 16ms. The device will then restart with a complete soft-start cycle. If the short circuit condition remains another ‘hiccup’ cycle of shutdown and restart will continue indefinitely unless the OTP threshold is reached.

Input Capacitor C_{IN}

To minimize the potential noise problem and improve power-supply rejection(PSRR) and transient response, place a typical X5R or better grade ceramic capacitor really close to the IN and GND pins. Care should be taken to minimize the loop area formed by C_{IN} , and IN/GND pins. In this case, a 4.7uF low ESR ceramic capacitor is recommended.

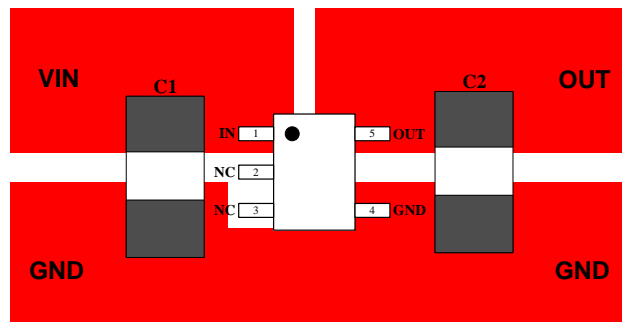
Output Capacitor C_{OUT}

For stable operation over the full temperature range, a 4.7μF low-ESR ceramic capacitor is recommended. Use larger output-capacitor values such as 22μF to reduce noise, improve load-transient response and PSRR. Some ceramic dielectrics exhibit large capacitance and ESR variations with temperature.

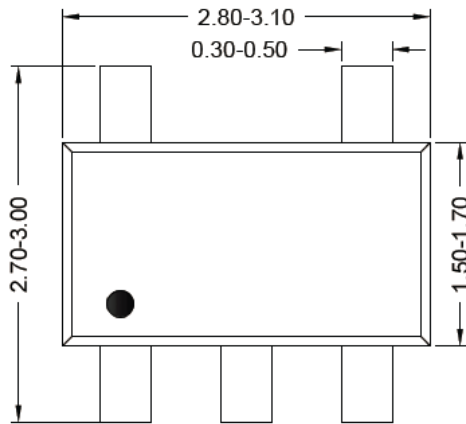
PCB Layout Guide

For best performance of the SA21345D, the following guidelines must be strictly followed:

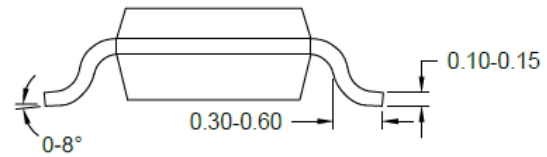
1. Keep all power trace as short and wide as possible. And it is desirable to use 2-layer or 4-layer board for thermal performance and better capability of current flow.
2. Place input/output capacitor close to the IC for better transient performance.



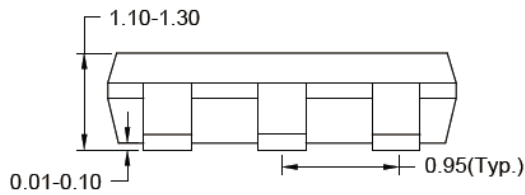
SOT23-5 Package Outline & PCB layout



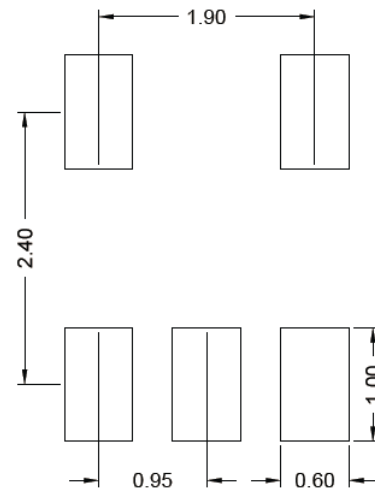
Top view



Side view



Front view



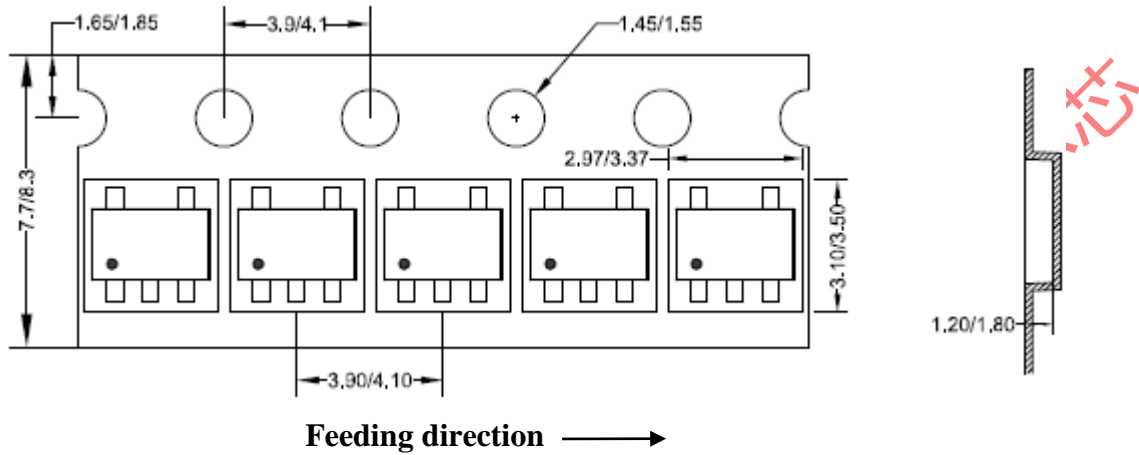
Recommended Pad Layout

Notes: All dimension in millimeter and exclude mold flash & metal burr.

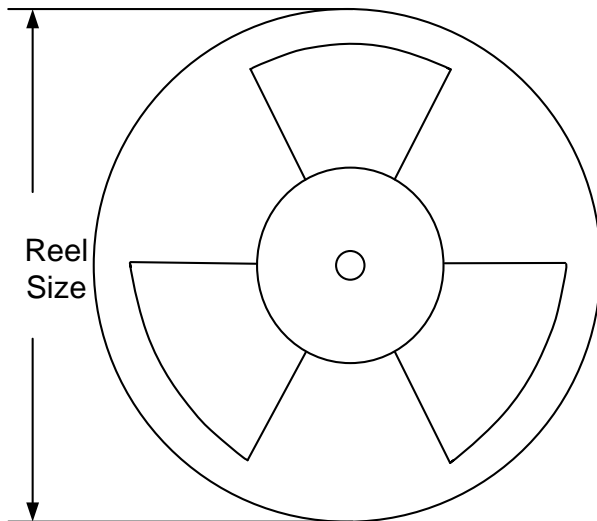
Taping & Reel Specification

1. Taping orientation

SOT23-5



2. Carrier Tape & Reel specification for packages



Package type	Tape width (mm)	Pocket pitch(mm)	Reel size (Inch)	Trailer length(mm)	Leader length (mm)	Qty per reel
SOT23-5	8	4	7"	280	160	3000

3. Others: NA

单击下面可查看定价，库存，交付和生命周期等信息

[>>SILERGY\(矽力杰\)](#)